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|  | document is being filed together with a new application.                |
| A. Patent Application No.(s) 11/003,084                          | B. Patent No.(s)  |
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| 5. Name and address to whom correspondence                       | 6. Total number of applications and patents                             |
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## ASSIGNMENT

We, Toshlba America Electronic Components, Inc., a corporation organized under the laws of California, located at 19900 MacArthur Boulevard, Suite 400, Irvine, CA 92612, U.S.A., the assignee of the following U.S. Patent Application:

SERIAL NO.

DATE FILED

INVENTORS

11/003,084

12/03/2004

Hiroaki Murakami, Hiromi Noro, Osamu Takahashi

## TITLE OF INVENTION

Systems And Methods For Preventing Malfunction Of Content Addressable Memory Resulting From Concurrent Write And Lookup Operations

hereby assign the entirety of our right, title and interest in said U.S. Patent Application, including all divisions, reissues, continuations, substitutions and extensions thereof and all rights arising under or pursuant to any and all international agreements, treaties or laws relating to the protection of industrial property, including rights of priority, resulting from the filing of any said application to KABUSHIKI KAISHA TOSHIBA, a Japanese corporation, located at 1-1 Shibaura 1-chome, Minato-ku, Tokyo, Japan, their successors, assigns, or other legal representatives.

The undersigned is authorized to sign this document on behalf of the corporation.

TOSHIBA AMERICA ELECTRONIC COMPONENTS, INC.

Name:

RECORDED: 07/14/2006

Takeaki Fukuyama

Title:

By:

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