Form PTO-1595 (Rev. 07/05) OMB No. 0651-0027 (exp. 6/30/2008)	U.S. DEPARTMENT OF COMMERCE United States Patent and Trademark Office	
RECORDATION FORM COVER SHEET		
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Name of conveying parties Tomomitsu KITAMURA Ken SUYAMA Aleksander DEC	Name and address of receiving party(ies) Renesas Technology Corp. 4-1, Marunouchi 2-chome Chiyoda-ku, Tokyo, Japan	
Additional name(s) of conveying party(ies) attached Yes No 3. Nature of conveyance/Execution Date(s): Execution Date(s) 1) March 28, 2006, 2, 3) June 7, 2006	2) Epoch Microelectronics, Inc. 7 Skyline Drive, Suite 265 Hawthorne, New York 10532	
Assignment Merger	Additional Name(s) & Addresses attached? Yes No	
Security Agreement Change of Name		
Joint Research Agreement		
Government Interest Assignment		
4. Application or patent number(s):	4. Application or patent number(s): This document is being filed together with a new application.	
A. Patent Application No.(s)	B. Patent No.(s)	
11/412,973		
Additional numbers a	attached? Yes No	
5. Name and address to whom correspondence concerning document should be mailed:	6. Total number of applications and patents involved: one	
Name: Daniel J. Stanger	7. Total fee (37 CFR 1.21(h) & 3.41) \$ 40.00	
Firm: Mattingly, Stanger, Malur & Brundidge, P.C.	Authorized to be charged by credit card	
Street Address: 1800 Diagonal Road, Suite 370	Authorized to be charged to deposit account	
	Enclosed	
City: Alexandria	None required (government interest not affecting title)	
State: Virginia Zip: 22314	8. Payment Information	
Phone Number:	a. Credit Card Last 4 Numbers <u>1009</u> Expiration Date <u>08/2009</u>	
Fax Number: 703 684-1157		
Email Address:	b. Deposit Account Number <u>50-1417</u>	
	Authorized User Name	
9. Signature: Signature	July 21, 2006 Date	
Daniel J. Stanger, Reg. No. 32,846	Total number of pages including cover	
Name of Person Signing	Sheet, attachments, and documents: 3	

Documents to be recorded (including cover sheet) should be faxed to (571) 273-0140, or mailed to:

Mail Stop Assignment Recordation Services, Director of the USPTO, P.O. Box 1450, Alexandria, VA 22313-1450

PATENT REEL: 017979 FRAME: 0388

ASSIGNMENT

(譲渡証)

As a below named inventor, I hereby declare that:

IN CONSIDERATION of the sum of One Dollar (\$1.00) or the equivalent thereof, and other good and valuable consideration paid to mellitizens of Japan and/or a citizen of U.S.A. by Renesas Technology Corp., a corporation organized under the laws of Japan and Epoch Microelectronics, Inc., a corporation organized under the laws of the State of New York, U.S.A., located at 4-1, Marunouchi 2-chome, Chiyoda-ku, Tokyo, Japan, and 7 Skyline Drive, Suite 265, Hawthorne, NY 10532, U.S.A., respectively, receipt of which is hereby acknowledged I do hereby sell and assign to said Renesas Technology Corp., and Epoch Microelectronics, Inc., their successors and assigns, all my right, title and interest, in and for the United States of America, in and to

EMBEDDED STRUCTURE CIRCUIT FOR VCO AND REGULATOR

invented by me (if only one is named below) or us (if plural inventors are named below) and described in the application for United States Letters Patent therefor, executed on even date herewith, and all United States Letters Patent which may be granted therefor, and all divisions, continuations and extensions thereof, the said interest being the entire ownership of the said Letters Patent when granted, to be held and enjoyed by said Renesas Technology Corp., and Epoch Microelectronics, Inc., their successors, assigns or other legal representatives, to the full end of term for which said Letters Patent may be granted, as fully and entirely as the same would have been held and enjoyed by me or us if this assignment and sale had not been made;

And I hereby agree to sign and execute any further documents or instruments which may be necessary, lawful, and proper in the prosecution of the above-named application or in the preparation and prosecution of any continuing, continuation-in-part, substitute, divisional, renewal, reviewed or reissue applications or in any amendment, extension, or interference proceedings, or otherwise to secure the title thereto in said assignee;

And I do hereby authorize and request the Commissioner of Patents to issue said Letters Patent to said Renesas Technology Corp., and Epoch Microelectronics, Inc.

Signed on the date(s) indicated aside signatures:

RECORDED: 07/21/2006

	INVENTOR(S) (発明者フルネームサイン)	Date Signed (客名日)
1)	Tomomitsu KITAMURA	28/March 12006
2)	Ven suyam	June 7, 2006
3)	A, De	June 7, 2006
4)	Aleksander DEC	
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6)		
7)		
8)		
9)		
10)		
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PATENT REEL: 017979 FRAME: 0389