



SHEET

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To the Director of the U.S. Patent and Trademark Office: Please record the attached documents or the new address(es) below.

6-28-06

1. Name of conveying party(ies)

Vijay Kumar Srinivasa Raghavan

Additional name(s) of conveying party(ies) attached? Yes No

2. Name and address of receiving party(ies)

Name: Cypress Semiconductor Corporation

Internal Address: Building 2.2

Street Address: 198 Champion Court

City: San Jose

State: CA

Country: United States Zip: 95134

Additional name(s) of receiving party(ies) attached? Yes No

11327 U.S. PTO
11/478006



062806

3. Nature of conveyance/Execution Date(s):

Execution Date(s) June 20, 2006

- Assignment Merger
- Security Agreement Change of Name
- Joint Research Agreement
- Government Interest Assignment
- Executive Order 9424, Confirmatory License
- Other _____

4. Application or patent number(s):

This document is being filed together with a new application.

A. Patent Application No.(s)

B. Patent No.(s)

Additional numbers attached? Yes No

5. Name and address to whom correspondence concerning document should be mailed:

Name: Cypress Semiconductor Corporation

Internal Address: Building 2.2

Street Address: 198 Champion Court

City: San Jose

State: CA Zip: 95134

Phone Number: 408-545-7358

Fax Number: 408-545-6911

Email Address: CMM@Cypress.com

6. Total number of applications and patents involved: 1 (one)

7. Total fee (37 CFR 1.21(h) & 3.41) \$ 40.00

- Authorized to be charged by credit card
- Authorized to be charged to deposit account
- Enclosed
- None required (government interest not affecting title)

8. Payment Information

a. Credit Card Last 4 Numbers 7851
Expiration Date 03/09

b. Deposit Account Number _____

Authorized User Name _____

9. Signature:

Michael T. Moore

Signature

June 28, 2006

Date

Michael T. Moore

Name of Person Signing

Total number of pages including cover sheet, attachments, and documents:

3

Cypress Semiconductor Corp.
Docket No. CD06069

ASSIGNMENT

PATENT

(For Execution Prior To Filing Patent Application)

In consideration of good and valuable consideration, the receipt of which is hereby acknowledged, We
the undersigned, Vijay Kumar Srinivasa Raghavan and Iulian Gradinariu

hereby sell, assign, and transfer to Cypress Semiconductor Corporation
a corporation of Delaware, having a principal place of business at
198 Champion Court, San Jose, California 95134-1709, ("Assignee"),

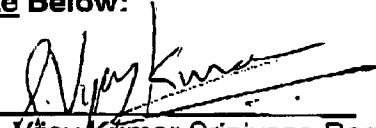
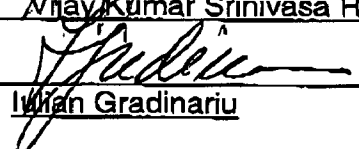
and its successors, assigns, and legal representatives, the entire right, title, and interest for the United States and all foreign countries, in and to any and all improvements that are disclosed in the application for the United States patent that has been executed by the undersigned prior hereto or concurrently herewith on the dates indicated below and is entitled Substrate Bias Feedback Scheme to Reduce Chip Leakage Power

and in and to said application and all divisional applications, continuation applications, continued prosecution applications, continuation-in-part applications, substitute applications, renewal applications, reissue applications, reexaminations, extensions, and all other patent applications that have been or shall be filed in the United States and all foreign countries on any of said improvements; and in and to all original patents, reissued patents, reexamination certificates, and extensions, that have been or shall be issued in the United States and all foreign countries on said improvements; and in and to all rights of priority resulting from the filing of said United States application;

agree that said Assignee may apply for and receive a patent or patents for said improvements in its own name; and that, when requested, without charge to, but at the expense of, said Assignee, its successors, assigns, and legal representatives, to carry out in good faith the intent and purpose of this Assignment, the undersigned will execute all divisional applications, continuation applications, continued prosecution applications, continuation-in-part applications, substitute applications, renewal applications, reissue applications, reexaminations, extensions and all other patent applications on any and all said improvements; execute all rightful oaths, assignments, powers of attorney, and other papers; communicate to said Assignee, its successors, assigns, and representatives all facts known to the undersigned relating to said improvements and the history thereof; and generally assist said Assignee, its successors, assigns, or representatives in securing and maintaining proper patent protection for said improvements and for vesting title to said improvements, and all applications for patents and all patents on said improvements, in said Assignee, its successors, assigns, and legal representatives; and

covenant with said Assignee, its successors, assigns, and legal representatives that no assignment, grant, mortgage, license, or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned.

Each Inventor: Please Sign and Date Below:

<u>June 20</u> , 20 <u>06</u> Date	 Name: <u>Vijay Kumar Srinivasa Raghavan</u>
<u>6/20</u> , 20 <u>06</u> Date	 Name: <u>Iwan Gradinariu</u>

Assignment Document Return Address:
Cypress Semiconductor Corp.
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198 Champion Court, San Jose, CA 95134
Tel: (408) 545-6962
Fax: (408) 545-6911