

08-24-2006

U.S. DEPARTMENT OF COMMERCE
United States Patent and Trademark Office



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To the Director of the U.S. Patent and Trademark Office, _____ documents or the new address(es) below.

1. Name of conveying party(ies)
Marvell Semiconductor, Inc.
Additional name(s) of conveying party(ies) attached? Yes No

2. Name and address of receiving party(ies)
Name: Marvell International Ltd.
Internal Address: _____

3. Nature of conveyance/Execution Date(s):
Execution Date(s) 08-15-2006
 Assignment MP0924 Merger
 Security Agreement Change of Name
 Joint Research Agreement
 Government Interest Assignment
 Executive Order 9424, Confirmatory License
 Other _____

Street Address: Argyle House
41A Cedar Avenue
City: Hamilton
State: _____
Country: BERMUDA Zip: HM12
Additional name(s) & address(es) attached? Yes No

4. Application or patent number(s): This document is being filed together with a new application.
A. Patent Application No.(s)
60/765,968
Additional numbers attached? Yes No

B. Patent No.(s)

5. Name and address to whom correspondence concerning document should be mailed:
Name: Andrew D. Fortney, Ph.D.
Internal Address: The Law Offices of Andrew D. Fortney, Ph.D., P.C.
Street Address: 401 W. Fallbrook Avenue., Suite 204
City: Fresno
State: CA Zip: 93711
Phone Number: 559-432-6847
Fax Number: 559-432-6872
Email Address: info@fortneylaw.com

6. Total number of applications and patents involved: 2
7. Total fee (37 CFR 1.21(h) & 3.41) \$ 80.
 Authorized to be charged by credit card
 Authorized to be charged to deposit account
 Enclosed ***** Additional Fees Only *****
 None required (government interest not affecting title)

8. Payment Information
a. Credit Card Last 4 Numbers 1007
Expiration Date 04/10
b. Deposit Account Number 50-1236
Authorized User Name Marvell Semiconductor, Inc.

9. Signature: Andrew D. Fortney 08-16-2006
Signature Date
Andrew D. Fortney, Ph.D.
Name of Person Signing
Total number of pages including cover sheet, attachments, and documents: **3**

Documents to be recorded (including cover sheet) should be faxed to (571) 273-0140, or mailed to:
Mail Stop Assignment Recordation Services, Director of the USPTO, P.O.Box 1450, Alexandria, V.A. 22313-1450

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02 FC:8021

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ASSIGNMENT

Corporate

Marvell Semiconductor, Inc., a California corporation, having a principal place of business at 5488 Marvell Lane, Santa Clara, CA 95054 (hereafter the "Assignor"), is the owner by respective Assignment of United States Patent Application(s) identified below (hereafter sometimes called the "Patent Application(s)").

Marvell International Ltd., a corporation of Bermuda, having a place of business at Argyle House, 41A Cedar Avenue, Hamilton, Hm12, Bermuda (hereafter the "Assignee"), desires to acquire all of the rights to the Patent Application(s) and all inventions described and claimed therein or entitled to the benefit thereof.

THEREFORE, in consideration of the sum of One Dollar (\$1.00) or equivalent and other good and valuable consideration paid to it, Assignor hereby sells and assigns to the Assignee the entire right, title and interest in and to the Patent Application(s) identified below, and all inventions described and claimed therein or entitled to the benefit thereof, in any and all Letters Patent therefor, and in any and all reissues, extensions, renewals, reexaminations, divisions and continuations of such applications or Letters Patent to the full end of the term or terms for which such Letters Patent issue, such entire right, title and interest to be held and enjoyed by the above-named Assignee the same as they would have been held and enjoyed by the Assignor had this Assignment and sale not been made.

<u>Marvell Semiconductor, Inc. Case No.</u>	<u>U. S. Serial No.</u>	<u>U. S. Filing Date</u>	<u>Title</u>
MP0924	To Be Assigned	Herewith	Integrated Circuit Devices and Systems with ESD and I/O Protection, and Methods for Fabricating Same
MP0924PR	60/765,968	02-07-2006	New Approach for ESD Protection

By its undersigned representative, the Assignor agrees

a. to execute all papers necessary in connection with the Patent Application(s) and any continuing, divisional, reissue, reexamination or corresponding application thereof and also to execute separate Assignment in connection with such application as the Assignee may deem necessary or expedient;

b. to execute all papers necessary in connection with any interference which may be declared concerning the Patent Application(s) or any continuation, division, reissue or reexamination thereof and to cooperate with the Assignee in every way possible in obtaining evidence and going forward with such interference; and

c. to perform all affirmative acts which may be necessary to obtain a grant of a valid United States patent to the Assignee on any of the Patent Application(s) and on any continuation, division, reissue or reexamination of any of the Patent Application.

The Assignor hereby covenants that it has the full right to convey the entire interest herein assigned, and that it has not executed, and will not execute, any agreement in conflict therewith.

Marvell Semiconductor, Inc., a California corporation, certifies that it is the Assignee of the entire right, title and interest in the Patent Application(s) identified above by virtue of Assignment from the inventors of the Patent Application(s) identified above. Assignment to be recorded in the Patent and Trademark Office as identified below.

<u>Marvell Semiconductor, Inc.</u> <u>Case No.</u>	<u>U. S. Serial No.</u>	<u>Assignment Execution Date</u>
MP0924	To Be Assigned	<u>August 15, 2006</u>

The undersigned has reviewed the documents in the Patent Application(s) identified above and, to the best of undersigned's knowledge and belief, title is in the Assignor identified above.

The undersigned is empowered to sign this certificate on behalf of the Assignor.

I hereby declare that all statements made herein of my own knowledge are true, and that all statements made on information and belief are believed to be true; and further, that these statements are made with the knowledge that willful false statements, and the like so made, are punishable by fine or imprisonment, or both, under Section 1001, Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

IN WITNESS WHEREOF, executed by the Assignor's undersigned representative on the date following the undersigned's name.

Marvell Semiconductor, Inc.

By: _____

Eric Janofsky

Title: Vice President Intellectual Property and General Patent Counsel

Date August 15, 2006