

09-28-2006

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To the Director of the U.S. Patent and Trademark Office: Please record the attached documents or the new address(es) below.

9-26-06

1. Name of conveying party(ies)/Execution Date(s):

Vima Microsystems Corporation

Execution Date(s) September 22, 2006

Additional name(s) of conveying party(ies) attached?  Yes  No

2. Name and address of receiving party(ies)

Name: Darien K. Wallace

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Additional name(s) & address(es) attached?  Yes  No

3. Nature of conveyance:

- Assignment  Merger
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4. Application or patent number(s):

This document is being filed together with a new application.

A. Patent Application No.(s) 10/722,323

B. Patent No.(s)

Patent Application Entitled: "Segment Buffer Loading in a Deinterlacer"

Additional numbers attached?  Yes  No

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5. Name and address to whom correspondence concerning document should be mailed:

Name: Silicon Edge Law Group LLP

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6. Total number of applications and patents involved:

1

7. Total fee (37 CFR 1.21(h) & 3.41) \$ 40.00

- Authorized to be charged by credit card
- Authorized to be charged to deposit account
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9. Signature:

Darien K. Wallace

Signature

September 22, 2006

Date

Darien K. Wallace

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Total number of pages including cover sheet, attachments, and documents:

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