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To the Honorable Commissioner of Patents and Trademarks: Please record the attached original documents or copy thereof.

FINANCE SECTION

1. Name of conveying party(ies):

Dinesh MAHESHWARI

Additional name(s) of conveying party(ies) attached? Yes No

3. Nature of conveyance:

- Assignment Merger
- Security Agreement Change of Name
- Other _____

Execution Date: 10/06/2006

2. Name and address of receiving party(ies)

Name: Netlogic Microsystems, Inc.

Internal Address: _____

Street Address: 1875 Charleston Road

City: Mountain View State: CA Zip: 94043

Additional name(s) & address(es) attached? Yes No

4. Application number(s) or patent number(s):

If this document is being filed together with a new application, the execution date of the application is: _____

A. Patent Application No.(s) 11/478,234

B. Patent No.(s) _____

Additional numbers attached? Yes No

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: Bradley T. Sako

Internal Address: _____

Street Address: 300 South First Street

Suite 235

City: San Jose State: CA Zip: 95113

6. Total number of applications and patents involved:

7. Total fee (37 CFR 3.41).....\$ 40.00

- Enclosed
- Authorized to be charged to deposit account

8. Deposit account number:

50-2087

DO NOT USE THIS SPACE

9. Signature.

Bradley T. Sako
Name of Person Signing

Bradley T. Sako
Signature

10/10/2006
Date

Total number of pages including cover sheet, attachments, and documents: 3

10/17/2006 09:09:04 00000044 11/17/02/34

Mall documents to be recorded with required cover sheet information to:
Commissioner of Patents & Trademarks, Box Assignments
Washington, D.C. 20231

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PATENT
REEL: 018414 FRAME: 0284

ASSIGNMENT (Page 1 of 2)

In consideration of good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, I/we, **Dinesh Maheshwari**, the undersigned Assignor(s), believing to be the original, first and sole or joint inventor(s) of any and all new and useful improvements disclosed in the application for the United States patent entitled

BLOCK MAPPING CIRCUIT AND METHOD FOR MEMORY DEVICE

hereby sell, assign, and transfer to **NetLogic Microsystems, Inc.** a Delaware Corporation, having a principal place of business at 1875 Charleston Road, Mountain View, CA 94043, ("Assignee"), and its successors, assigns, and legal representatives, the entire right, title, and interest for the United States and all foreign countries, in and to any and all improvements that are disclosed in the same patent application

 X which was filed with the United States Patent and Trademark Office on 06/29/2006, and which was assigned Application Number 11/478,234.

 which has been executed by the undersigned prior hereto or concurrently herewith on the dates indicated below.

I/We hereby further sell, assign, and transfer to Assignee said application and all divisional applications, continuation applications, continued prosecution applications, continuation-in-part applications, substitute applications, renewal applications, reissue applications, reexaminations, extensions, and all other patent applications that have been or shall be filed in the United States and all foreign countries on any of said improvements; and in and to all original patents, reissued patents, reexamination certificates, and extensions, that have been or shall be issued in the United States and all foreign countries on said improvements; and in and to all rights of priority resulting from the filing of said United States application; and the right to file foreign applications directly in the name of the Assignee and to claim for any such foreign applications any priority rights to which such applications are entitled under international conventions, treaties or otherwise.

I/We agree that said Assignee may apply for and receive a patent or patents for said improvements in its own name; and that, when requested, without charge to, but at the expense of, said Assignee, its successors, assigns, and legal representatives, to carry out in good faith the intent and purpose of this Assignment, the undersigned will execute all divisional applications, continuation applications, continued prosecution applications, continuation-in-part applications, substitute applications, renewal applications, reissue applications, reexaminations, extensions and all other patent applications on any and all said improvements; execute all rightful oaths, assignments, powers of attorney, and other papers; communicate to said Assignee, its successors, assigns, and representatives all facts known to the undersigned relating to said improvements and the history thereof; and generally assist said Assignee, its successors, assigns, or representatives in securing and maintaining proper patent protection for said improvements and for vesting title to said improvements, and all applications for patents and all patents on said improvements, in said Assignee, its successors, assigns, and legal representatives.

ASSIGNMENT (Page 2 of 2)

For: **BLOCK MAPPING CIRCUIT AND METHOD FOR MEMORY DEVICE**

To: **Netlogic Microsystems, Inc.**


I/We hereby covenant with said Assignee, its successors, assigns, and legal representatives that no assignment, grant, mortgage, license, or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned.

I/We hereby authorize and request the Officials of the United States Patent and Trademark Office and any and all foreign Patent Offices to issue any and all Letters Patents, when granted, to said Assignee as the assignee of the entire right, title and interest in and to the same, for the sole use of said Assignee, its successors and assigns, to the full end of the term for which said Letters Patents may be granted.

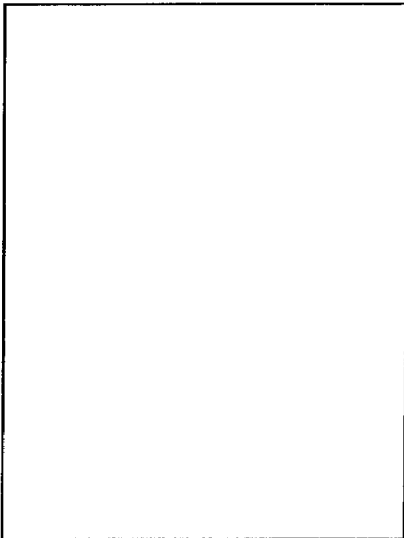
I/We hereby grant to WALKER & SAKO, LLP located at 300 South First Street, Suite 235, San Jose, CA 95113, the power to insert on this document any further identification necessary or desirable to comply with the rules of the United States Patent and Trademark Office for recordation of this Assignment.

Each Inventor/Assignor: Please Sign and Date Below:

Oct 06, 2006
Date


Dinesh Maheshwari
Name:

Signature



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): MAHESHWARI, Dinesh

Serial No.: 11/478,234

Filed: 06/29/2006

Title: Block Mapping Circuit and Method for Memory Device

Attorney Docket No.: CD04127

Group Art Unit: 2186

Examiner: N/A

TRANSMITTAL COVER SHEET (37 C.F.R. §1.8(a)(i)(A))

First Class Mail

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313

The following is included:

- Fee Transmittal Form
Fee Attached
Amendment/Response
After Final Rejection
Affidavits/Declaration(s)
Extension of Time Request
Information Disclosure Statement
Status Request Letter
Other
Certified Copy of Priority Documents
Response to Missing Parts/ Incomplete Applications
Response to Missing Parts Under 37 C.F.R. §1.52 or §1.53
Assignment Papers
Power of Attorney/Revocation/Change of Correspondence Address

Conditional Petition for Extension of Time/Authorization to Charge Fees

It is believed that the indicated items are timely filed. However, in the event an extension of time is required, please consider this a petition for extension of time under 37 C.F.R. §1.136(a). Please charge any fees that may be required to Deposit Account No. 50-2087.

Respectfully Submitted,

Bradley T. Sako, Reg. No. 37,923

October 10, 2006
Date

37 C.F.R. §1.8

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313.

Date of Deposit: OCTOBER 10, 2006

Typed/Printed Name: BRADLEY T. SAKO

Signature: [Handwritten Signature]