[] Security Agreement

[] Other

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e attached	original	documents	or	сору	thereof

. Name and	address of receiving party(ies):	7. 1.
Name:	Xilinx, Inc.	U.S. 11.
		α α

Internal Address:

[] Merger [] Change of Name

Street Address: 2100 Logic Drive Zip: 95124 City: San Jose State: California Additional name(s) & address(es) attached? [] Yes [**X**] No

Execution Date: September 29, 2006 4. Application number(s) or patent number(s): If this document is being filed together with a new application, the filing date of the application is:

October 13, 2006

A. Patent Application No.(s) New Patent Application Entitled:

"PARALLEL COEFFICIENT BIT MODELLING"

Additional numbers attached? [] Yes [X] No

B. Patent No.(s)

5. Name and address of party to whom correspondence concerning document should be mailed:

JUSTIN LIU, 51,959 Name: Internal Address: Xilinx. Inc

____ State: <u>California</u>

Street Address: 2100 Logic Drive

6. Total number of applications and patents involved: One

40.00 7. Total fee (37 CFR 3.41):....\$ [] Enclosed [X] Authorized to be charged to deposit account

8. Deposit Account Number: 24-0040 (Attach duplicate copy of this page if paying by deposit account)

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9. Statement and signature.

JAM

City: San Jose

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

Zip: 95124

JUSTIN LIU, 51,959

Name of Person Signing

Signature

October 13, 2006

Total number of pages including cover sheet, attachments, and document: [02]

Mail documents to be recorded with required cover sheet information to

Commissioner for Patents, Mail Stop: Assignments Alexandria, Virginia 22313-1450

0/20/2006

ASSIGNMENT

For good and valuable consideration	n, receipt of which is hereb	y acknowledged, I/we
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Tero Rissa of 171A Grey Turner Hse, Ducane Rd, London, W12 0UA

hereby sell, assign and transfer to Xilinx, Inc., a Delaware corporation, having a place of business at 2100 Logic Drive, San Jose, California 95124, its successors and assigns, the entire right, title and interest throughout the world in our invention

PARALLEL COEFFICIENT BIT MODELLING

for which I/we will execute a United States patent application on or about the date of this assignment, and all patent applications and patents of every country for said invention, including divisions, reissues, continuations and extensions thereof, and all rights of priority resulting from the filing of said applications; I/we authorize the above-named assignee to apply for patents of foreign countries for said invention, and to claim all rights of priority without further authorization from me/us; I/we agree to execute all papers useful in connection with said United States and foreign applications, and generally to do everything possible to aid said assignee, its successors, assigns and nominees, at its request and expense, in obtaining and enforcing patents for said invention in all countries; and I/we request the Commissioner of Patents and Trademarks to issue all patents granted for said invention to the abovenamed assignee, its successors and assigns.

Executed this **21** day of September 2006.

Tero Rissa

State of <u>United Kingdom</u>)
County of <u>London</u>)

CAPACITY CLAIMED BY SIGNER [X] oneself/themselves

On September 29th before me, MANISH KUMAR SONI
Notary Public, personally appeared Tero Rissa [] personally known to me --or -- N] proved to me on

Notary Public, personally appeared <u>Tero Rissa</u> [] personally known to me --or -- \[\] proved to me on the basis of satisfactory evidence to be the person whose name is subscribed to the within instrument and acknowledged to me that he/she executed the same in his/her authorized capacity, and that by his/her signature on the instrument the person, or the entity upon behalf of which the person acted, executed the instrument.

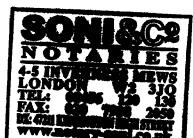
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