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RECORDATION FORM COVER SHEET

TI-38556

To the Commissioner for Patents. Please record the attached original documents or copy thereof.

<p>1. Name of conveying party(ies):</p> <p>1. Alexander Tessarolo, signed 11/14/2005</p> <p>2. David A. Figoli, signed 11/14/2005</p>	<p>2. Name and Address of receiving party(ies):</p> <p>Name: TEXAS INSTRUMENTS INCORPORATED Address: P.O. Box 655474, MS 3999 City: Dallas State: TX Zip: 75265 Additional name(s) & address(es) attached? ___ Yes <u>X</u> No</p>
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3. Nature of Conveyance:

X Assignment ___ Merger

___ Security Agreement ___ Change of Name

X Being re-filed with "correct" Application No. 11/290,959

Execution Date: 11/14/2005

4. Application number(s) or patent number(s).

___ This document is being filed together with a new application.

Execution date of the application: 11/14/2005

Title: APPARATUS AND METHOD FOR TEST, CHARACTERIZATION, AND CALIBRATION OF MICROPROCESSOR-BASED AND DIGITAL SIGNAL PROCESSOR-BASED INTEGRATED CIRCUIT DIGITAL DELAY LINES

Docket No: TI-38556

A. Patent Application No.(s)	B. Patent No.(s)
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Additional numbers attached? ___ Yes X No Additional numbers attached? ___ Yes X No


<p>5. Name and address of party to whom correspondence concerning document should be mailed:</p> <p>Name: William W. Holloway Texas Instruments Incorporated Address: P.O. Box 655474, MS 3999 City: Dallas State: TX Zip: 75265</p>	<p>6. Number of applications and patents involved: (1)</p> <hr/> <p>7. Amount of fee enclosed or authorized to be charged: \$40</p> <hr/> <p>8. Deposit Account No: 20-0668 (No duplicate copy is needed).</p>
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9. Statement and signature.

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

10/23/2006
Date


 William W. Holloway, Reg. No. 26,182

Total Number of Pages Including Cover Sheet, Attachments and Document: 3

Attorney Docket No.
TI-38556

ASSIGNMENT

WHEREAS, I, the undersigned inventor (or one of the undersigned joint inventors), of residence as listed, having invented certain new and useful improvements as below entitled, for which application for United States Letters Patent is made, the said application having been executed on the date set forth below; and


WHEREAS, TEXAS INSTRUMENTS INCORPORATED, a corporation organized and existing under the laws of the State of Delaware, with a place of business at 7839 Churchill Way, M/S 3999, Dallas, Texas 75251, is desirous of acquiring my entire right, title and interest in and to the said invention, and to the said application and any Letters Patent that may issue thereon;

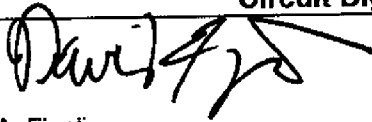
NOW, THEREFORE, for good and valuable consideration, the receipt of which is hereby acknowledged, I hereby sell and assign to the said TEXAS INSTRUMENTS INCORPORATED, its successors and assigns, my entire right, title and interest in and to the said invention and in to the said application and all patents which may be granted therefor, and all divisions, reissues, substitutions, continuations, and extensions thereof, and I hereby authorize and request the Commissioner of Patents and Trademarks to issue all patents for said invention, or patent resulting therefrom, insofar as my interest is concerned, to the said TEXAS INSTRUMENTS INCORPORATED, as assignee of my entire right, title and interest.

I also hereby sell and assign to TEXAS INSTRUMENTS INCORPORATED, its successors and assigns, my foreign rights to the invention disclosed in said application, in all countries of the world, including the right to file applications and obtain patents under the terms of the International Convention for the Protection of Industrial Property, and of the European Patent Convention, and further agree to execute any and all patent applications, assignments, affidavits, and any other papers in connection therewith necessary to perfect such patent rights.

I hereby further agree that I will communicate to said TEXAS INSTRUMENTS INCORPORATED, or to its successors, assigns, and legal representatives, any facts known to me respecting said invention, and at the expense of said assignee company, testify in any legal proceedings, sign all lawful papers, execute all divisional, continuation, reissue and substitute applications, make all lawful oaths, and generally do everything possible to aid said TEXAS INSTRUMENTS INCORPORATED, its successors, assigns and nominees to obtain and enforce proper patent protection for said invention in all countries.

IN WITNESS WHEREOF, I hereunto set hand and seal this day and year;

TITLE OF INVENTION	Apparatus and Method for Test, Characterization, and Calibration of Microprocessor-Based and Digital Signal Processor-Based Integrated Circuit Digital Delay Lines
SIGNATURE OF INVENTOR AND NAME	 Alexander Tessarolo
DATE	14 Nov 2005
RESIDENCE (CITY AND STATE)	29A Highfield Road Lindfield NSW 2070 AUSTRALIA
DATE APPLICATION EXECUTED	14 Nov 2005

TITLE OF INVENTION	Apparatus and Method for Test, Characterization, and Calibration of Microprocessor-Based and Digital Signal Processor-Based Integrated Circuit Digital Delay Lines
SIGNATURE OF INVENTOR AND NAME	 David A. Figoli
DATE	14 Nov 05
RESIDENCE (CITY AND STATE)	2115 Madewood Drive Missouri City, TX 77459 USA
DATE APPLICATION EXECUTED	14 Nov 05

After recording, return Assignment to:

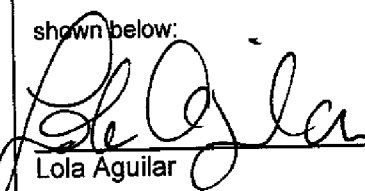
Texas Instruments Incorporated
PO Box 655474, M/S 3999
Dallas, TX 75265

To: **Technology Unit: 2817**
Facsimile Number: 571-273-0140

Total Pages Sent **4**

From: **William W. Holloway**
Texas Instruments Incorporated
Facsimile: 281-274-3657
Phone: 281-274-4064

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

<u>CERTIFICATION OF FACSIMILE TRANSMISSION</u>	
I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office at 571-273-0140 on the date shown below:	
	<u>10/23/2006</u>
Lola Aguilar	Date

FACSIMILE COVER SHEET

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<input type="checkbox"/> NEW APPLICATION	<input type="checkbox"/> EOT _____ (# Month)
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NAME OF INVENTOR(S): Tessarolo et al	
RECEIPT DATE & SERIAL NO.: Serial No.: 11/290,959	
TITLE OF INVENTION: APPARATUS AND METHOD FOR TEST, CHARACTERIZATION, AND CALIBRATION OF MICROPROCESSOR-BASED AND DIGITAL SIGNAL PROCESSOR-BASED INTEGRATED CIRCUIT DIGITAL DELAY LINES	
Filing Date:	
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FAXED: 10-23-2006	
DUE: 12-15-2005	
ATTY/SEC'Y: WWW/dsa	

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