



10-27-2006

FUJ-034

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PATENTS ONLY

U.S. DEPARTMENT OF COMMERCE  
Patent and Trademark Office

10/23/06

To the Honorable Commissioner of Patents and Trademarks: Please record the attached original documents or copy thereto:

1. Name of conveying party(ies):

1. Kouhei YAMADA

2. Name and address of receiving party(ies):

Name: FUJI ELECTRIC DEVICE TECHNOLOGY CO., LTD  
Street Address: 11-2 Osaki 1-chome, Shinagawa-ku,  
City: Tokyo  
State: Tokyo  
Zip: 141-0032  
Country: Japan

3. Nature of conveyance:

- Assignment  Merger
- Security Agreement  Change of Name
- Other

Execution Date: September 27, 2006

Additional name(s) & address(es) attached?  Yes  No

4. Application number(s) or patent number(s):

If the document is being filed together with a new application, the execution date of the application is: 09/27/2006

A. Patent Application No(s).

11/516,581 filed September 7, 2006

B. Patent No(s).

«PATENT» «ISSUED»

Additional numbers attached?  Yes  No

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: KANESAKA BERNER & PARTNERS  
PATENT AGENTS, LLP

Internal Address:

Street Address: 1700 Diagonal Road, Suite 310

City: Alexandria State: VA Zip: 22314

6. Total number of applications and patents involved: one

7. Total fee (37 CFR 3.41) \$ 40.00

- Enclosed
- Authorized to be charged to deposit account
- Credit Card payment form enclosed

8. Deposit account number:

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9. Signature.

Manabu kanesaka, No. 31,467

Name of Person, Registration No.

Signature

10/24/2006 JAD/08 08002186 11516581

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Total number of pages including cover sheet, attachment, and document:

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CMB No. 0851-0011 (exp. 4/94)

ASSIGNMENT

For value received, I, Kouhei YAMADA having post office address at c/o Fuji Electric Advanced Technology Co., Ltd.,  
1, Fuji-machi, Hino-shi, Tokyo 191-8502, Japan hereby sell, assign and transfer to FUJI ELECTRIC DEVICE TECHNOLOGY CO., LTD. a corporation existing under the laws of Japan located at 11-2 Osaki 1-chome, Shinagawa-ku, Tokyo, 141-0032, Japan and its successors, assigns, and legal representatives the entire right, title, and interest for all countries including the United States of America, in and to certain inventions relating to OVERCURRENT  
DETECTING CIRCUIT AND REFERENCE VOLTAGE GENERATING CIRCUIT described in application for Letters Patent of the United States, Serial No. 11/516,581 Filed September 7, 2006 and all patents which may be granted therefor, and all divisions, reissues, continuations and extensions thereof, and authorize and request the Commissioner of Patents and Trademarks to issue all patents on said improvements or resulting therefrom to said Company as assignee of the entire interest, and covenant that I/We have full right so to do, and agree that I/We will communicate to said Company or its representatives any facts known to me/us respecting said improvements and testify in any legal proceedings, sign all lawful papers, execute all divisional, continuing and reissue applications, make all rightful oaths and generally do everything possible to aid said Company, its successors, assigns, and nominees, to obtain and enforce proper protection for said invention in the United States.

Signature Kouhei Yamada Date September 27, 2006  
 Kouhei YAMADA