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prm <b>PTO-1595</b> (Rev. 08/05)	10-31-2006	U.S. DEPARTMEN
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To the Director of the U.S. Patent and Trademark Office: Please record the attached documents or the new address(es) below.				
1. Name of conveying party(ies)	2. Name and address of receiving party(ies)			
Marvell Semiconductor, Inc.	Name: Marvell International Ltd.			
	Internal Address: Argyle House			
Additional name(s) of conveying party(ies) attached?  Yes  No				
3. Nature of conveyance/Execution Date(s):	Street Address: 41A Cedar Avenue			
Execution Date(s) October 23, 2006				
☐ Security Agreement ☐ Change of Name	City: Hamilton			
☐ Joint Research Agreement	State:			
Government Interest Assignment	Country Bermuda 7in UM12			
☐ Executive Order 9424, Confirmatory License	Country: Bermuda Zip: HM12			
Other	Additional name(s) & address(es) attached? ☐ Yes ☒ No			
4. Application or patent number(s):				
A. Patent Application No.(s)	B. Patent No.(s)			
11/491,327	l žá			
60/734,581	77.27			
Additional numbers	I attached? ☐ Yes ☒ NO			
5. Name and address to whom correspondence concerning document should be mailed:	6. Total number of applications and patents involved: 2			
Name: Customer No. 45510	7. Total fee (37 CFR 1.21(h) & 3.41) \$80.00			
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Phone Number: <u>212-596-9000</u>	Expiration Date 04/10			
Fax Number: <u>212-596-9090</u>	b. Deposit Account Number 06-1075			
Email Address: chia-hao.la@ropesgray.com	Authorized User Name Fish & Neave			
9. Signature:	October 27, 2006			
Chia-Hao La	Date			
Reg. No. 57,729 Name of Person Signing	Total number of pages including cover sheet, attachments, and document: 4			
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Documents to be recorded (including cover sheet) should be faxed to (571) 273-0140, or mailed to:
Mail Stop Assignment Recordation Services, Director of the USPTO, P.O.Box 1450, Alexandria, V.A. 22313-1450

## **ASSIGNMENT**

## Corporate

Marvell Semiconductor, Inc., a California corporation, having a principal place of business at 5488 Marvell Lane, Santa Clara, CA 95054 (hereafter the "Assignor"), is the owner by respective Assignment of United States Patent Application(s) identified below (hereafter sometimes called the "Patent Application(s)").

Marvell International Ltd., a corporation of Bermuda, having a place of business at Argyle House, 41A Cedar Avenue, Hamilton, Hm12, Bermuda (hereafter the "Assignee"), desires to acquire all of the rights to the Patent Application(s) and all inventions described and claimed therein or entitled to the benefit thereof.

THEREFORE, in consideration of the sum of One Dollar (\$1.00) or equivalent and other good and valuable consideration paid to it, Assignor hereby sells and assigns to the Assignee the entire right, title and interest in and to the Patent Application(s) identified below, and all inventions described and claimed therein or entitled to the benefit thereof, in any and all Letters Patent therefor, and in any and all reissues, extensions, renewals, reexaminations, divisions and continuations of such applications or Letters Patent to the full end of the term or terms for which such Letters Patent issue, such entire right, title and interest to be held and enjoyed by the above-named Assignee the same as they would have been held and enjoyed by the Assignor had this Assignment and sale not been made.

<u>Marvell</u> Semiconductor, Inc. Case No.	U.S. Serial No.	<u>U.S. Filing</u> <u>Date</u>	<u>Title</u>
MP0851	11/491,327	July 21, 2006	Efficient Blind Equalization for Quadrature Amplitude Modulation
MP0851PR	60/734,581	Nov. 8, 2005	Fast Blind Equalization for Multiple QAM Modulations

By its undersigned representative, the Assignor agrees

- a. to execute all papers necessary in connection with the Patent Application(s) and any continuing, divisional, reissue, reexamination or corresponding application thereof and also to execute separate Assignment in connection with such application as the Assignee may deem necessary or expedient;
- b. to execute all papers necessary in connection with any interference which may be declared concerning the Patent Application(s) or any continuation, division,

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PATENT REEL: 018461 FRAME: 0425 reissue or reexamination thereof and to cooperate with the Assignee in every way possible in obtaining evidence and going forward with such interference; and

c. to perform all affirmative acts which may be necessary to obtain a grant of a valid United States patent to the Assignee on any of the Patent Application(s) and on any continuation, division, reissue or reexamination of any of the Patent Application.

The Assignor hereby covenants that it has the full right to convey the entire interest herein assigned, and that it has not executed, and will not execute, any agreement in conflict therewith.

Marvell Semiconductor, Inc., a California corporation, certifies that it is the Assignee of the entire right, title and interest in the Patent Application(s) identified above by virtue of Assignment(s) from the inventors of the Patent Application(s) identified above. Assignment(s) to be recorded in the Patent and Trademark Office as identified below.

Marvell Semiconductor, Inc. Case No.	U.S. Serial No.	Assignment Execution Date
MP0851	11/491,327	July 20, 2006

The undersigned has reviewed the documents in the Patent Application(s) identified above and, to the best of undersigned's knowledge and belief, title is in the Assignor identified above.

The undersigned is empowered to sign this certificate on behalf of the Assignor.

I hereby declare that all statements made herein of my own knowledge are true, and that all statements made on information and belief are believed to be true; and further, that these statements are made with the knowledge that willful false statements, and the like so made, are punishable by fine or imprisonment, or both, under Section 1001, Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

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> PATENT REEL: 018461 FRAME: 0426

IN WITNESS WHEREOF, executed by the Assignor's undersigned representative on the date following the undersigned's name.

Marvell Semiconductor,

Eric Janofsky

Title: Vice President Intellectual Property and General Patent Counsel

Date: October <u>25</u>, 2006

By:

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PATENT REEL: 018461 FRAME: 0427

**RECORDED: 10/27/2006**