

PATENT ASSIGNMENT

Electronic Version v1.1

Stylesheet Version v1.1

SUBMISSION TYPE:

NEW ASSIGNMENT

NATURE OF CONVEYANCE:

ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
Avago Technologies Sensor IP Pte. Ltd.	11/10/2006

RECEIVING PARTY DATA

Name:	Avago Technologies General IP (Singapore) Pte. Ltd.
Street Address:	No. 1 Yishun Avenue 7
City:	Singapore
State/Country:	SINGAPORE
Postal Code:	768923

PROPERTY NUMBERS Total: 12

Property Type	Number
Patent Number:	5969399
Patent Number:	6115019
Patent Number:	6246386
Patent Number:	6552745
Patent Number:	6594079
Patent Number:	6700561
Patent Number:	6715003
Patent Number:	6934437
Application Number:	11459135
Application Number:	10938098
Application Number:	10945735
Application Number:	10941859

CORRESPONDENCE DATA

Fax Number: (303)297-2266

500183375

PATENT  
REEL: 018545 FRAME: 0413

CH \$480.00 5969399

*Correspondence will be sent via US Mail when the fax attempt is unsuccessful.*

Phone: (303) 298-9888  
Email: angela.troussel@klaaslaw.com  
Correspondent Name: Avago Technologies, Ltd.  
Address Line 1: P.O. Box 1920  
Address Line 4: Denver, COLORADO 80201

NAME OF SUBMITTER:

Angela Troussel

Total Attachments: 5

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## ASSIGNMENT AND ASSUMPTION AGREEMENT

This Assignment and Assumption Agreement (this "Agreement") is made and entered into this 10<sup>th</sup> day of November, 2006 (the "Effective Date"), by and between **AVAGO TECHNOLOGIES SENSOR IP PTE. LTD.** (Company Registration No. 200602838N), a company incorporated under the laws of Singapore whose registered office is at No. 1 Yishun Avenue 7, Singapore 768923 ("Assignor"), and **AVAGO TECHNOLOGIES GENERAL IP (SINGAPORE) PTE. LTD.** (Company Registration No. 200512430D), a company incorporated under the laws of Singapore whose registered office is at No. 1 Yishun Avenue 7, Singapore 768923 ("Assignee").

WHEREAS, Micron Technology, Inc. (the "Purchaser") and certain affiliates of Assignor and Assignee (collectively, the "Sellers") intend to enter into a certain Purchase and Sale Agreement (the "Purchase Agreement"), pursuant to which Purchaser is acquiring, among other things, certain intellectual property assets and liabilities from Sellers; and

WHEREAS, in order to fulfill the Sellers' obligations pursuant to the Purchase Agreement, Assignor desires to retain all of its Exclusive Intellectual Property Rights (as defined in the Consortium Agreement, such agreement defined in Section 1 below) to the extent pertaining to the Business (as defined in the Purchase Agreement) and transfer all of the Shared Intellectual Property Rights (as defined in the Consortium Agreement) in its possession to Assignee.

NOW, THEREFORE, for and in consideration of the promises and the mutual covenants contained herein, and for the other good and valuable consideration, the receipt, adequacy and legal sufficiency of which are hereby acknowledged, the parties hereby agree as follows:

1. Capitalized Terms. Capitalized terms used but not defined herein shall have the meanings for such terms that are set forth in the Purchase Agreement between Avago Technologies Pte. Ltd. (f/k/a Argos Acquisition Pte. Ltd.) and Agilent Technologies, Inc., dated as of August 14, 2005, as amended (the "Agilent Agreement"), or the Consortium Agreement dated as of December 1, 2005, by and among Assignee and certain other parties, including, by a certain Amendment No. 1 to Consortium Agreement, Assignor (the "Consortium Agreement").

2. Assignment and Assumption. Assignor hereby assigns, sells, transfers and sets over (collectively, the "Assignment") to Assignee effective as of the Effective Date: (a) all of the Shared Intellectual Property Rights in Assignor's possession pursuant to the Consortium Agreement (collectively, the "Acquired Items"), including, but not limited to, the intellectual property rights listed in Schedule 1 to this Agreement; (b) all of the Assumed Liabilities directly relating to such Acquired Items; and (c) all of Assignor's right, title and interest in and to any and all proceeds, causes of action and rights of recovery for past and future infringement or misappropriation of any of the Acquired Items. The Acquired Items are conveyed subject to any and all licenses, permissions, consents or other rights that may have been granted by Assignor or its predecessors-in-interest with respect thereto prior to the Effective Date. Assignee hereby accepts the Assignment and assumes and agrees to observe and perform all of the duties, obligations, terms, provisions, and covenants of, and to pay and discharge, all of the Assumed Liabilities directly relating to the Acquired Items (other than those Assumed Liabilities that are

conveyed pursuant to the other instruments of transfer executed pursuant to the Agilent Agreement).

3. Terms of the Purchase Agreement. Assignor acknowledges and agrees that the representations, warranties, covenants, agreements, and indemnities contained in the Purchase Agreement shall not be superseded hereby but shall remain in full force and effect to the full extent provided therein. In the event of any conflict or inconsistency between the terms of the Purchase Agreement and the terms hereof, the terms of the Purchase Agreement shall govern.

4. Further Actions. Each of the parties hereto covenants and agrees, at its own expense, to execute and deliver, at the request of the other party hereto, such further instruments of transfer and assignment and to take such other action as such other party may reasonably request to more effectively consummate the assignments and assumptions contemplated by this Agreement.

5. Consent to Assignment. This Agreement shall not constitute an assignment of any claim, contract, permit, franchise, or license if the attempted assignment thereof, without the consent of the other party thereto, would constitute a breach of such claim, contract, permit, franchise, or license or in any way adversely affect the rights either party thereunder. If such consent is not obtained, or if any attempted assignment thereof would be ineffective or would adversely affect the rights of Assignor thereunder so that Assignee would not in fact receive all such rights, then Assignee may act as the attorney-in-fact of Assignor in order to obtain for Assignee the benefits thereunder.

6. No Additional Remedies. Nothing in this instrument, express or implied, is intended or shall be construed to confer upon, or give to, any person, firm, or corporation other than Assignee and its successors and assigns, any remedy or claim under or by reason of this instrument or any terms, covenants, or conditions hereof, and all the terms, covenants and conditions, promises, and agreements contained in this instrument shall be for the sole and exclusive benefit of Assignee and its successors and assigns.

7. Governing Law. This Agreement shall be governed in all respects, including validity, interpretation and effect, by the laws of the State of California.

8. Counterparts. This Agreement may be executed in one or more counterparts, all of which shall be considered one and the same agreement and shall become effective when one or more counterparts have been signed by each of the parties and delivered to the other party, it being understood that all parties need not sign the same counterpart.

(SIGNATURE PAGE TO FOLLOW)

IN WITNESS WHEREOF, the parties have caused this Agreement to be executed as of the Effective Date.

**ASSIGNOR**

**AVAGO TECHNOLOGIES SENSOR IP PTE. LTD.**

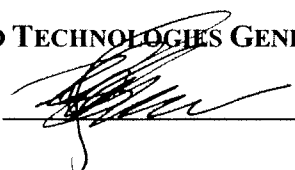
By:  \_\_\_\_\_

Name: \_\_\_\_\_

Title: \_\_\_\_\_

**ASSIGNEE**

**AVAGO TECHNOLOGIES GENERAL IP (SINGAPORE) PTE. LTD.**

By:  \_\_\_\_\_

Name: \_\_\_\_\_

Title: \_\_\_\_\_

# SCHEDULE 1

## PATENT APPLICATIONS AND ISSUED PATENTS

Case No.	SubCase	Country	Title	App. No.	File Date	Pat. No.	Issue Date
10060033	01	US	Method And Apparatus For Preventing Or Reducing Color Cross-Talk Between Adjacent Pixels In An Image Sensor Device		21-Jul-06		
10991476	01	US	Gamma Correction For Displays	09/703418	31-Oct-00	6700561	02-Mar-04
10060417			Expanding Camera Module Capability By Mounting Additional Dies Inside The Camera				
10970967	01	US	A Register Pixel For Liquid Crystal Displays	09/030245	25-Feb-98	6115019	05-Sep-00
10004182	01	US	Digital Camera And Method For Communicating Digital Image And At Least One Address Image Stored In The Camera To A Remotely Located Service Provider	09/504825	14-Feb-00	6715003	30-Mar-04
10970982	03	DE	A CMOS Active Pixel With Memory For Imaging Sensors	19857838.5	15-Dec-98		
10970982	02	JP	A CMOS Active Pixel With Memory For Imaging Sensors	PH11-100357	07-Apr-99		
10970982	01	US	A CMOS Active Pixel With Memory For Imaging Sensors	09/057429	08-Apr-98	6552745	22-Apr-03
10970982	04	GB	A CMOS Active Pixel With Memory For Imaging Sensors	9907847.9	06-Apr-99	2336497	13-Nov-02
10971120	02	JP	Integrated Micro-Display System	PH11-162062	09-Jun-99		
10971120	01	US	Integrated Micro-Display System	09/099918	18-Jun-98	6246386	12-Jun-01
10971120	03	EP	Integrated Micro-Display System	99303834.8	17-May-99		

Case No.	SubCase	Country	Title	App. No.	File Date	Pat. No.	Issue Date
10971863	01	US	High Gain Current Mode Photo-Sensor	09/080996	19-May-98	5969399	19-Oct-99
10971863	03	EP	High Gain Current Mode Photo-Sensor	99303245.7	27-Apr-99		
10971863	02	JP	High Gain Current Mode Photo-Sensor	PH11-132736	13-May-99		
			Optically-Controlled Switch And Optically-Controlled Optical Switching Method Using Dispersion Curve Shifting				
10020201	01	US		10/313358	06-Dec-02	6934437	23-Aug-05
			Image Screen And Method Of Forming Anti-Reflective Layer Thereon				
10991033	04	FR					
			Image Screen And Method Of Forming Anti-Reflective Layer Thereon				
10991033	06	GB					
			Image Screen And Method Of FORMING ANti-reflective Layer Thereon				
10991033	03	EP		00113903.9	30-Jun-00	1074882	29-Mar-06
			Fabrication Of Improved Projection Screen				
10991033	02	JP		P2000-220081	21-Jul-00		
			Image Screen And Method Of Forming Anti-Reflective Layer Thereon				
10991033	05	DE					
			Image Screen And Method Of Forming Anti-Reflective Layer Thereon				
10991033	01	US		09/368485	04-Aug-99	6594079	15-Jul-03
10040840	01	US	Reduced LUT By Using Row Activity				
			Imaging Algorithm Performance Acceleration Through Efficient Use Of Processor Cache				
10040841	01	US					
			Imaging Algorithm Through Use Of Custom CPU Pixel Windows Spanning Multiple Rows				
10040842	01	US					