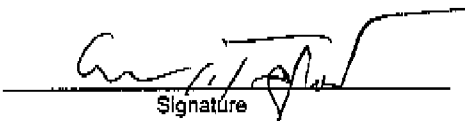
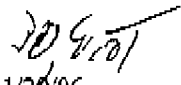


Recordation Form Cover Sheet PATENTS ONLY		Attorney Docket No.: <u>042390.P23926</u>
To the Director of the United States Patent and Trademark Office. Please record the attached original documents or copy thereof.		
1. Name of conveying party(ies): <u>Zarlink Semiconductor Limited</u> Additional name(s) of conveying party(ies) attached? <input checked="" type="checkbox"/> No <input type="checkbox"/> Yes	2. Name and address of receiving party(ies): Name: <u>Intel Corporation</u> Internal Address: _____ Street Address: <u>2200 Mission College Blvd.</u> City: <u>Santa Clara</u> State/Province: <u>CA</u> Zip: <u>95052-4040</u> Country: <u>U.S.A.</u> Additional name(s) & address(es) attached? Yes <input type="checkbox"/> No <input checked="" type="checkbox"/>	
3. Nature of Conveyance <input checked="" type="checkbox"/> Assignment <input type="checkbox"/> Merger <input type="checkbox"/> Security Agreement <input type="checkbox"/> Change of Name <input type="checkbox"/> Other: Execution Date(s): <u>July 14th, 2006</u>	4. Application number(s) or patent number(s): <input type="checkbox"/> If this document is being filed together with a new application, the execution date of the application is: _____ A. Patent Application No.(s) <u>11/423,015</u> B. Patent No.(s) _____ Additional numbers attached? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No	
5. Name and address of party to whom correspondence concerning document should be mailed: Name: <u>Blakely, Sokoloff, Taylor & Zafman LLP</u> Internal Address: _____ Street Address: <u>12400 Wilshire Boulevard, 7th Floor</u> <u>Los Angeles, California 90025</u>	6. Total number of applications and patents involved: <u>1</u> 7. Total Fee (37 CFR 3.41).....\$ <u>40.00</u> <input type="checkbox"/> Enclosed <input checked="" type="checkbox"/> Authorized to be charged to deposit account 8. Deposit Account Number: <u>02-2666</u> (Attach duplicate copy of this page if paying by deposit account)	
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9. Statement and signature. <i>To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.</i>		
<u>Edwin H. Taylor Reg. No. 25,129</u> Name of Person Signing	 Signature	 <u>11/25/06</u> Date
Total number of pages including cover sheet, attachments, and documents: <u>5</u>		

Mail documents to be recorded with required cover sheet information to:
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 Alexandria, Virginia 22313-1450

Based on Form PTO-1595 as modified by BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP on 05/09/03

CONFIRMATORY PATENT APPLICATION ASSIGNMENT

This Confirmatory Patent Application Assignment Agreement, dated July 14, 2006 is made by Zarlink Semiconductor Limited, a corporation incorporated under the laws of England and Wales ("Assignor"), to Intel Corporation, a Delaware corporation ("Assignee").

RECITALS

A. Assignor and Assignee and other parties have entered into an asset purchase agreement dated as of October 7, 2005, (the "Agreement"). All capitalized terms used herein but not otherwise defined shall have the meanings set forth in the Agreement.

B. Pursuant to the Agreement, Assignor has previously assigned to the Assignee all of Assignor's right, title and interest in and to the patent applications set forth on Exhibit A hereto (the "Patent Applications").

AGREEMENT

NOW, THEREFORE, in consideration of the foregoing premises, the mutual covenants and agreements contained in the Agreement and the covenants and agreements in the Assignment, Assignor hereby confirms as follows:

1. For good and valuable consideration, the receipt of which is hereby acknowledged, Assignor has, on October 7, 2005, pursuant to the terms of the Agreement, sold, transferred, conveyed, assigned and delivered to Assignee, with full title guarantee all of Assignor's right, title and interest in and to the Patent Applications and any patents that may issue there from, including any foreign counterparts, divisions continuations, or reissues of such patents, the same to be held by Assignee for Assignee's own use and enjoyment, and for the use and enjoyment of Assignee's successors, assigns and other legal representatives, as fully and entirely as the same would have been held and enjoyed by Assignor if this Assignment and sale had not been made; together with all claims for damages by reason of past infringements of the Patent Applications, along with the right to sue for and collect such damages for the use and benefit of Assignee and its successors, assigns and other legal representatives.

2. Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and, in the case of any patent applications filed with any office of any country or countries foreign to the United States, any officer of such country whose duty it is to issue patents or other evidence or forms of intellectual property protection or applications as aforesaid, to issue the same to Assignee and its successors, assigns and other legal representatives in accordance with the terms of this instrument.

Zarlink Confirmatory Patent Application Assignment

IN WITNESS WHEREOF, Assignor has executed this Assignment on the date first above written.

Assignor:

ZARLINK SEMICONDUCTOR LIMITED

By:  

Name: Scott Milligan Don McIntyre

Title: Director Director

[SIGNATURE PAGE TO ZARLINK CONFIRMATORY
PATENT APPLICATION ASSIGNMENT]

Zarlink Confirmatory Patent Application Assignment

Acknowledgment by Notary PublicState of n/aCounty of n/a

On this 14th day of July, 2006, before me, the undersigned Notary Public, personally appeared Don McIntyre and Scott Milligan personally known to me (or proved to me on the basis of satisfactory evidence) to be the person whose name is subscribed to the within instrument, and acknowledged to me that he or she executed the same.

Seal:

Signature: 

Name: Roberto Pontello - Corning Notary Public
in and for the Province of Ontario, Canada

Zarlink Confirmatory Patent Application Assignment

EXHIBIT A

S1121	Technique to control drift in analogue delay line	Dave Sawyer Nick Cowley Issac All	UK	08/06/2005	0511586.0
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**BLAKELY
SOKOLOFF
TAYLOR &
ZAFMAN**

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1279 OAKMEAD PARKWAY
SUNNYVALE, CALIFORNIA 94085
(408) 720-8300 (Telephone)
(408) 720-8383 (Facsimile)

FACSIMILE TRANSMITTAL SHEET (TRANSMITTAL TO PTO)

Deliver to: Assignment Division
Firm Name: U.S. PATENT AND TRADEMARK OFFICE
Fax Number: (571) 273-0140 Telephone No.: (571) 272-3350
From: Edwin H. Taylor
Date: 11/30/06 Time: _____
Operator: Mike Mendoza Matter: 042390.P23925
Number of pages including cover sheet: 4
In Re Patent Application of: Intel Corporation
Application.: 11/423,015
Filed: June 8, 2006
For: TECHNIQUE TO CONTROL DRIFT IN ANALOGUE DELAY LINE

Enclosed are the following documents

- Recordation Form Cover Sheet
- Assignment document from Zarlink Semiconductor Limited to Intel Corporation

CERTIFICATE OF TRANSMISSION

I hereby certify that this correspondence is being facsimile transmitted to the U.S. Patent and Trademark Office on:

Date of Transmission 11/30/2006

Mike Mendoza

(Typed or printed name of person transmitting paper)

(Signature of person transmitting paper)

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