D \$40.00 1132

PATENT ASSIGNMENT

Electronic Version v1.1 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
Rey H. Bruce	11/30/2006
Ricardo H. Bruce	11/29/2006
Patrick Digamon Bugayong	11/29/2006
Joel Alonzo Baylon	11/29/2006

RECEIVING PARTY DATA

Name:	BITMICRO NETWORKS, INC.
Street Address:	47929 Fremont Blvd
City:	Fremont
State/Country:	CALIFORNIA
Postal Code:	94538

PROPERTY NUMBERS Total: 1

Property Type	Number
Application Number:	11322442

CORRESPONDENCE DATA

Fax Number: (408)716-2651

Correspondence will be sent via US Mail when the fax attempt is unsuccessful.

Phone: 408.876.4470

Email: mail@uriartelaw.com

Correspondent Name: Uriarte Law

Address Line 1: 257 Rodonovan Drive

Address Line 2: Suite 100

Address Line 4: Santa Clara, CALIFORNIA 95051

ATTORNEY DOCKET NUMBER:	6005.011
NAME OF SUBMITTER:	Stephen R. Uriarte

PATENT REEL: 018594 FRAME: 0240

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Total Attachments: 4

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> PATENT REEL: 018594 FRAME: 0241

ASSIGNMENT

For good and valuable consideration, the receipt of which is hereby acknowledged, the person(s) named below (referred to as "INVENTOR" whether singular or plural) has sold, assigned, and transferred and does hereby sell, assign, and transfer to **BiTMICRO Networks**, Inc., a California corporation, having a place of business at 47929 Fremont Blvd., Fremont, CA 94538_("ASSIGNEE"), for itself and its successors, transferees, and assignees, the following:

1. The entire worldwide right, title, and interest in all inventions and improvements
("SUBJECT MATTER") that are disclosed in the application for United States Letters Patent
entitled: Multiple Chip Module and Package Stacking Method for Storage Devices
("APPLICATION"), which:
is to be filed herewith
was filed on December 29, 2005 , now bearing U.S. serial number 11/322,442 and fo which a Declaration was executed by INVENTOR on the date(s) below; and

2. The entire worldwide right, title, and interest in and to: (a) the APPLICATION, including any right of priority; (b) any divisional, continuation, substitute, renewal, reissue, and other related applications thereto which have been or may be filed in the United States or elsewhere in the world; and (c) any patents which may be granted on the applications set forth in (a) and (b) above.

INVENTOR agrees that ASSIGNEE may apply for and receive patents for SUBJECT MATTER in ASSIGNEE's own name.

INVENTOR agrees to do the following, when requested, and without further consideration, in order to carry out the intent of this Assignment: (1) execute all oaths, assignments, powers of attorney, applications, and other papers necessary or desirable to fully secure to ASSIGNEE the rights, titles and interests herein conveyed; (2) communicate to ASSIGNEE all known facts relating to the SUBJECT MATTER; and (3) generally do all lawful acts that ASSIGNEE shall consider desirable for securing, maintaining, and enforcing worldwide patent protection relating to the SUBJECT MATTER and for vesting in ASSIGNEE the rights, titles, and interests herein conveyed. INVENTOR further agrees to provide any successor, assign, or legal representative of ASSIGNEE with the benefits and assistance provided to ASSIGNEE hereunder.

INVENTOR represents that INVENTOR has the rights, titles, and interests to convey as set forth herein, and covenants with ASSIGNEE that the INVENTOR has made or will make hereafter no assignment, grant, mortgage, license, or other agreement affecting the rights, titles, and interests herein conveyed.

INVENTOR grants the attorney of record the power to insert on this Assignment any further identification which may be necessary or desirable in order to comply with the rules of the United States Patent and Trademark Office for recordation of this document.

Title of Document:

ASSIGNMENT

In Re Application of:

Bruce et al.

Title:

Multiple Chip Module and Package Stacking Method for Storage

Devices

Filed: (if applicable)

Serial No.: (if applicable)

Printed Name: Joel Alonzo Baylon

December 29, 2005 11/322,442

This Assignment may be executed in one or more counterparts, each of which shall be deemed an original and all of which may be taken together as one and the same Assignment.

Investor Signature	Date of <u>Signature</u>	Date Invention Declaration Signed by Inventor
Printed Name: Rey H. Bruce	, 2006	December 20, 2005
Inventor Signature	Date of <u>Signature</u>	Date Invention Declaration Signed by Inventor
Printed Name: Ricardo H. Brute	HOVEMBER 24, 2006	December 20, 2005
Inventor Signature	Date of <u>Signature</u>	Date Invention Declaration Signed by Inventor
Princed Name: Papirk Digamon Bugayoug	" NOVEMBER 24, 2006	December 20, 2005
Inventor Signature	Date of <u>Signature</u>	Date Invention Declaration Signed by Inventor
	NOVEMBER 27, 2006	December 20, 2005

Page 2 of 2

ASSIGNMENT

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Page 1 of 2

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11/322,442

This Assignment may be executed in one or more counterparts, each of which shall be deemed an original and all of which may be taken together as one and the same Assignment.

Inventor Signature	Date of <u>Signature</u>	Signed by Inventor
Printed Name: Rey H. Bruce	<u>//-30</u> , 2006	December 20, 2005
Inventor Signature	Date of <u>Signature</u>	Date Invention Declaration Signed by Inventor
Printed Name: Ricardo H. Bruce	, 2006	December 20, 2005
Inventor Signature	Date of <u>Signature</u>	Date Invention Declaration Signed by Inventor
Printed Name: Patrick Digamon Bugayong	, 2006	December 20, 2005
Inventor Signature	Date of <u>Signature</u>	Date Invention Declaration Signed by Inventor
Printed Name: Joel Alonzo Baylon	, 2006	December 20, 2005

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PATENT REEL: 018594 FRAME: 0245

RECORDED: 12/07/2006