

PATENT ASSIGNMENT

Electronic Version v1.1

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SUBMISSION TYPE:

NEW ASSIGNMENT

NATURE OF CONVEYANCE:

ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
Alliance Semiconductor Corporation	05/08/2006

RECEIVING PARTY DATA

Name:	Tundra Semiconductor Corporation
Street Address:	603 March Road
City:	Kanata
State/Country:	CANADA
Postal Code:	K2K 2M5

PROPERTY NUMBERS Total: 7

Property Type	Number
Patent Number:	6766317
Patent Number:	6839799
Patent Number:	6687256
Application Number:	09769929
Application Number:	10318769
Application Number:	10327223
Application Number:	11249690

CORRESPONDENCE DATA

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PATENT

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REEL: 018654 FRAME: 0167

CH \$280.00 6766317

ATTORNEY DOCKET NUMBER:	121-035
NAME OF SUBMITTER:	Lindsay G. McGuinness
Total Attachments: 3 source=assignment#page1.tif source=assignment#page2.tif source=assignment#page3.tif	

ASSIGNMENT

The undersigned, Alliance Semiconductor Corporation (the "Assignor") a Delaware corporation having a place of business at 2575 Augustine Drive, Santa Clara, CA 95054, United States of America, for good and valuable consideration, the receipt and sufficiency whereof is hereby acknowledged, does hereby sell, assign, transfer and set over to Tundra Semiconductor Corporation (the "Assignee") a corporation having a place of business at 603 March Road, Kanata, Ontario, Canada K2K 2M5, its successors and assigns, the entire right, title and interest in and to all inventions disclosed in the Letters Patent of the United States and applications therefor identified on the attached Schedule A (the "Patents"), and in and to the Patents, including any reissues and extensions thereof, and the right, where such right can be legally exercised, in its own name to apply for and obtain Letters Patent, Inventor's Certificates, Utility Models and Designs, in countries foreign to the United States, including the full right to claim for any such application the benefits of the International Convention as fully and entirely as it could have done if the application had been in its name, and the entire interest in any Letters Patent, Inventor's Certificate, Utility Model or Design which may be granted on any such application in such foreign country; and the Assignor hereby authorizes and requests any official of any country or countries foreign to the United States whose duty it is to issue Letters Patent, Inventor's Certificates, Utility Models or Designs, on applications as aforesaid, to issue any and all Letters Patent, Inventor's Certificates, Utility Models and Designs that may be granted for said invention to Assignee, its successors and assigns.

And for the above-named consideration, said Assignor hereby agrees that it will, without additional compensation but without cost to it, promptly communicate to said Assignee, its successors and assigns, any facts known to the Assignor respecting said inventions whenever reasonably requested, and testify in any legal proceedings, sign all lawful papers, execute all divisional, continuing and reissue applications for Letters Patent, Inventor's Certificates, Utility Models and Designs and assignments thereof, make all rightful oaths and generally do everything reasonably possible to aid said Assignee, its successors and assigns, as and when

requested by them, in obtaining and enforcing proper patent, utility model and design protection for said invention or inventions and improvements, in the United States and in all other countries.

EXECUTED at Santa Clara, California, with effect as of the 8th day of May, 2006.

Alliance Semiconductor Corporation

By:  Melvin L. Keating

Title: President and Chief Executive Officer

Signature of Witness

SCHEDULE A

Patent or Patent Application No.	Title	Filing Date	Date Issued
6,766,317	RANGE CHECK CELL AND A METHOD FOR THE USE THEREOF	July 18, 2001	July 20, 2004
6,839,799	METHOD OF PRIORITIZATION OF DATABASE ENTRIES	July 17, 2001	January 4, 2005
6,687,256	RECOVERABLE CUT-THROUGH BUFFER AND METHOD	December 19, 2002	February 3, 2004
09/769,929	INTEGRATED HIGH SPEED SWITCH ROUTER USING A MULTI-PORT ARCHITECTURE	January 24, 2001	Pending
10/318,769	SINGLE CYCLE WEIGHTED RANDOM EARLY DETECTION CIRCUIT AND METHOD	December 13, 2002	Pending
10/327,223	RECONFIGURABLE DATA PACKET HEADER PROCESSOR	December 20, 2002	Pending
11/249,690	OFF-LOAD ENGINE TO RE- SEQUENCE DATA PACKETS WITHIN HOST MEMORY	October 12, 2005	Pending