

12-12-2006



103347669

Form PTO-1595  
(Rev. 10/02)  
OMB No. 0651-0027 (exp. 6/30/20)

IEET U.S. DEPARTMENT OF COMMERCE  
U.S. Patent and Trademark Office

Attorney Docket No. 501.32328X00

To the Honorable Commissioner of Patents and Trademarks: Please record the attached original documents or copy thereof.

12.7.06

1. Name of conveying party(ies):  
Manabu TSUNOZAKI, Kyoko ISHII, Koichi NOZAKI, Hiroshi YOSHIOKA, Yoshihisa KOYAMA, Shinji UDO, Hidetomo AOYAGI, Sinichi MIYATAKE, Makoto MORINO, Akihiko HOSHIDA

Additional name(s) of conveying party(ies) attached?  Yes  No

2. Name and address of receiving party(ies)  
Name: (1) Hitachi, Ltd.  
6, Kanda Surugadai 4-chome,  
Chiyoda-ku, Tokyo, Japan  
  
(2) Hitachi VLSI Engineering Corp.  
5-20-1, Josuihon-cho, Kodaira-shi,  
Tokyo, Japan  
  
(3) Hitachi Hokkai Semiconductor, Ltd.  
145, Aza Nakajima, Nanae-cho,  
Kameda-gun, Hokkaido, Japan

Additional name(s) & address(es) attached?  Yes  No

3. Nature of conveyance/ Execution Date: October 1, 1993;  
Oct 1, 1993; Oct 1, 1993; Oct 1, 1993; Oct 1, 1993; Oct  
1, 1993; Oct 1, 1993; Oct 1, 1993; Oct 1, 1993; Oct 1,  
1993.

- Assignment  Merger
- Security Agreement  Change of Name
- Joint Research Agreement
- Government Interest Assignment
- Executive Order 9424, Confirmatory License
- Other **CORRECTED ASSIGNMENT -Correction of Recordation of Assignment in 1st named Assignee misspelled in original Recordation Cover Sheet dated Oct 5, 1994 and Notice of Recordation Reel/Frame 7156/0325.**

4. Application number(s) or patent number(s):

This document is being filed together with a new application.

A. Patent Application No.(s)

08/109,071

B. Patent Registration No.(s)

5,440,521

Additional number(s) attached  Yes  No

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: **ANTONELLI, TERRY, STOUT & KRAUS, LLP**

Internal Address:

**1300 NORTH 17<sup>TH</sup> STREET – SUITE 1800**

City: **ARLINGTON** State: **VA** Zip **22209**

Phone Number: (703) 312-6600  
Facsimile Number: (703) 312-6666  
Email Address: email@antonelli.com

6. Total number of applications and patents involved **1**

7. Total fee (37 CFR 3.41)..... **\$80.00**

- Authorized to be charged by credit card
- Authorized to be charged to deposit account
- Enclosed
- None required (government interest not affecting title)

8. Payment Information:

a. Credit Card Last 4 numbers:  
Expiration date:

b. Deposit account number: 01-2135

Authorized User Name:

( )

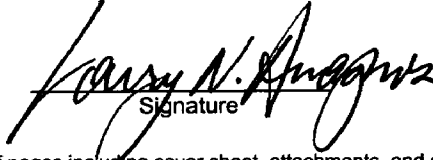
DO NOT USE THIS SPACE

9. Statement and signature.

*To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.*

Larry N. Anagnos, Reg#32,392

\_\_\_\_\_  
Name of Person Signing

  
\_\_\_\_\_  
Signature

December 7, 2006

\_\_\_\_\_  
Date

Total number of pages including cover sheet, attachments, and document: **10**

Documents to be recorded (including cover sheet) should be faxed to (571) 273-0140, or mailed to:  
Mail Stop Assignment Recordation Services, Director of the USPTO, P.O. Box 1450, Alexandria, VA 22313-1450



CORDATION FORM COVER SHEET  
PATENTS ONLY

501.32328X00

Tab settings

To the Honorable Commissioner of Patents and Trademarks: Please record the attached original documents or copy thereof.

1. Name of conveying party(ies):

Manabu Tsunozaki, Kyoko Ishii,  
Koichi Nozaki, Hiroshi Yoshioka,  
Yoshihisa Koyama, Shinji Udo

Additional name(s) of conveying party(ies) attached?  Yes  No

2. Name and address of receiving party(ies)

Hitachi, Ltd., Hitachi VLSI  
Name: Engineering Corp. and Hitachi  
Hokkai Semiconductor, Ltd.

Internal Address: \_\_\_\_\_

3. Nature of conveyance:

- Assignment  Merger
- Security Agreement  Change of Name
- Other \_\_\_\_\_

Execution Date: October 1, 1993

6, Kanda Surugadai 4-chome,  
Street Address: Chiyoda-ku, Tokyo, Japan;  
5-20-1, Josuihon-cho, Kodaira-shi,  
Tokyo, Japan; and 145, Aza Nakajima,  
Nanae-cho, Kameda-gun, Hokkaido, Japan

Additional name(s) & address(es) attached?  Yes  No

4. Application number(s) or patent number(s):

If this document is being filed together with a new application, the execution date of the application is: \_\_\_\_\_

A. Patent Application No.(s)

Serial No. 08/109,071  
Filed: August 19, 1993

B. Patent No.(s)

Additional numbers attached?  Yes  No

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: Antonelli, Terry, Stout & Kraus

Internal Address: \_\_\_\_\_

Street Address: 1300 North Seventeenth St.  
Suite 1800

City: Arlington State: Va. ZIP: 22209

6. Total number of applications and patents involved: 1

7. Total fee (37 CFR 3.41).....\$ 80.00

- Enclosed
- Additional fees:
- /Authorized to be charged to deposit account

8. Deposit account number:

01-2135

(Attach duplicate copy of this page if paying by deposit account)

DO NOT USE THIS SPACE

91861132

9. Statement and signature.

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

Larry N. Anagnos

Name of Person Signing  
Reg. No. 32,392

Larry N. Anagnos  
Signature

Oct. 5, 1994

Date

Total number of pages including cover sheet, attachments, and document: 4

Mail documents to be recorded with required cover sheet information to:  
Commissioner of Patents & Trademarks, Box Assignments  
Washington D.C. 20231

PATENT

REEL: 018679 FRAME: 0317

Continuation of Information in Item 1:

Hidetomo Aoyagi, Sinichi Miyatake,  
Makoto Morino and Akihiko Hoshida

REEL 156 FRAME 326

ASSIGNMENT

For good and valuable consideration, the sufficiency and receipt of which is hereby acknowledged, as a below named inventor, I hereby sell and assign to Hitachi, Ltd., Hitachi VLSI Engineering Corp. & Hitachi Hokkai Semiconductor, Ltd. of Chiyoda-ku, Tokyo, Japan, Kodaira-shi, Tokyo, Japan & Kameda-gun, Hokkaido, Japan its successors and assigns or other legal representatives all my rights, title and interest, in and for the United States of America, in and to

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE, U.S. Serial No. 08/109,071 filed on August 19, 1993

invented by me (if only one is named below) or us (if plural inventors are named below) and described in the application for United States Letters Patent therefor, executed by me even date herewith, and all United States Letters Patent which may be granted therefor, and all divisions, continuations and extensions thereof, the said interest being the

entire ownership of the said Letters Patent when granted, to be held and enjoyed by said Hitachi, Ltd., Hitachi VLSI Engineering Corp. & Hitachi Hokkai Semiconductor, Ltd.

, its successors, assigns or other legal representatives, to the full end of the term for which said Letters Patent may be granted, as fully and entirely as the same would have been held and enjoyed by me or us if this assignment and sale had not been made;

And I hereby agree to sign and execute any further documents or instruments which may be necessary, lawful, and proper in the prosecution of the above-named application or in the preparation and prosecution of any continuing, continuation-in-part, substitute, divisional, renewal, reviewed or reissue applications or in any amendment, extension, or interference proceedings, or otherwise to secure the title thereto in said assignee;

And I do hereby authorize and request the Commissioner of Patents to issue said Letters Patent to said Hitachi, Ltd., Hitachi VLSI Engineering Corp. & Hitachi Hokkai Semiconductor, Ltd.

(Full Name)

(Signature)

- 1) Date October 1, 1993 Inventor Manabu TSUNOZAKI Residence Same as Post Office Address Citizenship Japan  
Post Office Address 5-22-3, Josuihon-cho, Kodaira-shi, Tokyo, Japan
- 2) Date October 1, 1993 Inventor Kyoko ISHII Residence Same as Post Office Address Citizenship Japan  
Post Office Address 1-2-43, Sakae-machi, Hamura-shi, Tokyo, Japan
- 3) Date October 1, 1993 Inventor Koichi NOZAKI Residence Same as Post Office Address Citizenship Japan  
Post Office Address 1-23-10, <sup>riekuei</sup> Kitasakae, Chitose-shi, Hokkaido, Japan
- 4) Date October 1, 1993 Inventor Hiroshi YOSHIOKA Residence Same as Post Office Address Citizenship Japan  
Post Office Address 1-9-32-106, Tanaka-cho, Akishima-shi, Tokyo, Japan
- 5) Date October 1, 1993 Inventor Yoshihisa KOYAMA Residence Same as Post Office Address Citizenship Japan  
Post Office Address 5-8-2-201, Matsubara-cho, Akishima-shi, Tokyo, Japan

Witness(es)

All) \_\_\_\_\_ 2) Asami Negashima 4) Shunji Murakami

REEL 018679 FRAME 0319

OCT - 5 94

For good and valuable consideration, the sufficiency and receipt of which is hereby acknowledged, as a below named inventor, I hereby sell and assign to Hitachi, Ltd., Hitachi VLSI Engineering Corp. & Hitachi Hokkai Semiconductor, Ltd. of Chiyoda-ku, Tokyo, Japan, Kodaira-shi, Tokyo, Japan & Kameda-gun, Hokkaido, Japan its successors and assigns or other legal representatives all my rights, title and interest, in and for the United States of America, in and to

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE, U.S. Serial No. 08.109,071 filed on August 19, 1993

invented by me (if only one is named below) or us (if plural inventors are named below) and described in the application for United States Letters Patent therefor, executed by me even date herewith, and all United States Letters Patent which may be granted therefor, and all divisions, continuations and extensions thereof, the said interest being the

entire ownership of the said Letters Patent when granted, to be held and enjoyed by said Hitachi, Ltd., Hitachi VLSI Engineering Corp. & Hitachi Hokkai Semiconductor, Ltd., its successors, assigns or other legal representatives, to the full end of the term for which said Letters Patent may be granted, as fully and entirely as the same would have been held and enjoyed by me or us if this assignment and sale had not been made;

And I hereby agree to sign and execute any further documents or instruments which may be necessary, lawful, and proper in the prosecution of the above-named application or in the preparation and prosecution of any continuing, continuation-in-part, substitute, divisional, renewal, reviewed or reissue applications or in any amendment, extension, or interference proceedings, or otherwise to secure the title thereto in said assignee;

And I do hereby authorize and request the Commissioner of Patents to issue said Letters Patent to said Hitachi, Ltd., Hitachi VLSI Engineering Corp. & Hitachi Hokkai Semiconductor, Ltd.

(Full Name)

(Signature)

- 1) Date October 1, 1993 Inventor Shinji UDO Shinji Udo  
 Residence Same as Post Office Address Citizenship Japan  
 Post Office Address 1-9-32-205, Tanaka-cho, Akishima-shi, Tokyo, Japan
- 2) Date October 1, 1993 Inventor Hidetomo AOYAGI Hidetomo Aoyagi  
 Residence Same as Post Office Address Citizenship Japan  
 Post Office Address 519-18-102, Miyazawa-cho, Akishima-shi, Tokyo, Japan
- 3) Date October 1, 1993 Inventor Sinichi MIYATAKE Sinichi Miyatake  
 Residence Same as Post Office Address Citizenship Japan  
 Post Office Address 1917-1-301, Hane, Hamura-shi, Tokyo, Japan
- 4) Date October 1, 1993 Inventor Makoto MORINO Makoto Morino  
 Residence Same as Post Office Address Citizenship Japan  
 Post Office Address 5-5-7, Mihori-cho, Akishima-shi, Tokyo, Japan
- 5) Date October 1, 1993 Inventor Akihiko HOSHIDA Akihiko Hoshida  
 Residence Same as Post Office Address Citizenship Japan  
 Post Office Address 1658-1, Kumagawa, Fussa-shi, Tokyo, Japan

Witness(es)

All) \_\_\_\_\_ 2) Yukihide SUZUKI 4) Hidetoshi Miyawaki

REEL 156 FRAME 378