

PATENT ASSIGNMENT

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SUBMISSION TYPE:

NEW ASSIGNMENT

NATURE OF CONVEYANCE:

ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
TNS Holdings, Inc.	12/06/2006

RECEIVING PARTY DATA

Name:	Spansion LLC
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PROPERTY NUMBERS Total: 8

Property Type	Number
Patent Number:	6295571
Patent Number:	6467011
Patent Number:	6519672
Patent Number:	6892298
Patent Number:	6675277
Patent Number:	6665777
Patent Number:	6782440
Patent Number:	6715059

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PATENT

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APPENDIX B: FORM PATENT ASSIGNMENT AGREEMENT

ASSIGNMENT OF PATENT RIGHTS

For good and valuable consideration, the receipt of which is hereby acknowledged, **TNS Holdings, Inc.**, a Texas Company, with an office at 111 Congress Avenue, Suite 3000, Austin, TX, 78701, ("**Assignor**"), does hereby sell, assign, transfer and convey unto Spansion LLC, having an office at 915 DeGuine Drive, P.O. Box 3453, Sunnyvale, CA 94088 ("**Assignee**") or its designees, all of Assignor's right, title and interest in and to the patents listed below, any and all counterpart foreign patents, applications and certificates of invention, and all reissues, re-examinations, renewals, extensions and of any of the foregoing (collectively "**Patent Rights**"):

Patent No.	Country	Filing Date	Title and Inventor(s)
<u>6,295,571</u>	US	03/19/1999	Shared memory apparatus and method for multiprocessor systems; Scardamalia , et al.
<u>6,467,011</u>	US	05/15/2001	Shared memory apparatus and method for multiprocessor systems, Scardamalia , et al
<u>6,519,672</u>	US	05/10/2001	Shared memory apparatus and method for multiprocessor systems; Scardamalia , et al.
<u>6,892,298</u>	US	07/25/2001	Load/store micropacket handling system; West
<u>6,675,277</u>	US	07/25/2001	Method and apparatus for demand usable adapter memory access management; West , et al.
<u>6,665,777</u>	US	07/25/2001	Method, apparatus, network, and kit for multiple block sequential memory management; West
<u>6,782,440</u>	US	07/25/2001	Resource locking and thread synchronization in a multiprocessor environment; Miller
<u>6,715,059</u>	US	07/25/2001	Methods and systems for a shared memory unit with extendable functions; Miller

Serial or Publication No.	Country	PCT Filing or Publication Date	Title
<u>60/220974</u>	US	07/26/2000	A technique for providing globally-available synchronization primitives in a distributed computing environment where nodes within that environment share at least one storage resource
<u>60/220748</u>	US	07/26/2000	A technique for providing globally-available synchronization primitives in a distributed computing environment where nodes within that environment share at least one storage resource
<u>CA2382927</u>	Canada	03/08/2001	Semaphore control of shared-memory
<u>CA2367065</u>	Canada	09/28/2000	Shared memory apparatus and method for multiprocessor systems
<u>CA2382836</u>	Canada	04/19/2001	Low latency, high bandwidth multi-computer system interconnect
<u>CA2382929</u>	Canada	03/08/2001	Shared memory disk
<u>CA2382728</u>	Canada	03/08/2001	Efficient event waiting
<u>EP1166209</u>	Europe	01/02/2002	Shared memory apparatus and method for multiprocessor systems
<u>EP1224560</u>	Europe	07/24/2002	Low latency, high bandwidth multi-computer system interconnect
<u>EP1214651</u>	Europe	06/19/2002	Semaphore control of shared-memory

Serial or Publication No.	Country	PCT Filing or Publication Date	Title
<u>EP1214653</u>	Europe	06/19/2002	Shared memory disk
<u>WO0057286</u>	PCT	09/28/2000	Shared memory apparatus and method for multiprocessor systems
<u>WO0127781</u>	PCT	04/19/2001	Low latency, high bandwidth multi-computer system interconnect
<u>WO0116737</u>	PCT	03/08/2001	Cache-coherent shared-memory cluster
<u>WO0116738</u>	PCT	03/08/2001	Efficient page ownership control
<u>WO0116740</u>	PCT	03/08/2001	Efficient event waiting
<u>WO0116741</u>	PCT	05/02/2001	Semaphore control of shared-memory
<u>WO0116742</u>	PCT	03/08/2001	Network shared memory
<u>WO0116743</u>	PCT	03/08/2001	Shared memory disk
<u>WO0116750</u>	PCT	03/08/2001	High-availability, shared-memory cluster
<u>WO0116760</u>	PCT	03/08/2001	Switchable shared-memory cluster
<u>WO0116761</u>	PCT	03/08/2001	Efficient page allocation
<u>WO0208907</u>	PCT	01/31/2002	Multiple block sequential memory management
<u>WO0208916</u>	PCT	01/31/2002	Load/store micropacket handling system
<u>WO0208918</u>	PCT	01/31/2002	Shared as needed programming model
<u>WO0208929</u>	PCT	01/31/2002	High-availability shared-memory system

Serial or Publication No.	Country	PCT Filing or Publication Date	Title
<u>WO03010626</u>	PCT	02/06/2003	Distributed shared memory management
<u>WO03010667</u>	PCT	02/06/2003	Resource locking and thread synchronization in a multiprocessor environment
<u>WO03014864</u>	PCT	02/06/2003	Technique for guaranteeing the availability of per thread storage in a distributed computing environment

In addition, Assignor agrees to and hereby does sell, assign, transfer and convey unto Assignee all rights (i) in and to causes of action and enforcement rights for the Patent Rights including all rights to pursue damages, injunctive relief and other remedies for past and future infringement of the Patent Rights, and (ii) the right to apply in any or all countries of the world for patents, certificates of invention or other governmental grants for the Patent Rights, including without limitation under the Paris Convention for the Protection of Industrial Property, the International Patent Cooperation Treaty, or any other convention, treaty, agreement or understanding.

Assignor also hereby authorizes the respective patent office or governmental agency in each jurisdiction to issue any and all patents or certificates of invention which may be granted upon any of the Patent Rights in the name of Assignee, as the assignee to the entire interest therein.

The terms and conditions of this Assignment shall inure to the benefit of Assignee, its successors, assigns and other legal representatives, and shall be binding upon Assignor, its successor, assigns and other legal representatives.

IN WITNESS WHEREOF this Assignment of Patent Rights is executed at 800 pm on Wednesday, December 6, 2006

ASSIGNOR

By: TNS Holdings, Inc.
Name: [Signature]
Title: Corp Secretary
(Signature MUST be notarized)

[Signature]
Notary

TNS Patent Option

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