# PATENT ASSIGNMENT

Electronic Version v1.1 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	Termination and Release of Security Interest in Patent Rights

## **CONVEYING PARTY DATA**

Name	Execution Date
Wells Fargo Foothill, Inc. (formerly known as Foothill Capital Corporation)	12/22/2006

# RECEIVING PARTY DATA

Name:	Intergraph Corporation	
Street Address:	Mailstop: IW2008	
City:	Huntsville	
State/Country:	ALABAMA	
Postal Code:	35894	

## PROPERTY NUMBERS Total: 57

Property Type	Number
Patent Number:	4860192
Patent Number:	4872125
Patent Number:	4873656
Patent Number:	4884197
Patent Number:	4899275
Patent Number:	4910685
Patent Number:	4916647
Patent Number:	4933835
Patent Number:	4969114
Patent Number:	5047971
Patent Number:	5091846
Patent Number:	5255384
Patent Number:	5274593
Patent Number:	5299147
Patent Number:	5426780
	DATENT

PATENT REEL: 018746 FRAME: 0019

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Patent Number:  Patent Number:  Patent Number:  Patent Number:  Patent Number:  Patent Number:	5461709         5463750         5479646         5502829         5542088         5546569
Patent Number: Patent Number: Patent Number: Patent Number:	5479646       5502829       5542088
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Patent Number:	
	5546569
Patent Number:	5560028
Patent Number:	5579222
Patent Number:	5598115
Patent Number:	5682468
Patent Number:	5692184
Patent Number:	5745099
Patent Number:	5764936
Patent Number:	5778227
Patent Number:	5790461
Patent Number:	5794003
Patent Number:	5798923
Patent Number:	5835095
Patent Number:	5892654
Patent Number:	5910804
Patent Number:	5924125
Patent Number:	5996062
Patent Number:	6014127
Patent Number:	6016392
Patent Number:	6029257
Patent Number:	6032240
Patent Number:	6052691
Patent Number:	6124861
Patent Number:	6158025
Patent Number:	6185668
Patent Number:	6204851
Patent Number:	6237044
Patent Number:	6297798
Patent Number:	6374329 PATENT

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Patent Number:	6392651
Patent Number:	6615233
Patent Number:	6892293
Patent Number:	D379800
Patent Number:	D401915
Patent Number:	D408373
Patent Number:	6219226

#### **CORRESPONDENCE DATA**

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NAME OF SUBMITTER: Kirstie Howard	ATTORNEY DOCKET NUMBER:	041945/0052
	NAME OF SUBMITTER:	Kirstie Howard

## Total Attachments: 8

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PATENT REEL: 018746 FRAME: 0021

# TERMINATION AND RELEASE OF SECURITY INTEREST IN PATENT RIGHTS

TERMINATION AND RELEASE dated as of December 22, 2006, from WELLS FARGO FOOTHILL, INC. (formerly known as FOOTHILL CAPITAL CORPORATION), a California corporation, with its principal place of business located at 2450 Colorado Avenue, Suite 3000, West Santa Monica, CA 90404, as Secured Party (the "Secured Party") to INTERGRAPH CORPORATION, a Delaware corporation ("Debtor").

#### WITNESSETH:

WHEREAS, pursuant to the Loan And Security Agreement, dated as of December 20, 1996 and amended and restated on November 30, 1999, between the Debtor and the Secured Party (the "Loan Agreement"), a security interest (the "Security Interest") was granted by the Debtor to the Secured Party in certain collateral, including the Patent Collateral (as hereinafter defined);

WHEREAS, pursuant to that certain Amended and Restated Patent Security Agreement, dated as of November 30, 1999, between the Secured Party and the Debtor (the "Patent Security Agreement"), the Debtor, by reference to the Loan Agreement, reaffirmed its intent to grant a Security Interest to the Secured Party specifically in certain Patent Collateral;

WHEREAS, the Patent Security Agreement was recorded in the Patent Division of the United States Patent and Trademark Office on December 16, 1999, at Reel 010425 and Frame 0955; and

WHEREAS, the Secured Party now desires to terminate and release the entirety of its Security Interest in the Patent Collateral;

NOW, THEREFORE, for good and valuable consideration including the satisfaction of all obligations, indebtedness and liabilities secured by the Patent Collateral pursuant to the Loan Agreement, the receipt and adequacy of which are hereby acknowledged, and upon the terms set forth in this Termination and Release, the Secured Party hereby states as follows:

- 1. <u>Definitions</u>. The term "<u>Patent Collateral</u>," as used herein, shall mean all of the Debtor's right, title and interest of every kind and nature as of the date hereof in the Patents (including, without limitation, those items listed on <u>Schedule A</u> hereto). The term "<u>Patents</u>" shall have the meaning provided by reference in the Loan Agreement and the Patent Security Agreement.
- 2. Release of Security Interest. The Secured Party hereby terminates, releases and discharges its Security Interest in the Patent Collateral, and any right, title or interest of the Secured Party in such Patent Collateral shall hereby cease and become void.

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3. <u>Further Assurances</u>. The Secured Party hereby agrees to duly execute, acknowledge, procure and deliver any further documents and to do such other acts as may be reasonably necessary to effect the release of the Security Interest contemplated hereby.

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IN WITNESS WHEREOF, the undersigned has executed this Termination and Release by its duly authorized officer as of the date first above written.

> WELLS FARGO FOOTHILL, INC. as Secured Party

By: MSerring
Name: Kotsekt BELVIEL
Title: VP

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STATE OF Georgia	)	
COUNTY OF Fulton	)	ss.:

On this 22 day of December, 2006, before me personally appeared Robert Bernier to me known who, being by me duly sworn, did depose and say that he/she is \_\_\_\_\_\_ of WELLS FARGO FOOTHILL, INC., described herein and which executed the foregoing instrument, and that he/she signed his/her name thereto pursuant to the authority granted by WELLS FARGO FOOTHILL, INC.

Motary Public

(Affix Seal Below)



Schedule A

# U.S. Patents and Patent Applications

<u>Patent</u>	Application Number	Patent Number
Quadword Boundary Cache System	06/915,274	4,860,192
Multiple Processor Accelerator for Logic Simulation	07/142,721	4,872,125
Multiple Processor Accelerator for Logic Simulation	07/067,633	4,873,656
Method and Apparatus for Addressing a Cache Memory	06/915,319	4,884,197
Cache-MMU System	07/346,251	4,899,275
Video Circuit Including a Digital to Analog Converter in the Monitor which converts the Digital data to Analog currents before conversion to Analog voltages	06/530,607	4,910,685
Hardware Pipeline Processor for Logic Simulation	07/067,634	4,916,647
Apparatus for Maintaining Consistency of a Cache Memory with a Primary Memory (as amended)	07/300,174	4,933,835
Method for Determining an Intuitively Defined Spatial Relationship Among Physical Entities	07/270,255	4,969,114
Circuit Simulation	07/394,232	5,047,971
Cache Providing Caching/Non- caching Write-through and Copyback Modes for Virtual Addresses and Including Bus Snooping to Maintain Coherency	07/428,480	5,091,846
Memory Address Translation System having Modifiable & Non-Modifiable Translation Mechanisms	07/776,906	5,255,384
High Speed Redundant Rows and Columns for Semiconductor Memories	07/590,243	5,274,593
Decoder Scheme for Fully Associative Translation- Lookaside Buffer	08/023,783	5,299,147

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System For Dynamic	07/845,337	5,426,780
Segmentation Analysis Using	01/010,007	0,.20,700
Conversion of Relational Data		
Into Object-Oriented Data		
Pulsed Ground Circuit for CAM	08/023,904	5,446,685
and PAL Memories	[	5,113,005
3D Input System for CAD	08/024,239	5,461,709
Systems	00/024,237	3,401,709
Method and Apparatus for	08/146,818	5,463,750
Translating Virtual Addresses in	06/140,610	3,403,730
a Data Processing System		
having Multiple Instruction		
Pipelines and Separate TLB's		
for Each Pipeline (as amended)	•	
Method and Apparatus for	08/118,539	5,479,646
Obtaining Data from a Data	00.110,000	5, 175,010
Circuit Utilizing Alternating		
Clock Pulses to Gate the Data to		
the Output (As amended)		
Apparatus for Obtaining Data	08/148,219	5,502,829
from a Translation Memory	00/140,219	3,302,027
Based on Carry Signal from an		
Adder, as amended		
Method and Apparatus for	08/235,595	5,542,088
Enabling Control of Task	00/233,373	3,342,000
Execution		
Apparatus for Writing Data to	08/118,378	5,546,569
and Reading Data from a Multi-	00/110,5/0	3,340,303
port RAM in a Single Clock		
Cycle		
Software Scheduled Superscaler	08/422,753	5,560,028
Computer Architecture	,	0,000,020
Distributed Administration	07/990,583	5,579,222
System for Computer Software		5,5 / 5,222
Comparator Cell for Use in a	08/385,496	5,598,115
Content Addressable Memory	00.000,.50	3,550,110
OLE for Design and Modeling	08/378,251	5,682,468
Object Relationship	08/437,942	5,692,184
Management System		3,072,104
Cursor Positioning Method	08/573,689	5,745,099
Method and Apparatus for	08/435,348	5,764,936
Dynamically Interpreting	00/755,5 <del>70</del>	J, / U-1, ZJU
Drawing Commands		
System for Adding Attributes to	08/509,847	5,778,227
an Object at Run Time in an	00/307,047	J, 1 10,221
Object Orientated Computer		
Environment Computer		
Register File With Bypass	08/905,034	5,790,461
Capability	00/200,034	3,770,701

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Instruction Cache Associative Crossbar Switch System	08/754,337	5,794,003
Optimal Projection Design and Analysis	08/544,812	5,798,923
Visible Line Processor	08/438,048	5,835,095
Apparatus for Improved Airflow	08/866,479	5,892,654
Through a Computer Chassis		
OLE for Design and Modeling	08/855,775	5,910,804
Method and Apparatus for	08/520,973	5,924,125
Parallel Access to Consecutive	•	, ,
TLB Entries		
Method and Apparatus for	08/751,273	5,996,062
Controlling an Instruction		
Pipeline in a Data Processing		
System		
Cursor Positioning Method	09/026,955	6,014,127
Method for Object-Oriented	08/552,812	6,016,392
Programming Using Dynamic		
Interfaces		
Apparatus and Method for	08/985,808	6,029,257
Testing Computer Systems		
Bypassing a Non-paged Pool	09/178,870	6,032,240
Controller When Accessing a		
Remainder Portion of a Random		
Access Memory (Apparatus &		
Method of Assessing Random Access Memory)		
Object Relationship	08/937,147	6,052,691
Management System	08/93/,14/	0,032,091
Method and Apparatus for	08/974,930	6,124,861
Unambiguous Selection of	08/9/4,930	0,124,601
Graphic Objects, Keypoints and		
Relationships		1
Apparatus and Method for	09/123,339	6,158,025
Memory Error Detection	07/125,557	0,150,025
Method and Apparatus for	08/576,876	6,185,668
Speculative Execution of	00/3/0,0/0	0,165,500
Instructions		
Apparatus and Method for	09/054,964	6,204,851
Applying Effects to Graphical		,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,
Images		
A Method for Object-Oriented	09/204,329	6,237,044 B1
Programming Using Dynamic	-	
Interfaces		
Method and Apparatus for	09/092,332	6,297,798
Dynamically Interpreting		
Drawing Command		
High-Availability Super Server	08/802,827	6,374,329

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Interactive Time Line Visualization	09/054,629	6,392,651
Apparatus and Method for Transmitting Documents Between a Server Computer and a Client Computer	09/249,403	6,615,233
VLIW Processor and Method Therefor (formerly Instruction Cache Associative Crossbar Switch)	09/057,861	6,892,293
Computer	29/047,791	D379,800
Computer Chassis Cover	29/080,880	D401,915
Computer Chassis	29/084,114	D408,373
Computer Chassis with Retractable Access Door	09/260,845	6,219,226

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**RECORDED: 01/11/2007**