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Form PTO-1595 (Rev. 08/05) OMB No. 0651-0027 (exp. 6/30/2008)  01 - 16 -	2007 United States	PARTMENT OF COMMERCE S Patent and Trademark Office
To the Director of the U.S. Patent a 10336	documents or the	e new address(es) below.
1. Name of conveying party(ies) Seiko Epson Corporation	2. Name and address of receivance: Transmeta Corporation Internal Address:	• • • • •
Additional name(s) of conveying party(ies) attached? Yes $X$ No 3. Nature of conveyance/Execution Date(s):	Street Address: 3940 Freedo	om Circle 89
Execution Date(s) 05/24/2001  X Assignment  Change of Name	City: Santa Clara	1132

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Additional name(s) of conveying party(ies) attached? $oxed{\square}$ Yes $oxed{X}$ No		
3. Nature of conveyance/Execution Date(s):	Street Address: 3940 Freedom Circle	
Execution Date(s) 05/24/2001	£-	
X Assignment Merger		
Security Agreement Change of Name	City: Santa Clara	
☐ Joint Research Agreement	State: California	
Government Interest Assignment	Country: United StatesZip: 95054	
Executive Order 9424, Confirmatory License	Country. Officed States 2ip. 93034	
Other	Additional name(s) & address(es) attached? Yes X No	
4. Application or patent number(s):	document is being filed together with a new application.	
A. Patent Application No.(s)	B. Patent No.(s)	
To Be Assigned		
Additional numbers at	tached? Yes $\overline{X}$ No	
5. Name and address to whom correspondence	6. Total number of applications and patents	
concerning document should be mailed:	involved: 1	
Name: Sterne, Kessler, Goldstein & Fox P.L.L.C.	7. Total fee (37 CFR 1.21(h) & 3.41) \$40.00	
Internal Address: <u>c/o Thomas C. Fiala</u>	X Authorized to be charged by credit card	
	Authorized to be charged to deposit account	
Street Address: 1100 New York Avenue, N.W.	Enclosed	
Sheet Address. 1100 New 101k Avenue, N. W.	None required (government interest not affecting title)	
City Washington	8. Payment Information	
City: Washington	a. Credit Card Last 4 Numbers 1005	
State: <u>D.C.</u> Zip: <u>20005-3934</u>	Expiration Date 08/2009	
Phone Number: <u>202-371-2600</u>	b. Deposit Account Number 19-0036	
Fax Number: <u>202-371-2540</u>		
Email Address: tfiala@skgf.com	Authorized User Name SKGF PLLC	
9. Signature:	January 9, 2007	
Signature	Date	
Thomas C. Fiala	Total number of pages including cover	
Name of Person Signing	sheet, attachments, and documents: 5	

Documents to be recorded (including cover sheet) should be faxed to (571) 273-0140, or mailed to:
Mail Stop Assignment Recordation Services, Director of the USPTO, P.O.Box 1450, Alexandria, V.A. 22313-1450

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408 919 6967;

CONFIDENTIAL - Subject to Nondisclosure Agreement

Sent By: Transmeta Corporation:

ASSIGNMENT OF PATENTS

Whereas, SEIKO EPSON CORPORATION, a Japanese corporation having

its principal place of business at 3-5, Owa 3-Chome, Suwa-shi, Nagano-ken,

392-8502 Japan, (hereinafter referred to as "Assignor"), is the assignee of record of

certain U.S. patents and the foreign counterparts to such patents and any

continuations, divisions, reissues, reexaminations of such patents (hereinafter

referred to as the "Patents"); such patents being listed in Exhibit A attached hereto

and incorporated herein by reference thereto;

Whereas, TRANSMETA CORPORATION, a Delaware corporation, having its

principal place of business at 3940 Freedom Circle, Santa Clara, California 95054

U.S.A., (hereinafter referred to as "Assignee"), is desirous of acquiring the entire

right, title and interest in the Patents;

Now, therefore, in consideration of good and valuable consideration, receipt of

which is hereby acknowledged, SEIKO EPSON CORPORATION, the Assignor and

owner of the entire unencumbered right, title and interest in said Patents, does

hereby sell, assign and transfer unto said Assignee the entire right, title and

interest in and to the Patents aforesaid along with any past claims for infringement

thereof and any divisions, continuations, and foreign counterparts thereof; the same

to be held and enjoyed by the said Assignee for its own use and benefit, and for its

legal representatives and assigns, to the full ends of the terms for which said

Final Agreement

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PAGE 215 \* RCVD AT 1218/2003 11:34:20 PM [Eastern Standard Time] \* SVR:NT-RIGHTFAX/1 \* DNIS:2 \* CSID:408 919 6967 \* DURATION (mm-ss):01-20

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Patents are granted, as fully and entirely as the same would have been held by Assignor had this assignment and sale not been made.

Executed this 24 day of May , 2001, at Suwa of Japan .

SEIKO EPSON CORPORATION (Assignor)

Attachment

ุ่ธ๊ent ซึ่ง: Transmeta Corporation :

Final Agreement

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PAGE 3/5 \* RCVD AT 12/8/2003 11:34:20 PM [Eastern Standard Time] \* SVR:NT-RIGHTFAX/1 \* DNIS:2 \* CSID:408 919 6967 \* DURATION (mm-ss):01-20

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#### Exhibit A

### List of Patents and Patent Applications

Family 1: RISC microprocessor architecture implementing multiple typed register sets

U.S. Patent No. 6,044,449

U.S. Patent No. 5,838,986

U.S. Patent No. 5,682,546

U.S. Patent No. 5,560,035

U.S. Patent No. 5,493,687

U.S. Patent Application No. 09/480,136

U.S. Patent Application No. 09/840,026

European Patent No. 547216

Great Britain Patent No. 547216

German Patent No. 69230057.0

French Patent No. 547216

Hong Kong Patent Application No. 99104718.5

European Patent Application No. 99102406.8

Korean Patent No. 0294964

JP Application No. 93/502403

Family 2: Superscalar RISC instruction scheduling

U.S. Patent No. 5,974,526

U.S. Patent No. 5,737,624

U.S. Patent No. 5,497,499

U.S. Patent Application No. 09/329,352 (this application is being abandoned and a new continuation application is scheduled to be filed before July 2, 2001)

U.S. Patent Application No. 09/329,354

European Patent No. 636256

Great Britain Patent No. 636256

German Patent No. 69311330.8

French Patent No. 636256

Korean Patent No. 0294277

Korean Patent Application No. 10-2000-7014693

Korean Patent Application No. 10-2000-7014694

JP Application No. 93/517293

JP Application No. 2000/8149

JP Application No. 2000/8148

JP Application No. 2000/8147

JP Application No. 2000/8146

JP Application No. 2000/8145

JP Application No. 2000/8144

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Family 3: Superscalar microprocessor for out-of-order and concurrently executing at least two RISC instructions translating from in-order CISC instructions

U.S. Patent No. 5,983,334

U.S. Patent No. 5,619,666

U.S. Patent No. 5,546,552

U.S. Patent No. 5,438,668

U.S. Patent Application No. 09/401,860

U.S. Patent Application (a new continuation of allowed U.S. Patent Application No. 09/401,860)

filed on May 10, 2001 but no serial number yet received from the PTO

European Patent No. 636257

Great Britain Patent No. 636257

German Patent No. 69329644.5

French Patent No. 636257

Hong Kong Patent Application No. 98116064.0

European Patent Application No. 00108579.4

Korean Patent Application No. 10-1994-0703361

Korean Patent Application No. 10-2001-7005744

JP Application No. 93/517306

JP Application No. 2000/7265

JP Application No. 2000/7264

JP Application No. 2000/7263

JP Application No. 2000/7262

JP Application No. 2000/7261

JP Application No. 2000/7260

JP Application No. 2000/7259

JP Application No. 2000/7258