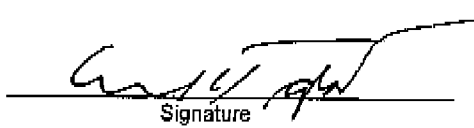


| Recordation Form Cover Sheet PATENTS ONLY | | Attorney Docket No.: <u>042390.P23850</u> |
|--|---|--|
| To the Director of the United States Patent and Trademark Office, Please record the attached original documents or copy thereof. | | |
| 1. Name of conveying party(ies): <u>Zarlink Semiconductor Limited</u> Additional name(s) of conveying party(ies) attached? <input checked="" type="checkbox"/> No <input type="checkbox"/> Yes | 2. Name and address of receiving party(ies): Name: <u>Intel Corporation</u> Internal Address: _____ Street Address: <u>2200 Mission College Blvd.</u> City: <u>Santa Clara</u> State/Province: <u>CA</u> Zip: <u>95052-4040</u> Country: <u>U.S.A.</u> Additional name(s) & address(es) attached? Yes <input type="checkbox"/> No <input checked="" type="checkbox"/> | |
| 3. Nature of Conveyance <input checked="" type="checkbox"/> Assignment <input type="checkbox"/> Merger <input type="checkbox"/> Security Agreement <input type="checkbox"/> Change of Name <input type="checkbox"/> Other: Execution Date(s): <u>July 14th, 2006</u> | 4. Application number(s) or patent number(s): <input type="checkbox"/> If this document is being filed together with a new application, the execution date of the application is: _____ A. Patent Application No.(s) <u>07/010,930</u> B. Patent No.(s) <u>4,833,344</u> Additional numbers attached? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No | |
| 5. Name and address of party to whom correspondence concerning document should be mailed: Name: <u>Blakely, Sokoloff, Taylor & Zafman LLP</u> Internal Address: _____ Street Address: <u>12400 Wilshire Boulevard, 7th Floor</u> <u>Los Angeles, California 90025</u> | 6. Total number of applications and patents involved: <u>1</u> 7. Total Fee (37 CFR 3.41).....\$ <u>40.00</u> <input type="checkbox"/> Enclosed <input checked="" type="checkbox"/> Authorized to be charged to deposit account 8. Deposit Account Number: <u>02-2666</u> (Attach duplicate copy of this page if paying by deposit account) | |
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| 9. Statement and signature. <i>To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.</i> | | |
| <u>Edwin H. Taylor Reg. No. 25,129</u> Name of Person Signing |  Signature | <u>01/18/07</u> Date |
| Total number of pages including cover sheet, attachments, and documents: <u>6</u> | | |

Mail documents to be recorded with required cover sheet information to:
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Based on Form PTO-1595 as modified by BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP on 05/09/03

CONFIRMATORY PATENT ASSIGNMENT

This Confirmatory Patent Assignment Agreement, dated July 14, 2006, is made by Zarlink Semiconductor Limited, a corporation incorporated under the laws of England and Wales ("Assignor"), to Intel Corporation, a Delaware corporation ("Assignee").

RECITALS

A. Assignor and Assignee and other parties have entered into an asset purchase agreement dated as of October 7, 2005, (the "Agreement"). All capitalized terms used herein but not otherwise defined shall have the meanings set forth in the Agreement.

B. Pursuant to the Agreement, Assignor has previously assigned to the Assignee all of Assignor's right, title and interest in and to the patents set forth on Exhibit A hereto (the "Patents").

AGREEMENT

NOW, THEREFORE, in consideration of the foregoing premises, the mutual covenants and agreements contained in the Agreement and the covenants and agreements in this Assignment, Assignor hereby confirms as follows:

1. For good and valuable consideration, the receipt of which is hereby acknowledged, Assignor has, on October 7, 2005, pursuant to the terms of the Agreement, sold, transferred, conveyed, assigned and delivered to Assignee, with full title guarantee all of Assignor's right, title and interest in and to the Patents, including any foreign counterparts, divisions, continuations or reissues thereof, the same to be held by Assignee for Assignee's own use and enjoyment, and for the use and enjoyment of Assignee's successors, assigns and other legal representatives, as fully and entirely as the same would have been held and enjoyed by Assignor if this Assignment and sale had not been made; together with all claims for damages by reason of past infringements of the Patents, along with the right to sue for and collect such damages for the use and benefit of Assignee and its successors, assigns and other legal representatives.

2. Assignor hereby authorizes and requests the Assistant Commissioner of Patents of the United States, and any officer of any country or countries foreign to the United States, whose duty it is to issue patents or other evidence or forms of intellectual property protection or applications as aforesaid, to issue the same to Assignee and its successors, assigns and other legal representatives in accordance with the terms of this instrument.

IN WITNESS WHEREOF, Assignor has executed this Assignment on the date first above written.

Assignor:

ZARLINK SEMICONDUCTOR LIMITED

By:  

Name: Scott Milligan Don McIntyre

Title: Director Director

[SIGNATURE PAGE TO ZARLINK CONFIRMATORY PATENT ASSIGNMENT]

Zarlink Confirmatory Patent Assignment

MIL_LAW #1824222 v. 2

PATENT

REEL: 018787 FRAME: 0818

Acknowledgment by Notary PublicState of n/aCounty of n/a

On this 14th day of July, 2006, before me, the undersigned Notary Public, personally appeared Don McIntyre and Scott Milligan personally known to me (or proved to me on the basis of satisfactory evidence) to be the person whose name is subscribed to the within instrument, and acknowledged to me that he or she executed the same.

Seal:

Signature: 

Name: Renato Pontello-Concina Notary Public
in and for the Province of Ontario, Canada

Zarlink Confirmatory Patent Assignment

EXHIBIT A

| | | | | | | |
|--|-----------------------------|----|------------|--------|---------|------------|
| has current circuit, and decade and ring circuits incorporating same | Phil Moon Simon Atkinson | US | 05/02/1987 | 010930 | 4833344 | 23/05/1989 |
|--|-----------------------------|----|------------|--------|---------|------------|

**BLAKELY
SOKOLOFF
TAYLOR &
ZAFMAN**

1279 OAKMEAD PARKWAY
SUNNYVALE, CALIFORNIA 94085
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(408) 720-8383 (Facsimile)

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Fax Number: (571) 273-0140 Telephone No.: (571) 272-3350
From: Edwin H. Taylor
Date: 01/18/07 Time: _____
Operator: Mike Mendoza Matter: 042390.P23850
Number of pages including cover sheet: 6
In Re Patent Application of: Intel Corporation
Application.: 07/010,930
Filed: February 5, 1987
For: LOW VOLTAGE BIAS CIRCUIT

Enclosed are the following documents

- Recordation Form Cover Sheet
- Assignment document from Zarlink Semiconductor Limited to Intel Corporation

CERTIFICATE OF TRANSMISSION

I hereby certify that this correspondence is being facsimile transmitted to the U.S. Patent and Trademark Office on:

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PATENT 05/01/03

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