Form <b>PTO-159</b> 5 (Rev. 07/05) OMB No. 0651-0027 (exp. 6/30/2008)	U.S. DEPARTMENT OF COMMERCE United States Patent and Trademark Officer			
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Name of conveying party(ies)	2. Name and address of receiving party(ies)			
APD Semiconductor, Inc.	Name: Diodes, Inc.			
	Internal Address:			
Additional name(s) of conveying party(ies) attached? Yes No.  3. Nature of conveyance/Execution Date(s):  Execution Date(s) 10/31/06  Assignment Merger  Security Agreement Change of Name  Joint Research Agreement  Government Interest Assignment  Executive Order 9424, Confirmatory License  Other  4. Application or patent number(s): This  A. Patent Application No.(s)	Street Address: 3050 E. Hillcrest Drive  City: Westlake Village  State: California  Country: U.S Zip:91362  Additional name(s) & address(es) attached? Yes No document is being filed together with a new application.  B. Patent No.(s) 6,537,860			
Additional numbers at 5. Name and address to whom correspondence concerning document should be mailed:	teched? Yes No  6. Total number of applications and patents involved; 1			
Name:Courtney Staniford & Gregory LLP				
Internal Address: P.O. Box 9686	7. Total fee (37 CFR 1.21(h) & 3.41) \$ 40.00 Authorized to be charged by credit card			
Street Address:	Authorized to be charged by credit card  Authorized to be charged to deposit account  Enclosed  None required (government interest not affecting title)			
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9. Signature:  Signature  Signature  Barbara B. Courtney  Name of Person Signing	にー  ス  3  -  え  の  で  7  Date  Total number of pages including cover sheet, attachments, and documents: 4			

Documents to be recorded (including cover sheet) should be faxed to (571) 273-0140, or mailed to: Mail Stop Assignment Recordation Services, Director of the USPTO, P.O.Box 1450, Alexandria, V.A. 22313-1450

Attorney Docket No.: CO 1

Patent

## **ASSIGNMENT**

In consideration of good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, <u>APD Semiconductor</u>, <u>Inc.</u>, the undersigned Assignor, having a principal place of business at 1 Lagoon Drive, Suite 410, Redwood City, CA 94065, believing to be the current co-assignee (with Fijifilm Microdevices Co. Ltd.) of the entire right, title, and interest in **United States Patent Number 6,537,860**, titled

## METHOD OF FABRICATING POWER VLSI DIODE DEVICES

hereby sells, assigns, and transfers to Diodes Incorporated, a Delaware corporation, and DII Talwan Corporation Ltd., a Taiwan corporation (collectively referred to herein as "Assignee"), and its successors, assigns, and legal representatives, Assignor's entire right, title, and interest in and to any and all improvements that are disclosed in the same United States Patent.

The undersigned further sells, assigns, and transfers to Assignee said United States Patent and all divisional applications and patents, continuation applications and patents, continued prosecution applications and patents, continuation-in-part applications and patents, substitute applications and patents, renewal applications and patents, reissue applications and patents, reexamination applications and patents, extensions, and all other patent applications and patents that have been or shall be filed in the United States and all foreign countries on any of said improvements; and in and to all original patents, reissued patents, reexamination certificates, and extensions, that have been or shall be issued in the United States and all foreign countries on said improvements; and in and to all rights of priority resulting from the filing of said United States application; and the right to file foreign applications directly in the name of the Assignee and to claim for any such foreign applications any priority rights to which such applications are entitled under international conventions, treaties or otherwise.

The undersigned further agrees that said Assignee may apply for and receive a patent or patents for said improvements in its own name; and that, when requested, without charge to, but at the expense of, said Assignee, its successors, assigns, and legal representatives, to carry out in good faith the intent and purpose of this Assignment, the undersigned will execute all divisional applications, continuation applications, continuation applications, continuation-in-part applications, substitute applications, renewal applications, reissue applications, reexaminations, extensions and all other patent applications on any and all said improvements; execute all rightful oaths, assignments, powers of attorney, and other papers; communicate to said Assignee, its successors, assigns, and representatives all facts known to the undersigned relating to said improvements and the history thereof; and generally assist said Assignee, its successors, assigns, or representatives in securing and maintaining proper patent protection for said improvements and for vesting title to said improvements, and all applications for patents and all patents on said improvements, in said Assignee, its successors, assigns, and legal representatives.

The undersigned covenants with said Assignee, its successors, assigns, and legal representatives that no assignment, grant, mortgage, license, or other agreement affecting the rights and property

- 1 -

PATENT REEL: 018797 FRAME: 0783 Attorney Docket No.: CO 1

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on behalf of APD Semiconductor, Inc.

**RECORDED: 01/23/2007** 

Patent

herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned.

The undersigned hereby grants to the practitioners at Customer Number 53186, the Customer Number of Courtney Staniford & Gregory LLP, the power to insert on this document any further identification necessary or desirable to comply with the rules of the United States Patent and Trademark Office for recordation of this Assignment.

Assignor Signature:Da	ate:	<u>Od</u>	<u>7/,</u>	201	6
Print name and title <u>David Chow, Director</u>					

**PATENT REEL: 018797 FRAME: 0784**