

PATENT ASSIGNMENT

Electronic Version v1.1

Stylesheet Version v1.1

SUBMISSION TYPE:

NEW ASSIGNMENT

NATURE OF CONVEYANCE:

Security Agreement

CONVEYING PARTY DATA

Name	Execution Date
Freescale Semiconductor, Inc.	12/01/2006
Freescale Acquisition Corporation	12/01/2006
Freescale Acquisition Holdings Corp.	12/01/2006
Freescale Holdings (Bermuda) III, Ltd.	12/01/2006

RECEIVING PARTY DATA

Name:	CITIBANK, N.A. as Collateral Agent
Street Address:	390 Greenwich Street
City:	New York
State/Country:	NEW YORK
Postal Code:	10013

PROPERTY NUMBERS Total: 5512

Property Type	Number
Patent Number:	4698128
Patent Number:	4723363
Patent Number:	4728391
Patent Number:	4728606
Patent Number:	4730127
Patent Number:	4737468
Patent Number:	4737732
Patent Number:	4739192
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Patent Number:	4745370

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REEL: 018855 FRAME: 0129

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ATTORNEY DOCKET NUMBER:	376679
NAME OF SUBMITTER:	Oleh Hereliuk

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INTELLECTUAL PROPERTY SECURITY AGREEMENT

dated as of

December 1, 2006

among

FREESCALE ACQUISITION CORPORATION,
as Borrower (prior to the Merger)

FREESCALE SEMICONDUCTOR, INC.,
as Borrower (after the Merger)

FREESCALE ACQUISITION HOLDINGS CORP.,
as Holdings

CERTAIN SUBSIDIARIES OF FREESCALE HOLDINGS (BERMUDA) III, LTD.

IDENTIFIED HEREIN

and

CITIBANK, N.A.,
as Collateral Agent

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Exhibits

Exhibit I Form of Supplement

INTELLECTUAL PROPERTY SECURITY AGREEMENT dated as of December 1, 2006, among FREESCALE ACQUISITION CORPORATION ("Merger Sub" and, prior to the Merger, the "Borrower"), a Delaware corporation to be merged with and into FREESCALE SEMICONDUCTOR, INC., a Delaware corporation ("Freescale" and, after the Merger, the "Borrower"), FREESCALE ACQUISITION HOLDINGS CORP., a Delaware corporation ("Holdings"), the Subsidiaries of FREESCALE HOLDINGS (BERMUDA) III, LTD., a Bermuda corporation ("Parent"), from time to time party hereto and CITIBANK, N.A., as Collateral Agent.

Reference is made to the Credit Agreement dated as of December 1, 2006 (as amended, supplemented or otherwise modified from time to time, the "Credit Agreement"), among the Borrower, Holdings, Parent, Citibank, N.A., as Administrative Agent, Swing Line Lender and an L/C Issuer, and each lender from time to time party thereto (collectively, the "Lenders" and individually, a "Lender"). The Lenders have agreed to extend credit to the Borrower subject to the terms and conditions set forth in the Credit Agreement. The obligations of the Lenders to extend such credit are conditioned upon, among other things, the execution and delivery of this Agreement. Holdings is an affiliate of the Borrower, will derive substantial benefits from the extension of credit to the Borrower pursuant to the Credit Agreement and are willing to execute and deliver this Agreement in order to induce the Lenders to extend such credit. Accordingly, the parties hereto agree as follows:

ARTICLE I

DEFINITIONS

SECTION 1.01. *Credit Agreement.*

(a) Capitalized terms used in this Agreement and not otherwise defined herein have the meanings specified in the Credit Agreement. All terms defined in the New York UCC (as defined herein) and not defined in this Agreement have the meanings specified therein; the term "instrument" shall have the meaning specified in Article 9 of the New York UCC.

(b) The rules of construction specified in Article I of the Credit Agreement also apply to this Agreement.

SECTION 1.02. *Other Defined Terms.* As used in this Agreement, the following terms have the meanings specified below:

"Agreement" means this Intellectual Property Security Agreement.

"Borrower" has the meaning assigned to such term in the preliminary statement of this Agreement.

"Claiming Party" has the meaning assigned to such term in Section 4.02.

"Collateral" has the meaning assigned to such term in Section 2.01.

“Collateral Agent” has the meaning assigned to such term in the preliminary statement of this Agreement.

“Contributing Party” has the meaning assigned to such term in Section 4.02.

“Copyright License” means any written agreement, now or hereafter in effect, granting any right to any third party under any copyright now or hereafter owned by any Grantor or that such Grantor otherwise has the right to license, or granting any right to any Grantor under any copyright now or hereafter owned by any third party, and all rights of such Grantor under any such agreement.

“Copyrights” means all of the following now owned or hereafter acquired by any Grantor: (a) all copyright rights in any work subject to the copyright laws of the United States or any other country, whether as author, assignee, transferee or otherwise, and (b) all registrations and applications for registration of any such copyright in the United States or any other country, including registrations, recordings, supplemental registrations and pending applications for registration in the United States Copyright Office, including those listed on Schedule I.

“Credit Agreement” has the meaning assigned to such term in the preliminary statement of this Agreement.

“Grantor” means each of Holdings, Merger Sub, Freescale and each Restricted Subsidiary that is a Material Domestic Subsidiary.

“Holdings” has the meaning assigned to such term in the preliminary statement of this Agreement.

“Intellectual Property” means all intellectual and similar property of every kind and nature now owned or hereafter acquired by any Grantor, including inventions, designs, Patents, Copyrights, Licenses, Trademarks, trade secrets, confidential or proprietary technical and business information, know-how, show-how or other data or information, the intellectual property rights in software and databases and related documentation, and all additions, improvements and accessions to, and books and records describing any of the foregoing.

“Intellectual Property Security Agreement Supplement” means an instrument in the form of Exhibit I hereto.

“Lender” has the meaning assigned to such term in the preliminary statement of this Agreement.

“License” means any Patent License, Trademark License, Copyright License or other Intellectual Property license or sublicense agreement to which any Grantor is a party.

“New York UCC” means the Uniform Commercial Code as from time to time in effect in the State of New York.

“Parent” has the meaning assigned to such term in the preliminary statement of this Agreement.

“Patent License” means any written agreement, now or hereafter in effect, granting to any third party any right to make, use or sell any invention on which a patent, now or hereafter owned by any Grantor or that any Grantor otherwise has the right to license, is in existence, or granting to any Grantor any right to make, use or sell any invention on which a patent, now or hereafter owned by any third party, is in existence, and all rights of any Grantor under any such agreement.

“Patents” means all of the following now owned or hereafter acquired by any Grantor: (a) all letters patent of the United States or the equivalent thereof in any other country, all registrations and recordings thereof, and all applications for letters patent of the United States or the equivalent thereof in any other country, including registrations, recordings and pending applications in the United States Patent and Trademark Office or any similar offices in any other country, including those listed on Schedule I, and (b) all reissues, continuations, divisions, continuations-in-part, renewals or extensions thereof, and the inventions disclosed or claimed therein, including the right to make, use and/or sell the inventions disclosed or claimed therein.

“Perfection Certificate” means a certificate substantially in the form of Exhibit II to the Security Agreement, completed and supplemented with the schedules and attachments contemplated thereby, and duly executed by the chief financial officer and the chief legal officer of each of Parent, Holdings and the Borrower.

“Proceeds” has the meaning specified in Section 9-102 of the New York UCC.

“Security Interest” has the meaning assigned to such term in Section 2.01(a).

“Trademark License” means any written agreement, now or hereafter in effect, granting to any third party any right to use any trademark now or hereafter owned by any Grantor or that any Grantor otherwise has the right to license, or granting to any Grantor any right to use any trademark now or hereafter owned by any third party, and all rights of any Grantor under any such agreement.

“Trademarks” means all of the following now owned or hereafter acquired by any Grantor: (a) all trademarks, service marks, trade names, corporate names, company names, business names, fictitious business names, trade styles, trade dress, logos, other source or business identifiers, designs and general intangibles of like nature, now existing or hereafter adopted or acquired, all registrations and recordings thereof, and all registration and recording applications filed in connection therewith, including registrations and registration applications in the United States Patent and Trademark Office or any similar offices in any State of the United States or any other country or any political subdivision thereof, and all extensions or renewals thereof, including those listed on Schedule I, (b) all goodwill connected with the use of and symbolized thereby and (c) all other assets, rights and interests that uniquely reflect or embody such goodwill.

ARTICLE II

SECURITY INTERESTS

SECTION 2.01. *Security Interest.* (a) As security for the payment or performance, as the case may be, in full of the Obligations, including the Guarantees, each Grantor hereby assigns and pledges to the Collateral Agent, its successors and assigns, for the benefit of the Secured Parties, and hereby grants to the Collateral Agent, its successors and assigns, for the benefit of the Secured Parties, a security interest (the “Security Interest”) in all right, title or interest in or to any and all of the following assets and properties now owned or at any time hereafter acquired by such Grantor or in which such Grantor now has or at any time in the future may acquire any right, title or interest (collectively, the “Collateral”):

(i) all Copyrights;

(ii) all Patents;

(iii) all Trademarks;

(iv) all Licenses;

(v) all other Intellectual Property; and

(vi) all Proceeds and products of any and all of the foregoing and all collateral security and guarantees given by any Person with respect to any of the foregoing.

provided, however, that notwithstanding any of the other provisions herein (and notwithstanding any recording of the Collateral Agent’s Lien made in the U.S. Patent and Trademark Office, U.S. Copyright Office, or other IP registry office), this Agreement shall not constitute a grant of a security interest in any property to the extent that such grant of a security interest is prohibited by any rule of law, statute or regulation or is prohibited by, or constitutes a breach or default under or results in the termination of any contract, license, agreement, instrument or other document evidencing or giving rise to such property, or would result in the forfeiture of the Grantors’ rights in the property including, without limitation, any Trademark applications filed in the United States Patent and Trademark Office on the basis of such Grantor’s “intent-to-use” such trademark, unless and until acceptable evidence of use of the Trademark has been filed with the United States Patent and Trademark Office pursuant to Section 1(c) or Section 1(d) of the Lanham Act (15 U.S.C. 1051, et seq.), to the extent that granting a lien in such Trademark application prior to such filing would adversely affect the enforceability or validity of such Trademark application.

(b) Each Grantor hereby irrevocably authorizes the Collateral Agent for the benefit of the Secured Parties at any time and from time to time to file in any relevant jurisdiction any initial financing statements with respect to the Collateral or any part thereof and amendments thereto that contain the information required by Article 9 of the Uniform Commercial Code or the analogous legislation of each applicable jurisdiction for the filing of any

financing statement or amendment, including whether such Grantor is an organization, the type of organization and any organizational identification number issued to such Grantor. Each Grantor agrees to provide such information to the Collateral Agent promptly upon request.

The Collateral Agent is further authorized to file with the United States Patent and Trademark Office or United States Copyright Office (or any successor office or any similar office in any other country) such documents as may be necessary or advisable for the purpose of perfecting, confirming, continuing, enforcing or protecting the Security Interest granted by each Grantor, without the signature of any Grantor, and naming any Grantor or the Grantors as debtors and the Collateral Agent as secured party.

(c) The Security Interest is granted as security only and shall not subject the Collateral Agent or any other Secured Party to, or in any way alter or modify, any obligation or liability of any Grantor with respect to or arising out of the Collateral.

SECTION 2.02. *Representations and Warranties.* Holdings and the Borrower jointly and severally represent and warrant, as to themselves and the other Grantors, to the Collateral Agent and the other Secured Parties that:

(a) Except as would not be expected to have a Material Adverse Effect, each Grantor has good and valid rights in and title to (or with respect to the Patents set forth on Schedule II hereto, a joint ownership interest in) the Collateral with respect to which it has purported to grant a Security Interest hereunder and has full power and authority to grant to the Collateral Agent the Security Interest in such Collateral pursuant hereto and to execute, deliver and perform its obligations in accordance with the terms of this Agreement, without the consent or approval of any other Person other than any consent or approval that has been obtained.

(i) The Perfection Certificate has been duly prepared, completed and executed and the information set forth therein, including the exact legal name of each Grantor, is correct and complete in all material respects as of the Closing Date.

(ii) The Uniform Commercial Code financing statements (including fixture filings, as applicable) or other appropriate filings, recordings or registrations prepared by the Collateral Agent based upon the information provided to the Collateral Agent in the Perfection Certificate for filing in each governmental, municipal or other office specified in Schedule 6 to the Perfection Certificate (or specified by notice from the Borrower to the Collateral Agent after the Closing Date in the case of filings, recordings or registrations required by Section 6.11 of the Credit Agreement), are all the filings, recordings and registrations (other than filings required to be made in the United States Patent and Trademark Office and the United States Copyright Office in order to perfect the Security Interest in Collateral consisting of United States Patents, Trademarks and Copyrights) that are necessary to establish a valid and perfected security interest in favor of the Collateral Agent (for the benefit of the Secured Parties) in respect of all Collateral in which the Security Interest may be perfected by filing, recording or registration in the United States (or any political subdivision thereof) and its territories and possessions, and no further or subsequent filing, refile, recording, rerecording, registration or

reregistration is necessary in any such jurisdiction, except as provided under applicable law with respect to the filing of continuation statements.

(iii) Each Grantor represents and warrants that a fully executed agreement in the form hereof and containing a description of all Collateral consisting of United States Patents and United States registered Trademarks (and Trademarks for which United States registration applications are pending) and United States registered Copyrights have been delivered to the Collateral Agent for recording by the United States Patent and Trademark Office and the United States Copyright Office pursuant to 35 U.S.C. § 261, 15 U.S.C. § 1060 or 17 U.S.C. § 205 and the regulations thereunder, as applicable, and otherwise as may be required pursuant to the laws of any other necessary jurisdiction, to establish a valid and perfected security interest in favor of the Collateral Agent (for the benefit of the Secured Parties) in respect of all Collateral consisting of Patents, Trademarks and Copyrights in which a security interest may be perfected by filing, recording or registration in the United States (or any political subdivision thereof) and its territories and possessions under the Federal intellectual property laws, and no further or subsequent filing, refile, recording, rerecording, registration or reregistration is necessary (other than such filings and actions as are necessary to perfect the Security Interest with respect to (i) any Collateral consisting of Patents, Trademarks and Copyrights (or registration or application for registration thereof) acquired or developed by any Grantor after the date hereof), (ii) as maybe required under the laws of jurisdictions outside the United States with respect to Collateral created under such laws, and (iii) the Uniform Commercial Code financing and continuation statements contemplated in subsection (i) of this Section 2.02(a).

(b) The Security Interest constitutes (i) a valid security interest in all the Collateral securing the payment and performance of the Obligations, including the Guarantees, (ii) subject to the filings described in Section 2.02(b), a perfected security interest in all Collateral in which a security interest may be perfected by filing, recording or registering a financing statement or analogous document in the United States (or any political subdivision thereof) and its territories and possessions pursuant to the Uniform Commercial Code and (iii) a security interest that shall be perfected in all Collateral in which a security interest may be perfected upon the receipt and recording of this Agreement (or a fully executed short form agreement in form and substance reasonably satisfactory to the Collateral Agent and the Borrower) with the United States Patent and Trademark Office and the United States Copyright Office, as applicable, within the three-month period (commencing as of the date hereof) pursuant to 35 U.S.C. § 261 or 15 U.S.C. § 1060 or the one-month period (commencing as of the date hereof) pursuant to 17 U.S.C. § 205 and otherwise as may be required pursuant to the laws of any other necessary jurisdiction. The Security Interest is and shall be prior to any other Lien on any of the Collateral, other than (i) any nonconsensual Lien that is expressly permitted pursuant to Section 7.01 of the Credit Agreement and has priority as a matter of law and (ii) Liens expressly permitted pursuant to Section 7.01 of the Credit Agreement.

(c) The Collateral, which is purported to be owned in whole or in part by the Grantors, is owned by the Grantors free and clear of any Lien, except for Liens expressly permitted pursuant to Section 7.01 of the Credit Agreement. None of the Grantors has filed or consented to the filing of (i) any financing statement or analogous document under the Uniform

Commercial Code or any other applicable laws covering any Collateral, (ii) any assignment in which any Grantor assigns any Collateral or any security agreement or similar instrument covering any Collateral with the United States Patent and Trademark Office or the United States Copyright Office or (iii) any assignment in which any Grantor assigns any Collateral or any security agreement or similar instrument covering any Collateral with any foreign governmental, municipal or other office, which financing statement or analogous document, assignment, security agreement or similar instrument is still in effect, except, in each case, for Liens expressly permitted pursuant to Section 7.01 of the Credit Agreement and Liens that are no longer effective.

SECTION 2.03. *Covenants.*

(a) The Borrower agrees promptly to notify the Collateral Agent in writing of any change (i) in legal name of any Grantor, (ii) in the identity or type of organization or corporate structure of any Grantor, or (iii) in the jurisdiction of organization of any Grantor.

(b) Each Grantor shall, at its own expense, take any and all commercially reasonable actions necessary to defend title to the Collateral against all Persons and to defend the Security Interest of the Collateral Agent in the Collateral and the priority thereof against any Lien not expressly permitted pursuant to Section 7.01 of the Credit Agreement.

(c) Each year, at the time of delivery of annual financial statements with respect to the preceding fiscal year pursuant to Section 6.01 of the Credit Agreement, the Borrower shall deliver to the Collateral Agent a certificate executed by the chief financial officer and the chief legal officer of the Borrower setting forth the information required pursuant to Sections 1(a), 1(c), 1(d), 2(b) and 12 of the Perfection Certificate or confirming that there has been no change in such information since the date of such certificate or the date of the most recent certificate delivered pursuant to this Section 2.03(c).

(d) The Borrower agrees, on its own behalf and on behalf of each other Grantor, at its own expense, to execute, acknowledge, deliver and cause to be duly filed all such further instruments and documents and take all such actions as the Collateral Agent may from time to time reasonably request to better assure, preserve, protect and perfect the Security Interest and the rights and remedies created hereby, including the payment of any fees and taxes required in connection with the execution and delivery of this Agreement, the granting of the Security Interest and the filing of any financing statements or other documents in connection herewith or therewith. If any amount payable under or in connection with any of the Collateral that is in excess of \$5,000,000 shall be or become evidenced by any promissory note or other instrument, such note or instrument shall be promptly pledged and delivered to the Collateral Agent, for the benefit of the Secured Parties, duly endorsed in a manner reasonably satisfactory to the Collateral Agent.

Without limiting the generality of the foregoing, each Grantor hereby authorizes the Collateral Agent, with prompt notice thereof to the Grantors, to supplement this Agreement by supplementing Schedule I or adding additional schedules hereto to specifically identify any asset or item that may constitute Copyrights, Patents or Trademarks; *provided* that any Grantor shall have the right, exercisable within 10 days after it has been notified by the Collateral Agent

of the specific identification of such Collateral, to advise the Collateral Agent in writing of any inaccuracy of the representations and warranties made by such Grantor hereunder with respect to such Collateral. Each Grantor agrees that it will use its best efforts to take such action as shall be necessary in order that all representations and warranties hereunder shall be true and correct with respect to such Collateral within 30 days after the date it has been notified by the Collateral Agent of the specific identification of such Collateral.

(e) At its option, the Collateral Agent may discharge past due taxes, assessments, charges, fees, Liens, security interests or other encumbrances at any time levied or placed on the Collateral and not permitted pursuant to Section 7.01 of the Credit Agreement, and may pay for the maintenance and preservation of the Collateral to the extent any Grantor fails to do so as required by the Credit Agreement or this Agreement and within a reasonable period of time after the Collateral Agent has requested that it do so, and each Grantor jointly and severally agrees to reimburse the Collateral Agent within 10 days after demand for any payment made or any reasonable expense incurred by the Collateral Agent pursuant to the foregoing authorization; *provided, however*, Grantors shall not be obligated to reimburse the Collateral Agent with respect to any Intellectual Property Collateral which any Grantor has failed to maintain or pursue, or otherwise allowed to lapse, terminate or be put into the public domain, in accordance with Section 2.04(f). Nothing in this paragraph shall be interpreted as excusing any Grantor from the performance of, or imposing any obligation on the Collateral Agent or any Secured Party to cure or perform, any covenants or other promises of any Grantor with respect to taxes, assessments, charges, fees, Liens, security interests or other encumbrances and maintenance as set forth herein or in the other Loan Documents.

(f) Each Grantor (rather than the Collateral Agent or any Secured Party) shall remain liable (as between itself and any relevant counterparty) to observe and perform all the conditions and obligations to be observed and performed by it under each contract, agreement or instrument relating to the Collateral, all in accordance with the terms and conditions thereof, and each Grantor jointly and severally agrees to indemnify and hold harmless the Collateral Agent and the other Secured Parties from and against any and all liability for such performance.

SECTION 2.04. *Additional Covenants.*

(a) Except to the extent failure to act could not reasonably be expected to have a Material Adverse Effect, with respect to registration or pending application of each item of its Collateral for which such Grantor has standing to do so, each Grantor agrees to take, at its expense, all reasonable steps, including, without limitation, in the U.S. Patent and Trademark Office, the U.S. Copyright Office and any other governmental authority located in the United States, to (i) maintain the validity and enforceability of any registered Collateral (or applications therefor) and maintain such Collateral in full force and effect, and (ii) pursue the registration and maintenance of each Patent, Trademark, or Copyright registration or application, now or hereafter included in such Collateral of such Grantor, including, without limitation, the payment of required fees and taxes, the filing of responses to office actions issued by the U.S. Patent and Trademark Office, the U.S. Copyright Office or other governmental authorities, the filing of applications for renewal or extension, the filing of affidavits under Sections 8 and 15 of the U.S. Trademark Act, the filing of divisional, continuation, continuation-in-part, reissue and renewal

applications or extensions, the payment of maintenance fees and the participation in interference, reexamination, opposition, cancellation, infringement and misappropriation proceedings.

(b) Except as could not reasonably be expected to have a Material Adverse Effect, no Grantor shall do or permit any act or knowingly omit to do any act whereby any of its Collateral may lapse, be terminated, or become invalid or unenforceable or placed in the public domain (or in the case of a trade secret, becomes publicly known).

(c) Except where failure to do so could not reasonably be expected to have a Material Adverse Effect, each Grantor shall take all reasonable steps to preserve and protect each item of its Collateral, including, without limitation, maintaining the quality of any and all products or services used or provided in connection with any of the Trademarks, consistent with the quality of the products and services as of the date hereof, and taking all reasonable steps necessary to ensure that all licensed users of any of the Trademarks abide by the applicable license's terms with respect to the standards of quality.

(d) Each Grantor agrees that, should it obtain an ownership or other interest in any Collateral after the Closing Date ("After-Acquired Intellectual Property") (i) the provisions of this Agreement shall automatically apply thereto, and (ii) any such After-Acquired Intellectual Property shall automatically become part of the Collateral subject to the terms and conditions of this Agreement with respect thereto.

(e) Once every fiscal quarter of the Borrower, with respect to issued or registered Patents (or published applications therefor), Trademarks (or applications therefor), and registered Copyrights, each Grantor shall sign and deliver to the Collateral Agent an appropriate supplemental Intellectual Property Security Agreement with respect to all applicable Intellectual Property owned by it as of the last day of such period, to the extent that such Intellectual Property is not covered by any previous Intellectual Property Security Agreement so signed and delivered by it. In each case, it will promptly cooperate as reasonably necessary to enable the Collateral Agent to make any necessary or reasonably desirable recordings with the U.S. Copyright Office or the U.S. Patent and Trademark Office, as appropriate.

(f) Nothing in this Agreement prevents any Grantor from disposing of, discontinuing the use or maintenance of, failing to pursue, or otherwise allowing to lapse, terminate or be put into the public domain, any of its Collateral to the extent permitted by the Credit Agreement if such Grantor determines in its reasonable business judgment that such discontinuance is desirable in the conduct of its business.

ARTICLE III

REMEDIES

SECTION 3.01. *Remedies Upon Default.* If an Event of Default occurs and is continuing, each Grantor agrees to deliver each item of Collateral to the Collateral Agent on demand, and it is agreed that the Collateral Agent shall have the right, at the same or different times, with respect to any Collateral, on demand, to cause the Security Interest to become an assignment, transfer and conveyance of any of or all such Collateral by the applicable Grantors

to the Collateral Agent, or to license or sublicense, whether general, special or otherwise, and whether on an exclusive or nonexclusive basis, any such Collateral throughout the world on such terms and conditions and in such manner as the Collateral Agent shall determine (other than in violation of any then-existing licensing arrangements to the extent that waivers cannot be obtained), and, generally, to exercise any and all rights afforded to a secured party with respect to the Obligations under the Uniform Commercial Code or other applicable law. Without limiting the generality of the foregoing, each Grantor agrees that the Collateral Agent shall have the right, subject to the mandatory requirements of applicable law and the notice requirements described below, to sell or otherwise dispose of all or any part of the Collateral securing the Obligations at a public or private sale, for cash, upon credit or for future delivery as the Collateral Agent shall deem appropriate. Each such purchaser at any sale of Collateral shall hold the property sold absolutely, free from any claim or right on the part of any Grantor, and each Grantor hereby waives (to the extent permitted by law) all rights of redemption, stay and appraisal which such Grantor now has or may at any time in the future have under any rule of law or statute now existing or hereafter enacted.

The Collateral Agent shall give the applicable Grantors 10 days' written notice (which each Grantor agrees is reasonable notice within the meaning of Section 9-611 of the New York UCC or its equivalent in other jurisdictions) of the Collateral Agent's intention to make any sale of Collateral. Such notice, in the case of a public sale, shall state the time and place for such sale. Any such public sale shall be held at such time or times within ordinary business hours and at such place or places as the Collateral Agent may fix and state in the notice (if any) of such sale. At any such sale, the Collateral, or portion thereof, to be sold may be sold in one lot as an entirety or in separate parcels, as the Collateral Agent may (in its sole and absolute discretion) determine. The Collateral Agent shall not be obligated to make any sale of any Collateral if it shall determine not to do so, regardless of the fact that notice of sale of such Collateral shall have been given. The Collateral Agent may, without notice or publication, adjourn any public or private sale or cause the same to be adjourned from time to time by announcement at the time and place fixed for sale, and such sale may, without further notice, be made at the time and place to which the same was so adjourned. In case any sale of all or any part of the Collateral is made on credit or for future delivery, the Collateral so sold may be retained by the Collateral Agent until the sale price is paid by the purchaser or purchasers thereof, but the Collateral Agent shall not incur any liability in case any such purchaser or purchasers shall fail to take up and pay for the Collateral so sold and, in case of any such failure, such Collateral may be sold again upon like notice. At any public (or, to the extent permitted by law, private) sale made pursuant to this Agreement, any Secured Party may bid for or purchase, free (to the extent permitted by law) from any right of redemption, stay, valuation or appraisal on the part of any Grantor (all said rights being also hereby waived and released to the extent permitted by law), the Collateral or any part thereof offered for sale and may make payment on account thereof by using any claim then due and payable to such Secured Party from any Grantor as a credit against the purchase price, and such Secured Party may, upon compliance with the terms of sale, hold, retain and dispose of such property without further accountability to any Grantor therefor. For purposes hereof, a written agreement to purchase the Collateral or any portion thereof shall be treated as a sale thereof; the Collateral Agent shall be free to carry out such sale pursuant to such agreement and no Grantor shall be entitled to the return of the Collateral or any portion thereof subject thereto, notwithstanding the fact that after the Collateral Agent shall have entered into such an agreement all Events of Default shall have been remedied

and the Obligations paid in full. As an alternative to exercising the power of sale herein conferred upon it, the Collateral Agent may proceed by a suit or suits at law or in equity to foreclose this Agreement and to sell the Collateral or any portion thereof pursuant to a judgment or decree of a court or courts having competent jurisdiction or pursuant to a proceeding by a court-appointed receiver. Any sale pursuant to the provisions of this Section 3.01 shall be deemed to conform to the commercially reasonable standards as provided in Section 9-610(b) of the New York UCC or its equivalent in other jurisdictions.

SECTION 3.02. Application of Proceeds.

(a) The Collateral Agent shall apply the proceeds of any collection or sale of Collateral, including any Collateral consisting of cash, in accordance with Section 8.04 of the Credit Agreement as of the Closing Date.

The Collateral Agent shall have absolute discretion as to the time of application of any such proceeds, moneys or balances in accordance with this Agreement. Upon any sale of Collateral by the Collateral Agent (including pursuant to a power of sale granted by statute or under a judicial proceeding), the receipt of the Collateral Agent or of the officer making the sale shall be a sufficient discharge to the purchaser or purchasers of the Collateral so sold and such purchaser or purchasers shall not be obligated to see to the application of any part of the purchase money paid over to the Collateral Agent or such officer or be answerable in any way for the misapplication thereof.

(b) In making the determinations and allocations required by this Section 3.02, the Collateral Agent may conclusively rely upon information supplied by the Administrative Agent as to the amounts of unpaid principal and interest and other amounts outstanding with respect to the Obligations, and the Collateral Agent shall have no liability to any of the Secured Parties for actions taken in reliance on such information, *provided* that nothing in this sentence shall prevent any Grantor from contesting any amounts claimed by any Secured Party in any information so supplied. All distributions made by the Collateral Agent pursuant to this Section 3.02 shall be (subject to any decree of any court of competent jurisdiction) final (absent manifest error), and the Collateral Agent shall have no duty to inquire as to the application by the Administrative Agent of any amounts distributed to it.

SECTION 3.03. *Grant of License to Use Intellectual Property.* For the purpose of enabling the Collateral Agent to exercise rights and remedies under this Agreement at such time as the Collateral Agent shall be lawfully entitled to exercise such rights and remedies, each Grantor shall, upon request by the Collateral Agent at any time after and during the continuance of an Event of Default, grant to the Collateral Agent an irrevocable (until the termination of the Credit Agreement) nonexclusive license (exercisable without payment of royalty or other compensation to the Grantors) to use, license or sublicense any of the Collateral now owned or hereafter acquired by such Grantor, and wherever the same may be located, and including in such license reasonable access to all media in which any of the licensed items may be recorded or stored and to all computer software and programs used for the compilation or printout thereof; *provided, however*, that nothing in this Section 3.03 shall require Grantors to grant any license that is prohibited by any rule of law, statute or regulation or is prohibited by, or constitutes a breach or default under or results in the termination of any contract, license, agreement,

instrument or other document evidencing, giving rise to or theretofore granted, to the extent permitted by the Credit Agreement, with respect to such property; *provided, further*, that such licenses to be granted hereunder with respect to Trademarks shall be subject to the maintenance of quality standards with respect to the goods and services on which such Trademarks are used sufficient to preserve the validity of such Trademarks. The use of such license by the Collateral Agent may be exercised, at the option of the Collateral Agent, during the continuation of an Event of Default; *provided* that any license, sublicense or other transaction entered into by the Collateral Agent in accordance herewith shall be binding upon the Grantors notwithstanding any subsequent cure of an Event of Default.

ARTICLE IV

INDEMNITY, SUBROGATION AND SUBORDINATION

SECTION 4.01. *Indemnity*. In addition to all such rights of indemnity and subrogation as the Grantors may have under applicable law (but subject to Section 4.03), the Borrower agrees that, in the event any assets of any Grantor shall be sold pursuant to this Agreement or any other Collateral Document to satisfy in whole or in part an Obligation owed to any Secured Party, the Borrower shall indemnify such Grantor in an amount equal to the greater of the book value or the fair market value of the assets so sold.

SECTION 4.02. *Contribution and Subrogation*. Each Grantor (a "Contributing Party") agrees (subject to Section 4.03) that, in the event assets of any other Grantor shall be sold pursuant to any Collateral Document to satisfy any Obligation owed to any Secured Party, and such other Grantor (the "Claiming Party") shall not have been fully indemnified by the Borrower as provided in Section 4.01, the Contributing Party shall indemnify the Claiming Party in an amount equal to the greater of the book value or the fair market value of such assets, in each case multiplied by a fraction of which the numerator shall be the net worth of the Contributing Party on the date hereof and the denominator shall be the aggregate net worth of all the Contributing Parties together with the net worth of the Claiming Party on the date hereof (or, in the case of any Grantor becoming a party hereto pursuant to Section 5.14, the date of the Intellectual Property Security Agreement Supplement executed and delivered by such Grantor). Any Contributing Party making any payment to a Claiming Party pursuant to this Section 4.02 shall be subrogated to the rights of such Claiming Party to the extent of such payment.

SECTION 4.03. *Subordination*.

(a) Notwithstanding any provision of this Agreement to the contrary, all rights of the Grantors under Sections 4.01 and 4.02 and all other rights of indemnity, contribution or subrogation under applicable law or otherwise shall be fully subordinated to the indefeasible payment in full in cash of the Obligations. No failure on the part of the Borrower or any Grantor to make the payments required by Sections 4.01 and 4.02 (or any other payments required under applicable law or otherwise) shall in any respect limit the obligations and liabilities of any Grantor with respect to its obligations hereunder, and each Grantor shall remain liable for the full amount of the obligations of such Grantor hereunder.

(b) Each Grantor hereby agrees that upon the occurrence and during the continuance of an Event of Default and after notice from the Collateral Agent all Indebtedness owed by it to any Subsidiary shall be fully subordinated to the indefeasible payment in full in cash of the Obligations.

ARTICLE V

MISCELLANEOUS

SECTION 5.01. *Notices.* All communications and notices hereunder shall (except as otherwise expressly permitted herein) be in writing and given as provided in Section 10.02 of the Credit Agreement. All communications and notices hereunder to any Grantor shall be given to it in care of the Borrower as provided in Section 10.02 of the Credit Agreement.

SECTION 5.02. *Waivers; Amendment.*

(a) No failure or delay by the Collateral Agent, any L/C Issuer or any Lender in exercising any right or power hereunder or under any other Loan Document shall operate as a waiver thereof, nor shall any single or partial exercise of any such right or power, or any abandonment or discontinuance of steps to enforce such a right or power, preclude any other or further exercise thereof or the exercise of any other right or power. The rights and remedies of the Collateral Agent, the L/C Issuers and the Lenders hereunder and under the other Loan Documents are cumulative and are not exclusive of any rights or remedies that they would otherwise have. No waiver of any provision of this Agreement or consent to any departure by any Grantor therefrom shall in any event be effective unless the same shall be permitted by paragraph (b) of this Section 5.02, and then such waiver or consent shall be effective only in the specific instance and for the purpose for which given. Without limiting the generality of the foregoing, the making of a Loan or issuance of a Letter of Credit shall not be construed as a waiver of any Default, regardless of whether the Collateral Agent, any Lender or any L/C Issuer may have had notice or knowledge of such Default at the time. No notice or demand on any Grantor in any case shall entitle any Grantor to any other or further notice or demand in similar or other circumstances.

(b) Neither this Agreement nor any provision hereof may be waived, amended or modified except pursuant to an agreement or agreements in writing entered into by the Collateral Agent and the Grantor or Grantors with respect to which such waiver, amendment or modification is to apply, subject to any consent required in accordance with Section 10.01 of the Credit Agreement.

SECTION 5.03. *Collateral Agent's Fees and Expenses; Indemnification.*

(a) The parties hereto agree that the Collateral Agent shall be entitled to reimbursement of its expenses incurred hereunder as provided in Section 10.04 of the Credit Agreement.

(b) Without limitation of its indemnification obligations under the other Loan Documents, the Borrower agrees to indemnify the Collateral Agent and the other Indemnitees (as

defined in Section 10.05 of the Credit Agreement) against, and hold each Indemnatee harmless from, any and all losses, claims, damages, liabilities and related expenses, including the reasonable fees, charges and disbursements of any counsel for any Indemnatee, incurred by or asserted against any Indemnatee arising out of, in connection with, or as a result of, the execution, delivery or performance of this Agreement or any claim, litigation, investigation or proceeding relating to any of the foregoing agreement or instrument contemplated hereby, or to the Collateral, whether or not any Indemnatee is a party thereto; *provided* that such indemnity shall not, as to any Indemnatee, be available to the extent that such losses, claims, damages, liabilities or related expenses are determined by a court of competent jurisdiction by final and nonappealable judgment to have resulted from the gross negligence or willful misconduct of such Indemnatee or any Affiliate, director, officer, employee, counsel, agent or attorney-in-fact of such Indemnatee.

(c) Any such amounts payable as provided hereunder shall be additional Obligations secured hereby and by the other Collateral Documents. The provisions of this Section 5.03 shall remain operative and in full force and effect regardless of the termination of this Agreement or any other Loan Document, the consummation of the transactions contemplated hereby, the repayment of any of the Obligations, the invalidity or unenforceability of any term or provision of this Agreement or any other Loan Document, or any investigation made by or on behalf of the Collateral Agent or any other Secured Party. All amounts due under this Section 5.03 shall be payable within 10 days of written demand therefor.

SECTION 5.04. *Successors and Assigns.* Whenever in this Agreement any of the parties hereto is referred to, such reference shall be deemed to include the permitted successors and assigns of such party; and all covenants, promises and agreements by or on behalf of any Grantor or the Collateral Agent that are contained in this Agreement shall bind and inure to the benefit of their respective successors and assigns.

SECTION 5.05. *Survival of Agreement.* All covenants, agreements, representations and warranties made by the Grantors in the Loan Documents and in the certificates or other instruments prepared or delivered in connection with or pursuant to this Agreement or any other Loan Document shall be considered to have been relied upon by the Lenders and shall survive the execution and delivery of the Loan Documents and the making of any Loans and issuance of any Letters of Credit, regardless of any investigation made by any Lender or on its behalf and notwithstanding that the Collateral Agent, any L/C Issuer or any Lender may have had notice or knowledge of any Default or incorrect representation or warranty at the time any credit is extended under the Credit Agreement, and shall continue in full force and effect as long as the principal of or any accrued interest on any Loan or any fee or any other amount payable under any Loan Document is outstanding and unpaid or any Letter of Credit is outstanding and so long as the Commitments have not expired or terminated.

SECTION 5.06. *Counterparts; Effectiveness; Several Agreement.* This Agreement may be executed in counterparts, each of which shall constitute an original but all of which when taken together shall constitute a single contract. Delivery of an executed signature page to this Agreement by facsimile transmission or other electronic communication shall be as effective as delivery of a manually signed counterpart of this Agreement. This Agreement shall become effective as to any Grantor when a counterpart hereof executed on behalf of such

Grantor shall have been delivered to the Collateral Agent and a counterpart hereof shall have been executed on behalf of the Collateral Agent, and thereafter shall be binding upon such Grantor and the Collateral Agent and their respective permitted successors and assigns, and shall inure to the benefit of such Grantor, the Collateral Agent and the other Secured Parties and their respective successors and assigns, except that no Grantor shall have the right to assign or transfer its rights or obligations hereunder or any interest herein or in the Collateral (and any such assignment or transfer shall be void) except as expressly contemplated by this Agreement or the Credit Agreement. This Agreement shall be construed as a separate agreement with respect to each Grantor and may be amended, modified, supplemented, waived or released with respect to any Grantor without the approval of any other Grantor and without affecting the obligations of any other Grantor hereunder.

SECTION 5.07. *Severability.* Any provision of this Agreement held to be invalid, illegal or unenforceable in any jurisdiction shall, as to such jurisdiction, be ineffective to the extent of such invalidity, illegality or unenforceability without affecting the validity, legality and enforceability of the remaining provisions hereof; and the invalidity of a particular provision in a particular jurisdiction shall not invalidate such provision in any other jurisdiction. The parties shall endeavor in good-faith negotiations to replace the invalid, illegal or unenforceable provisions with valid provisions the economic effect of which comes as close as possible to that of the invalid, illegal or unenforceable provisions.

SECTION 5.08. *Right of Set-Off.* In addition to any rights and remedies of the Lenders provided by Law, upon the occurrence and during the continuance of any Event of Default, each Lender and its Affiliates and each L/C Issuer and its Affiliates is authorized at any time and from time to time, without prior notice to the Borrower or any other Grantor, any such notice being waived by the Borrower (on its behalf and on behalf of each Grantor and its Subsidiaries) to the fullest extent permitted by applicable Law, to set off and apply any and all deposits (general or special, time or demand, provisional or final) at any time held by, and other Indebtedness at any time owing by, such Lender and its Affiliates or such L/C Issuer and its Affiliates, as the case may be, to or for the credit or the account of the respective Grantors and their Subsidiaries against any and all Obligations owing to such Lender and its Affiliates or such L/C Issuer and its Affiliates hereunder or under any other Loan Document, now or hereafter existing, irrespective of whether or not such Agent or such Lender or Affiliate shall have made demand under this Agreement or under any other Loan Document and although such Obligations may be contingent or unmatured or denominated in a currency different from that of the applicable deposit or Indebtedness. Each Lender and each L/C Issuer agrees promptly to notify the Borrower and the Collateral Agent after any such set off and application made by such Lender or such L/C Issuer, as the case may be; *provided* that the failure to give such notice shall not affect the validity of such setoff and application. The rights of each Lender and each L/C Issuer under this Section 5.08 are in addition to other rights and remedies (including other rights of setoff) that the Collateral Agent, such Lender and such L/C Issuer may have.

SECTION 5.09. *Governing Law; Jurisdiction; Consent to Service of Process.*

(a) This Agreement shall be construed in accordance with and governed by the law of the State of New York.

(b) Each of the Grantors hereby irrevocably and unconditionally submits, for itself and its property, to the exclusive jurisdiction of the Supreme Court of the State of New York sitting in New York City and of the United States District Court of the Southern District of New York, and any appellate court from any thereof, in any action or proceeding arising out of or relating to this Agreement or any other Loan Document, or for recognition or enforcement of any judgment, and each of the parties hereto hereby irrevocably and unconditionally agrees that all claims in respect of any such action or proceeding may be heard and determined in such New York State or, to the extent permitted by law, in such Federal court. Each of the parties hereto agrees that a final judgment in any such action or proceeding shall be conclusive and may be enforced in other jurisdictions by suit on the judgment or in any other manner provided by law. Nothing in this Agreement or any other Loan Document shall affect any right that the Collateral Agent, any L/C Issuer or any Lender may otherwise have to bring any action or proceeding relating to this Agreement or any other Loan Document against any Grantor or its properties in the courts of any jurisdiction.

(c) Each of the Grantors hereby irrevocably and unconditionally waives, to the fullest extent it may legally and effectively do so, any objection which it may now or hereafter have to the laying of venue of any suit, action or proceeding arising out of or relating to this Agreement or any other Loan Document in any court referred to in paragraph (b) of this Section 5.09. Each of the parties hereto hereby irrevocably waives, to the fullest extent permitted by law, the defense of an inconvenient forum to the maintenance of such action or proceeding in any such court.

(d) Each party to this Agreement irrevocably consents to service of process in the manner provided for notices in Section 5.01. Nothing in this Agreement or any other Loan Document will affect the right of any party to this Agreement to serve process in any other manner permitted by law.

SECTION 5.10. *WAIVER OF JURY TRIAL.* EACH PARTY HERETO HEREBY WAIVES, TO THE FULLEST EXTENT PERMITTED BY APPLICABLE LAW, ANY RIGHT IT MAY HAVE TO A TRIAL BY JURY IN ANY LEGAL PROCEEDING DIRECTLY OR INDIRECTLY ARISING OUT OF OR RELATING TO THIS AGREEMENT, ANY OTHER LOAN DOCUMENT OR THE TRANSACTIONS CONTEMPLATED HEREBY (WHETHER BASED ON CONTRACT, TORT OR ANY OTHER THEORY). EACH PARTY HERETO (A) CERTIFIES THAT NO REPRESENTATIVE, AGENT OR ATTORNEY OF ANY OTHER PARTY HAS REPRESENTED, EXPRESSLY OR OTHERWISE, THAT SUCH OTHER PARTY WOULD NOT, IN THE EVENT OF LITIGATION, SEEK TO ENFORCE THE FOREGOING WAIVER AND (B) ACKNOWLEDGES THAT IT AND THE OTHER PARTIES HERETO HAVE BEEN INDUCED TO ENTER INTO THIS AGREEMENT BY, AMONG OTHER THINGS, THE MUTUAL WAIVERS AND CERTIFICATIONS IN THIS SECTION 5.10.

SECTION 5.11. *Headings.* Article and Section headings and the Table of Contents used herein are for convenience of reference only, are not part of this Agreement and are not to affect the construction of, or to be taken into consideration in interpreting, this Agreement.

SECTION 5.12. *Security Interest Absolute.* All rights of the Collateral Agent hereunder, the Security Interest, the grant of a security interest in the Pledged Collateral and all obligations of each Grantor hereunder shall be absolute and unconditional irrespective of (a) any lack of validity or enforceability of the Credit Agreement, any other Loan Document, any agreement with respect to any of the Obligations or any other agreement or instrument relating to any of the foregoing, (b) any change in the time, manner or place of payment of, or in any other term of, all or any of the Obligations, or any other amendment or waiver of or any consent to any departure from the Credit Agreement, any other Loan Document or any other agreement or instrument, (c) any exchange, release or non-perfection of any Lien on other collateral, or any release or amendment or waiver of or consent under or departure from any guarantee, securing or guaranteeing all or any of the Obligations or (d) any other circumstance that might otherwise constitute a defense available to, or a discharge of, any Grantor in respect of the Obligations or this Agreement.

SECTION 5.13. *Termination or Release.*

(a) This Agreement, the Security Interest and all other security interests granted hereby shall terminate with respect to all Obligations (other than (x) obligations under Secured Hedge Agreements not yet due and payable, (y) Cash Management Obligations not yet due and payable and (z) contingent indemnification obligations not yet accrued and payable) when all the outstanding Obligations have been indefeasibly paid in full and the Lenders have no further commitment to lend under the Credit Agreement, the L/C Obligations have been reduced to zero and the L/C Issuers have no further obligations to issue Letters of Credit under the Credit Agreement.

(b) A Grantor shall automatically be released from its obligations hereunder and the Security Interest in the Collateral of such Grantor shall be automatically released upon the consummation of any transaction permitted by the Credit Agreement as a result of which such Grantor ceases to be a Subsidiary or is designated as an Unrestricted Subsidiary of Parent; *provided* that the Required Lenders shall have consented to such transaction (to the extent required by the Credit Agreement) and the terms of such consent did not provide otherwise.

(c) Upon any sale or other transfer by any Grantor of any Collateral (other than any transfer to another Grantor) that is permitted under the Credit Agreement, or upon the effectiveness of any written consent to the release of the security interest granted hereby in any Collateral pursuant to Section 10.01 of the Credit Agreement, the security interest in such Collateral shall be automatically released.

(d) A Grantor (other than Holdings and the Borrower) shall automatically be released from its obligations hereunder and the Security Interest in the Collateral of such Grantor shall be automatically released if such Grantor ceases to be a Material Domestic Subsidiary pursuant to the terms of the Credit Agreement.

(e) In connection with any termination or release pursuant to paragraph (a), (b) or (c) of this Section 5.13, the Collateral Agent shall execute and deliver to any Grantor, at such Grantor's expense, all documents that such Grantor shall reasonably request to evidence such

termination or release. Any execution and delivery of documents pursuant to this Section 5.13 shall be without recourse to or warranty by the Collateral Agent.

(f) In the event that any of the Collateral shall be transferred by any Grantor in connection with the Foreign Reorganization, the Security Interest granted hereunder on such Collateral shall automatically be discharged and released and all rights to such Collateral shall revert to the applicable Grantor without any further action by the Collateral Agent or any other Person. Without prejudice to the foregoing, upon the request of the applicable Grantor, the Collateral Agent, at the expense of such Grantor, shall promptly execute and deliver to such Grantor, all releases, termination statements, stock certificates, any certificated securities or any other documents necessary or desirable for the release of the Security Interest on such Collateral.

SECTION 5.14. *Additional Restricted Subsidiaries.* Pursuant to Section 6.11 of the Credit Agreement, certain Restricted Subsidiaries of Parent that were not in existence or not Restricted Subsidiaries on the date of the Credit Agreement are required to enter in this Agreement as Grantors upon becoming Restricted Subsidiaries. Upon execution and delivery by the Collateral Agent and a Restricted Subsidiary of an Intellectual Property Security Agreement Supplement, such Restricted Subsidiary shall become a Grantor hereunder with the same force and effect as if originally named as a Grantor herein. The execution and delivery of any such instrument shall not require the consent of any other Grantor hereunder. The rights and obligations of each Grantor hereunder shall remain in full force and effect notwithstanding the addition of any new Grantor as a party to this Agreement.

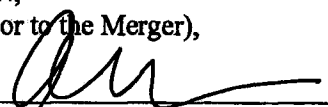
SECTION 5.15. *General Authority of the Collateral Agent.* By acceptance of the benefits of this Agreement and any other Collateral Documents, each Secured Party (whether or not a signatory hereto) shall be deemed irrevocably (a) to consent to the appointment of the Collateral Agent as its agent hereunder and under such other Collateral Documents, (b) to confirm that the Collateral Agent shall have the authority to act as the exclusive agent of such Secured Party for the enforcement of any provisions of this Agreement and such other Collateral Documents against any Grantor, the exercise of remedies hereunder or thereunder and the giving or withholding of any consent or approval hereunder or thereunder relating to any Collateral or any Grantor's obligations with respect thereto, (c) to agree that it shall not take any action to enforce any provisions of this Agreement or any other Collateral Document against any Grantor, to exercise any remedy hereunder or thereunder or to give any consents or approvals hereunder or thereunder except as expressly provided in this Agreement or any other Collateral Document and (d) to agree to be bound by the terms of this Agreement and any other Collateral Documents.

SECTION 5.16. *Collateral Agent Appointed Attorney-in-Fact.* Each Grantor hereby appoints the Collateral Agent the attorney-in-fact of such Grantor for the purpose of carrying out the provisions of this Agreement and taking any action and executing any instrument that the Collateral Agent may deem necessary or advisable to accomplish the purposes hereof at any time after and during the continuance of an Event of Default, which appointment is irrevocable (until the termination of the Credit Agreement) and coupled with an interest. Without limiting the generality of the foregoing, the Collateral Agent shall have the right, upon the occurrence and during the continuance of an Event of Default and notice by the Collateral Agent to the Borrower of its intent to exercise such rights, with full power of substitution either in the Collateral Agent's name or in the name of such Grantor (a) to receive,

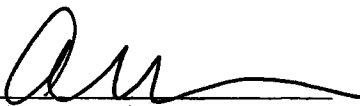
endorse, assign and/or deliver any and all notes, acceptances, checks, drafts, money orders or other evidences of payment relating to the Collateral or any part thereof; (b) to demand, collect, receive payment of, give receipt for and give discharges and releases of all or any of the Collateral; (c) to commence and prosecute any and all suits, actions or proceedings at law or in equity in any court of competent jurisdiction to collect or otherwise realize on all or any of the Collateral or to enforce any rights in respect of any Collateral; (d) to settle, compromise, compound, adjust or defend any actions, suits or proceedings relating to all or any of the Collateral; and (e) to use, sell, assign, transfer, pledge, make any agreement with respect to or otherwise deal with all or any of the Collateral, and to do all other acts and things necessary to carry out the purposes of this Agreement, as fully and completely as though the Collateral Agent were the absolute owner of the Collateral for all purposes; *provided* that nothing herein contained shall be construed as requiring or obligating the Collateral Agent to make any commitment or to make any inquiry as to the nature or sufficiency of any payment received by the Collateral Agent, or to present or file any claim or notice, or to take any action with respect to the Collateral or any part thereof or the moneys due or to become due in respect thereof or any property covered thereby. The Collateral Agent and the other Secured Parties shall be accountable only for amounts actually received as a result of the exercise of the powers granted to them herein, and neither they nor their officers, directors, employees or agents shall be responsible to any Grantor for any act or failure to act hereunder, except for their own gross negligence or wilful misconduct or that of any of their Affiliates, directors, officers, employees, counsel, agents or attorneys-in-fact.

IN WITNESS WHEREOF, the parties hereto have duly executed this Agreement
as of the day and year first above written.

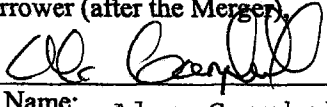
FREESCALE ACQUISITION
CORPORATION,
as Borrower (prior to the Merger),

By: 
Name: Anjan Mukherjee
Title: V President and Secretary

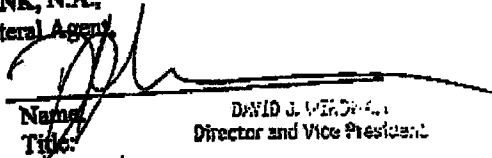
FREESCALE ACQUISITION HOLDINGS
CORP.,
as Holdings,

By: 
Name: Anjan Mukherjee
Title: V President and Secretary

FREESCALE SEMICONDUCTOR, INC.,
as the Borrower (after the Merger),

By: 
Name: Alan Campbell
Title: Senior Vice President and
Chief Financial Officer

CITIBANK, N.A.
as Collateral Agent

By: 

Name:
Title:

DAVID J. WILK
Director and Vice President

[IP Security Agreement]

[2646193]

U.S. Patents Assigned to Freescale

Patent #	Expiration Date	Title
4698128	17-Nov-06	SLOPED CONTACT ETCH PROC ESS
4723363	29-Dec-06	PROCESS FOR REMOVAL OF WATER
4728391	11-May-07	PEDESTAL TRANSISTORS AND METHOD OF PRODUCTION THE REOF
4728606	30-Mar-07	SELF-ALIGNED TRANSISTOR METHOD
4730127	22-Dec-06	METHOD OF MATCHING CURRE NTS FROM SPLIT COLLECTOR LATERAL PNP TRANSISTORS
4737468	13-Apr-07	PROCESS FOR DEVELOPING IMPLANTED BURIED LAYER AND/OR KEY LOCATORS
4737732	24-Feb-07	A LOW VOLTAGE OPERATIONALAMPLIFIER HAVING A SUBST ANTIALY FULL RANGE OUTPUT VOLTAGE
4739192	24-Feb-07	FAST SETTTLING DIGITAL TO ANALOG CONVERTER BIT SWI TCH
4739284	04-May-07	PHASE LOCKED LOOP HAVING FAST FREQUENCY LOCK STEE RING CIRCUIT
4740478	30-Jan-07	INTEGRATED CIRCUIT METHO D USING DOUBLE IMPLANT D OPING
4743568	24-Jul-07	MULTILEVEL INTERCONNECT TRANSFER PROCESS
4745370	03-Aug-07	ADJUSTABLE PHASE SHIFT C IRCUIT
4748133	26-Jun-07	DEPOSITION OF AMORPHOUS SSILICON FOR THE FORMATI ON OF INTERLEVEL DIELECT RICS IN SEMICONDUCTOR ME MORY DEVICES
4749929	22-Dec-06	INTERLOCKED STATE MACHIN ES
4750078	15-Jun-07	A SEMICONDUCTOR PROTECTION CIRCUIT HAVING BOTH POSITIVE AND NEGATIVE HIGH VOLTAGE PROTECTION
4751679	22-Dec-06	GATE STRESS TEST OF A MOS MEMORY
4753898	09-Jul-07	LDD CMOS PROCESS
4757280	22-Dec-07	NPN TRANSISTOR RC OSCILLATOR
4758986	20-Feb-07	SINGLE TRANSISTOR CELL FOR ELECTRICALLY-ERASABLE PROGRAMMABLE READ-ONLY MEMORY AND ARRAY THEREOF
4760034	15-Jun-07	METHOD OF FORMING EDGE-S EALED MULTI-LAYER STRUCT URE WHILE PROTECTING ADJ ACENT REGION BY SCREEN O XIDE LAYER
4760249	22-Dec-06	LOGIC ARRAY HAVING MULTIPLE OPTICAL LOGIC INPUTS
4760353	04-Jan-08	INTEGRATED GYRATOR OSCIL LATOR
4761385	10-Feb-07	TRENCH CAPACITOR
4763024	14-Jul-07	GAIN CONTROL CELL
4763181	08-Dec-06	HIGH DENSITY NON-CHARGE- SENSING DRAM CELL
4763306	22-Dec-06	CIRCUIT FOR ENABLING A TRANSMISSION GATE IN RES PONSE TO PREDECODED SIGN AL
4764477	06-Apr-07	A CMOS PROCESS FLOW WITH SMALL GATE GEOMETRY LDO N-CHANNEL TRANSISTORS FLOW
4766391	03-Aug-07	VIDEO DEMODULATOR SYSTEM
4766473	29-Dec-06	SINGLE TRANSISTOR CELL F OR ELECTRICALLY-ERASABLE PROGRAMMABLE READ-ONLY M EMORY AND ARRAY THEREOF

4769683	22-Jun-07	SUPERLATTICE GATE FIELD EFFECT TRANSISTOR
4771249	26-May-07	A PHASE LOCKED LOOP HAVI NG A FILTER WITH CONTROL LED VARIABLE BANDWIDTH
4772566	01-Jul-07	SINGLE TUB TRANSISTOR ME ANS AND METHOD
4775642	02-Feb-07	MODIFIED SOURCE/DRAIN IM PLANTS IN A DOUBLE-POLY NON-VOLATILE MEMORY PROC ESS
4775876	08-Sep-07	PHOTON RECYCLING LIGHT EMITTING DIODE
4779230	29-Dec-06	CMOS STATIC RAM CELL PROVIDED WITH AN ADDITIONAL BIPOLAR DRIVE TRANSISTOR
4781775	01-Jun-07	COPLANAR DIE TO SUBSTRAT E BOND METHOD
4781953	27-Mar-07	PLASTIC CARRIER TAPE HAV ING LOWERED CROSS RAILS
4782305	02-Oct-07	A DIFFERENTIAL INPUT-SIN GLE ON OUTPUT TWO POLE FIER IMPLEMENTED BY A SING LE AMPLIFIER
4782373	25-Sep-07	SELF-ALIGNED SEMICONDUCT OR DEVICE WITH NON-DIFFU SABLE CONTACTS
4784964	19-Oct-07	EPI DEFECT REDUCTION USI NG RAPID THERMAL ANNEALI NG
4785200	20-Aug-07	SELF CORRECTING SEU-HARD ENED CMOS REGISTER
4785258	17-Sep-07	A CMOS AMPLIFIER CIRCUIT WHICH MINIMIZES POWER SU PPLY NOISE COUPLED VIA A SUBSTRATE
4785259	01-Feb-08	BICMOS MEMORY SENSE AMPLIFIER SYSTEM
4785925	24-Sep-07	MECHANICAL CLUTCH
4786615	31-Aug-07	METHOD FOR IMPROVED SURF ACE PLANARITY IN SELECTI VE EPITAXIAL SILICON
4787958	28-Aug-07	METHOD OF CHEMICALLY ETCHING TIW AND/OR TIWN
4791073	17-Nov-07	TRENCH ISLOATION METHOD FOR SEMICONDUCTOR DEVICES
4791325	21-Dec-07	CLASS B CLAMP CIRCUIT
4791405	28-Nov-06	A DATA CONVERTOR FOR DIR ECTLY PROVIDING OUTPUTS IN TWO'S COMPLEMENT CODE
4791615	22-Dec-06	MEMORY WITH REDUNDANCY AND PREDECODED SIGNALS
4792899	02-Jan-07	MICROPROCESSOR SUPPORT INTEGRATED CIRCUIT
4794348	15-Apr-08	LINEAR WIDEBAND DIFFERENTIAL AMPLIFIER AND METHOD FOR LINEARIZING THE SAME
4794434	06-Jul-07	TRENCH CELL FOR A DRAM
4796102	03-Aug-07	AUTOMATIC FREQUENCY CONT ROL SYSTEM
4796157	04-Jan-08	SUBSTRATE MOUNTING ASSEMBLY
4796235	22-Jul-07	WRITE PROTECT MECHANISM FOR NON-VOLATILE MEMORY
4797577	14-Dec-07	BANDGAP REFERENCE CIRCUI HAVING HIGHER-ORDER TEMPERATURE COMPENSATION
4797858	30-Mar-07	SEMICONDUCTOR MEMORY WIT H DIVIDED WORD LINES AND SHARED SENSE AMPLIFIERS
4799392	06-Aug-07	METHOD FOR DETERMINING S ILICON (MASS 28) BEAM PU RITY PRIOR TO IMPLANTATI ON OF GALLIUM ARSENIDE
4799991	02-Nov-07	PROCESS FOR PREFERENTIAL LY ETCHING POLYCRYSTALLI NE SILICON
4800298	04-Aug-07	OUTPUT BUFFER FOR IMPROV ING DI/DT
4800429	14-Mar-08	AUTO SYNC POLARITY CONTROL CIRCUIT FOR USE WITH MONITOR
4800531	22-Dec-06	ADDRESS BUFFER CIRCUIT FOR A DRAM

4801350	29-Dec-06	METHOD FOR OBTAINING SUB MICRON FEATURES FROM OPTICAL LITHOGRAPHY TECHNOLOGY
4801555	14-Jan-07	DOUBLE-IMPLANT PROCESS FOR FORMING GRADED SOURCE/DRAIN REGIONS
4801560	02-Oct-07	SEMICONDUCTOR PROCESSING UTILIZING CARBON CONTAINING THICK FILM SPIN-ON GLASS
4802119	17-Mar-07	SINGLE CHIP MICROCOMPUTER WITH PATCHING AND CONFIGURATION CONTROLLED BY ON-BOARD NON-VOLATILE MEMORY
4802129	03-Dec-07	RAM WITH DUAL PRECHARGE CIRCUIT AND WRITE RECOVERY CIRCUITRY
4802236	30-Dec-06	INSTANTANEOUS DEVIATION LIMITER WITH PRE-EMPHASIS AND ZERO AVERAGE VALVE
4802325	20-Jul-07	METHOD OF PACKAGING
4803175	14-Sep-07	METHOD OF FABRICATING A BIPOLAR SEMICONDUCTOR DEVICE WITH SILICIDE CONTACTS
4804861	11-Feb-08	MULTIFUNCTION ONBOARD INPUT/OUTPUT TERMINATION
4805003	10-Nov-07	GAAS MESFET
4805007	30-Mar-07	FLIP CHIP MODULE
4806793	02-Oct-07	SIGNATURE CIRCUIT RESPONSIVE TO AN INPUT SIGNAL
4807104	15-Apr-08	A VOLTAGE MULTIPLYING AND INVERTING CHARGE PUMP
4807191	04-Jan-08	REDUNDANCY FOR A BLOCK-ARCHITECTURE MEMORY
4807198	28-Dec-07	RAM WITH DUAL PRECHARGE CIRCUIT AND WRITE RECOVERY CIRCUITRY
4808907	17-May-08	CURRENT REGULATOR AND METHOD
4809057	18-Dec-06	ELECTRICAL COMPONENT ASSEMBLY METHOD OF MANUFACTURE THEREOF
4809231	12-Nov-07	METHOD AND APPARATUS FOR POST-PACKAGING TESTING OF ONE-TIME PROGRAMMABLE MEMORIES
4810903	14-Dec-07	BICMOS DRIVER CIRCUIT INCLUDING SUBMICRON ON-CHIP VOLTAGE SOURCE
4811081	23-Mar-07	SEMICONDUCTOR DIE BONDING WITH CONDUCTIVE ADHESIVE
4812418	27-Nov-07	MICRON AND SUBMICRON PATTERNING WITHOUT USING A LITHOGRAPHIC MASK HAVING SUBMICRON DIMENSIONS
4812421	26-Oct-07	TAB-TYPE SEMICONDUCTOR PROCESS AND SEMICONDUCTOR STRUCTURE
4812679	09-Nov-07	POWER-ON-RESET CIRCUIT
4814258	24-Jul-07	PMGI BI-LAYER LIFT-OFF PROCESS
4814635	27-Nov-07	VOLTAGE TRANSLATOR CIRCUIT
4816673	01-Jul-07	SHOCK PROOF STEERING MODULE ASSEMBLY
4816996	24-Jul-07	QUEUED SERIAL PERIPHERAL INTERFACE FOR USE IN A DATA PROCESSING SYSTEM
4818323	26-Jun-07	METHOD OF MAKING A VOID FREE WAFER VIA VACUUM LAMINATION
4821231	14-Sep-07	A METHOD AND APPARATUS FOR SELECTIVELY EVALUATING AN EFFECTIVE ADDRESS FOR A COPROCESSOR
4825277	17-Nov-07	TRENCH ISOLATION PROCESS AND STRUCTURE
4829202	28-Aug-07	SEMICONDUCTOR INTEGRATED CIRCUITS
4829264	30-Jun-08	ALL NPN DIFFERENTIAL TO SINGLE ENDED AMPLIFIER

4829351	16-Mar-07	POLYSILICON PATTERN FOR AFLOATING GATE MEMORY
4830973	06-Oct-07	MERGED COMPLEMENTARY BIPOLAR AND MOS MEANS AND METHOD
4831287	11-Apr-08	LATCHING SENSE AMPLIFIER
4831337	25-Apr-08	WIDEBAND AMPLIFIER
4831342	25-Apr-08	VARIABLE FREQUENCY MULTIVIBRATOR
4831343	24-Mar-08	CRYSTAL CLOCK GENERATOR HAVING FIFTY PERCENT DUTY CYCLE
4832996	24-Feb-08	SEMICONDUCTOR DIE FOR PLASTIC ENCAPSULATION HAVING AN ADHESION PROMOTER
4835112	08-Mar-08	CMOS SALICIDE PROCESS USING GERMANIUM IMPLANTATION
4835589	28-Sep-07	RAM CELL HAVING TRENCH SIDEWALL LOAD
4836371	19-Oct-07	FILLER STRIPS
4837173	13-Jul-07	N-CHANNEL MOS TRANSISTORS HAVING SOURCE/DRAIN REGIONS WITH GERMANIUM
4837176	30-Jan-07	INTEGRATED CIRCUIT STRUCTURES HAVING POLYCRYSTAL LINE ELECTRODE CONTACTS AND PROCESS
4837183	02-May-08	SEMICONDUCTOR DEVICE METALLIZATION PROCESS
4839769	09-May-08	DRIVER PROTECTION CIRCUIT
4841133	30-Mar-08	DATA CARD CIRCUITS
4843390	24-Feb-08	AN OVERSAMPLED A/D CONVERTER HAVING DIGITAL ERROR CORRECTION
4844576	28-Dec-07	LIGHT DIFFUSER
4847210	05-Aug-08	INTEGRATED PIN PHOTO-DETECTOR AND METHOD
4847214	18-Apr-08	METHOD FOR FILLING TRENCHES FROM A SEED LAYER
4849371	14-Dec-07	MONOCRYSTALLINE SEMICONDUCTOR BURIED LAYERS FOR ELECTRICAL CONTACTS TO SEMICONDUCTOR DEVICES
4851892	08-Sep-07	STANDARD CELL ARRAY HAVING FAKE GATE FOR ISOLATING DEVICES FROM SUPPLY VOLTAGES
4852062	28-Sep-07	EPROM DEVICE USING ASYMMETRICAL TRANSISTOR CHARACTERISTICS
4853347	03-Oct-08	SELECTIVE METAL DEPOSITION PROCESS
4853641	29-Feb-08	DIFFERENTIAL PHASE REDUCTION CIRCUIT FOR A VIDEO DEMODULATING SYSTEM AND METHOD
4855804	17-Nov-07	MULTILAYER TRENCH ISOLATION PROCESS AND STRUCTURE
4857479	03-Jun-07	METHOD OF MAKING POLY-SIDEWALL CONTACT TRANSISTORS
4857481	14-Mar-09	METHOD OF FABRICATING AIRBRIDGE METAL INTERCONNECTS
4857863	25-Aug-08	A LOW POWER OUTPUT DRIVER CIRCUIT WITH SLEW RATE LIMITING
4858208	11-Jul-08	APPARATUS AND METHOD FOR TESTING SEMICONDUCTOR DEVICES
4862169	25-Mar-08	AN OVERSAMPLED A/D CONVERTER USING FILTERED, CASCADED NOISE SHAPING MODULATORS
4862352	16-Sep-07	DATA PROCESSOR HAVING PULSE WIDTH ENCODED STATUS OUTPUT SIGNAL
4866676	24-Mar-08	IMPROVED TESTING ARRANGEMENT FOR A DRAM WITH REDUNDANCY
4870303	03-Jun-08	PHASE DETECTOR

4870478	21-Apr-08	DUAL-GATE GALLIUM ARSENIDE POWER METAL SEMICONDUCTOR FIELD EFFECT TRANSISTOR
4871686	28-Mar-08	INTEGRATED SCHOTTKY DIODE AND TRANSISTOR
4871926	06-Sep-08	LOW POWER THREE STATE POWER UP CIRCUIT
4874308	04-Apr-08	VACUUM ASSISTED TRANSFER MOLD AND VENT PIN
4876217	24-Mar-08	METHOD OF FORMING SEMICONDUCTOR STRUCTURE ISOLATION REGIONS
4876542	25-Jan-08	A MULTIPLE OUTPUT OVERSAMPLING A/D CONVERTER WITH EACH OUTPUT CONTAINING DATA AND NOISE
4876543	31-May-08	A MULTI-RATE CASCADED NOISE SHAPING MODULATOR
4877482	23-Mar-09	NITRIDE REMOVAL METHOD
4878031	06-Mar-09	CLASS B VARIABLE GAIN CONTROL CIRCUIT
4878101	29-Dec-06	SINGLE TRANSISTOR CELL FOR ELECTRICALLY-ERASABLE PROGRAMMABLE READ-ONLY MEMORIES
4879506	02-Aug-08	SHUNT REGULATOR
4879682	15-Sep-08	SENSE AMPLIFIER PRECHARGE CONTROL
4880708	05-Jul-08	METALLIZATION SCHEME PROVIDING ADHESION AND BARRIER PROPERTIES
4882711	03-Feb-08	NON-VOLATILE MEMORY SYSTEM
4883774	21-Mar-08	SILVER FLASHING PROCESS ON SEMICONDUCTOR LEADFRAMES
4885056	02-Sep-08	METHOD OF REDUCING DEFECTS ON SEMICONDUCTOR WAFERS
4885484	05-Jul-08	VOLTAGE CLAMPED DIFFERENTIAL TO SINGLE ENDED CONVERTER CIRCUIT
4888688	18-Sep-07	DYNAMIC DISABLE MECHANISM FOR A MEMORY MANAGEMENT UNIT
4889590	27-Apr-09	SEMICONDUCTOR PRESSURE SENSOR MEANS AND METHOD
4890069	29-Feb-08	GALLIUM ARSENIDE POWER MONOLITHIC MICROWAVE INTEGRATED CIRCUIT
4890072	03-Feb-08	PHASE LOCKED LOOP HAVING A FAST LOCK CURRENT REDUCTION AND CLAMPING CIRCUIT
4890156	13-Mar-07	MULTICHIP IC MODULE HAVING COPLANAR DICE AND SUBSTRATE
4890223	26-Dec-06	PAGED MEMORY MANAGEMENT UNIT WHICH EVALUATES ACCESS PERMISSIONS WHEN CREATING TRANSLATOR
4893094	13-Mar-09	FREQUENCY SYNTHESIZER WITH CONTROL OF START-UP BATTERY SAVING OPERATIONS
4893233	18-Apr-08	METHOD AND APPARATUS FOR DYNAMICALLY CONTROLLING EACH STAGE OF A MULTI-STAGE PIPELINED DATA UNIT
4893267	01-Nov-08	METHOD AND APPARATUS FOR A DATA PROCESSOR TO SUPPORT MULTI-MODE MULTI-PRECISION INTEGER ARITHMETIC
4896094	30-Jun-09	A BANDGAP REFERENCE CIRCUIT WITH IMPROVED OUTPUT REFERENCE VOLTAGE
4901033	01-May-09	FREQUENCY SYNTHESIZER WITH DYNAMICALLY PROGRAMMABLE FREQUENCY RANGE OF SELECTED LOOP BANDWIDTH
4901036	29-Jun-09	FREQUENCY SYNTHESIZER WITH AN INTERFACE CONTROLLER AND BUFFER MEMORY
4902633	09-May-08	PROCESS FOR MAKING A BIPOLAR INTEGRATED CIRCUIT

4903264	18-Apr-08	METHOD AND APPARATUS FOR HANDLING OUT OF ORDER EXCEPTIONS IN A PIPELINED DATA UNIT
4903401	03-Apr-09	LEADFRAME SILVER BLEED ELIMINATION
4905070	02-Sep-08	SEMICONDUCTOR DEVICE EXHIBITING NO DEGRADATION OF LOW CURRENT GAIN
4907189	08-Aug-08	CACHE TAG COMPARATOR WITH READ MODE AND COMPARE MODE
4908688	13-Mar-07	MEANS AND METHOD FOR PROVIDING CONTACT SEPARATION IN SILICIDED DEVICES
4910393	23-May-07	MEMORY CARDS
4910656	20-Mar-07	A BUS MASTER HAVING SELECTIVE BURST INITIATION
4912062	20-May-08	METHOD OF ELIMINATING BIRD'S BEAKS WHEN FORMING FIELD OXIDE WITHOUT NITRIDE MASK
4914049	16-Oct-09	METHOD OF FABRICATING A HETEROJUNCTION BIPOLAR TRANSISTOR
4914573	05-Oct-07	A BUS MASTER WHICH SELECTIVELY ATTEMPTS TO FILL COMPLETE ENTRIES IN A CACHE LINE
4914578	03-Apr-07	A METHOD AND APPARATUS FOR INTERRUPTING A COPROCESSOR
4914581	26-Oct-07	METHOD AND APPARATUS FOR EVALUATING CONDITIONS IN A DATA PROCESSOR
4915779	23-Aug-08	RESIDUE-FREE PLASMA ETCH OF HIGH TEMPERATURE ALCU
4918333	31-Oct-08	MICROPROCESSOR HAVING HIGH CURRENT DRIVE
4918512	19-Dec-08	SEMICONDUCTOR PACKAGE HAVING AN OUTWARDLY ARCED DIE CAVITY
4918663	16-Sep-07	LATCH-UP CONTROL FOR A CMOS MEMORY WITH A PUMPED WELL
4921813	17-Oct-08	METHOD FOR MAKING A POLY SILICON TRANSISTOR
4922326	18-Jul-08	CERAMIC SEMICONDUCTOR PACKAGE HAVING CRACK ARRESTOR PATTERNS
4922493	15-Aug-08	ERROR DETECTION AND CORRECTION CIRCUIT
4924111	31-Oct-08	MICROPROCESSOR LAYOUT MINIMIZING TEMPERATURE AND CURRENT EFFECTS
4924291	24-Oct-08	FLAGLESS SEMICONDUCTOR PACKAGE
4925085	25-May-09	BONDING MEANS AND METHOD
4926066	12-Sep-08	CLOCK DISTRIBUTION CIRCUIT HAVING MINIMAL SKEW
4926319	19-Aug-08	INTEGRATED CIRCUIT TIMER WITH MULTIPLE CHANNELS AND DEDICATED SERVICE PROCESSOR
4927505	05-Jul-08	METALLIZATION SCHEME PROVIDING ADHESION AND BARRIER PROPERTIES
4927775	06-Mar-09	METHOD OF FABRICATING A HIGH PERFORMANCE BIPOLAR AND MOS DEVICE
4928002	05-Dec-08	METHOD OF RECORDING TEST RESULT OF DIE ON A WAFER
4928068	01-May-09	FM DEMODULATOR
4928156	13-Jul-07	N-CHANNEL MOS TRANSISTORS HAVING SOURCE/DRAIN REGIONS WITH GERMANIUM
4928268	21-Apr-09	A MEMORY USING DISTRIBUTED DATA LINE LOADING
4931717	05-Sep-09	LOAD RESPONSE CONTROL AND METHOD
4931748	26-Aug-08	INTEGRATED CIRCUIT WITH CLOCK GENERATOR CIRCUIT

4933643	28-Jul-09	OPERATIONAL AMPLIFIER HAVING IMPROVED DIGITALLY ADJUSTED NULL OFFSET
4933741	14-Nov-08	MULTIFUNCTION GROUND PLANE
4935734	19-Jun-07	SEMICONDUCTOR INTEGRATED CIRCUITS/SYSTEM
4942522	19-Aug-08	TIMER CHANNEL WITH MULTIPLE TIMER REFERENCE FEATURES
4943539	09-May-09	PROCESS FOR MAKING A MULTILAYER METALLIZATION STRUCTURE
4943708	01-Feb-08	DATA DEVICE MODULE HAVING LOCKING GROOVE
4943948	24-Jul-07	PROGRAM CHECK FOR A NON-VOLATILE MEMORY
4944447	29-Mar-09	BONDING VERIFICATION PROCESS TEST PATTERNS
4945319	28-Jul-09	HIGH FREQUENCY IMPEDANCE TRANSFORMER
4945415	29-Aug-08	SLEW ENHANCEMENT CIRCUIT FOR AN AUTOMATIC FREQUENCY CONTROL SYSTEM
4947135	28-Jul-09	SINGLE ENDED CHOPPER STABILIZED OPERATIONAL AMPLIFIER
4951005	27-Dec-09	A PHASE LOCKED LOOP WITH REDUCED FREQUENCY/PHASE LOCK TIME
4952367	19-Aug-08	TIMER CHANNEL FOR USE IN A MULTIPLE CHANNEL TIMER SYSTEM
4953027	24-Apr-09	OSD IN A TV RECEIVER INCLUDING A WINDOW SMOOTHING AND EDGE ENHANCING
4958277	18-Sep-07	QUEUED SERIAL PERIPHERAL INTERFACE FOR USE IN A DATA PROCESSING SYSTEM
4959702	05-Oct-09	SI-GAP-SI HETEROJUNCTION BIPOLAR TRANSISTOR (HBT) ON SI SUBSTRATE SI-GAP-SI
4961067	02-Oct-07	PATTERN DRIVEN INTERRUPT IN A DIGITAL DATA PROCESSOR
4962427	20-Apr-09	TV RECEIVER INCLUDING MULTISTANDARD OSC
4962428	20-Apr-09	MULTISTANDARD OSC IN A TV RECEIVER INCLUDING DISPLAY POSITIONING
4963506	24-Apr-09	SELECTIVE DEPOSITION OF AMORPHOUS AND POLYCRYSTALLINE SILICON
4964083	27-Apr-09	A NON-ADDRESS TRANSISTION DETECTION MEMORY WITH IMPROVED ACCESS TIME
4965473	18-Jul-09	EPROM LOW VOLTAGE SENSE AMPLIFIER
4965526	14-Jul-09	HYBRID AMPLIFIER
4966858	02-Nov-09	METHOD OF FABRICATING A LATERAL SEMICONDUCTOR STRUCTURE INCLUDING FIELD PLATES FOR SELF-ALIGNMENT
4966864	27-Mar-09	CONTACT STRUCTURE AND METHOD
4968931	03-Nov-09	APPARATUS AND METHOD FOR BURNING IN INTEGRATED CIRCUIT WAFERS
4969032	18-Jul-08	MONOLITHIC MICROWAVE INTEGRATED CIRCUIT HAVING VERTICALLY STACKED COMPONENTS
4970176	29-Sep-09	MULTIPLE STEP METALLIZATION PROCESS
4970408	30-Oct-09	CMOS POWER-ON RESET CIRCUIT
4970412	28-Jul-09	COMPARATOR CIRCUIT
4972089	03-Apr-09	SINGLE PACKAGE ELECTRO-OPTIC TRANSMITTER RECEIVER
4972102	08-May-09	SINGLE-ENDED SENSE AMPLIFIER WITH DUAL FEEDBACK AND A LATCHING DISABLE MODE THAT SAVES POWER

4972374	27-Dec-09	OUTPUT AMPLIFYING STAGE WITH P OWER SAVING FEATURE
4972413	23-Mar-09	METHOD AND APPARATUS FOR HIGH SPEED INTEGRATED CIRCUIT TESTI NG
4975146	08-Sep-09	PLASMA REMOVAL OF UNWANTED MAT ERIAL
4975882	30-Oct-09	A USER PROGRAMMABLE REDUNDANT MEMORY
4976814	18-Jul-08	METHOD OF MAKING A CERAMIC SEM ICONDUCTOR PACKAGE HAVING CRAC K ARRESTOR
4977381	02-Jun-09	DIFFERENTIAL RELAXATION OSCILL ATOR
4977436	25-Jul-08	HIGH DENSITY DRAM
4977535	08-Dec-09	METHOD OF COMPUTATION OF NORMA LISED NUMBERS
4977541	09-Jun-09	EPROM PROGRAMMING
4979145	18-Dec-07	STRUCTURE AND METHOD FOR IMPRO VING HIGH SPEED DATA RATE IN A DRAM
4983531	12-Feb-10	METHOD OF FABRICATING A SINGLE POLYSILICON BIPOLAR TRANSISTO R WHICH IS COMPATIBLE WITH A M ETHOD OF FABRICATING CMOS TRAN
4983974	06-Feb-10	ANALOG-TO-DIGITAL CONVERSION B Y VARYING BOTH INPUTS OF A COM PARATOR UTILIZING SUCCESSIVE A PPROXIMATION
4984266	08-Jan-08	SUBSCRIBER LINE CARD ARRANGEME NT
4985988	03-Nov-09	METHOD FOR ASSEMBLYING, TESTIN G, AND PACKAGING INTEGRATED CI RCUITS
4989069	29-Jan-10	SEMICONDUCTOR PACKAGE HAVING L EADS THAT BREAK-AWAY FROM SUPP ORTS
4989209	24-Mar-09	METHOD AND APPARATUS FOR TESTI NG HIGH PIN COUNT INTEGRATED C IRCUITS
4991187	21-Jul-09	HIGH SPEED PRESCALER
4993559	31-Jul-09	WAFER CARRIER
4994408	06-Feb-09	EPITAXIAL FILM GROWTH USING LOW PRESSURE MOCVD
4994761	06-Nov-09	VHF POWER AMPLIFIER
4994961	19-Feb-08	COPROCESSOR INSTRUCTION FORMAT
4995953	30-Oct-09	A METHOD OF FORMING A SEMICOND UCTOR MEMBRANE USING AN ELECTR OCHEMICAL ETCH-STOP
4996170	30-Jul-10	MOLDING PROCESS FOR ENCAPSUL ATING SEMICONDUCTOR DEVICES US ING A THIXOTROPIC COMPOUND
4996448	27-Nov-09	LOW POWER PEAK DETECTOR/BUFFER WITH FAST CHARGE-UP TIME
4996498	03-Jan-10	COMMON MODE COMPENSATION FOR D IFFERENTIAL INTEGRATING FILTER
4997790	13-Aug-10	PROCESS FOR FORMING A SELF-ALI GNED CONTACT STRUCTURE
4997812	12-Sep-08	SUPERCONDUCTING THICK FILM
4998158	05-Mar-08	HYPOEUTECTIC OHMIC CONTA CT TO N-TYPE GALLIUM ARS ENIDE WITH DIFFUSION BAR RIER
4998160	23-Jan-09	SUBSTRATE POWER SUPPLY CONTACT FOR POWER INTEGRATED CIRCUITS
4999521	12-Mar-08	CMOS ANALOG MULTIPLYING CIRCUIT
4999796	14-Dec-09	STICKY BIT DETECTION AND SHIFT ING LOGIC
5001368	08-Dec-09	SEMICONDUCTOR INTEGRATED CIRCU IT
5001524	05-Jun-09	DIGITALLY CONTROLLED FIELD EFF ECT ATTENUATOR

DEVICES		
5001731	02-Oct-09	METHOD AND APPARATUS FOR ELIMINATING CLOCKSKEW RACE CONDITION ERRORS
5003286	07-Aug-09	BINARY MAGNITUDE COMPARATOR WITH ASYNCHRONOUS COMPARE OPERATION AND METHOD THEREFOR
5003513	23-Apr-10	LATCHING INPUT BUFFER FOR AN ATD MEMORY
5006486	29-Jan-10	DIRECT CONTACT TAB METHOD AND PACKAGE
5006673	07-Dec-09	FABRICATION OF PAD ARRAY CARRIERS FROM A UNIVERSAL INTERCONNECT STRUCTURE
5006909	30-Oct-09	DRAM WITH A VERTICAL CAPACITOR AND TRANSISTOR
5008615	03-Nov-09	MEANS AND METHOD FOR TESTING INTEGRATED CIRCUITS ATTACHED TO A LEADFRAME
5011066	27-Jul-10	ENHANCED COLLAPSE SOLDER INTERCONNECTION
5012386	27-Oct-09	HIGH PERFORMANCE OVERMOLDED ELECTRONIC PACKAGE
5015892	16-Mar-10	AN ASYNCHRONOUS DELAY CIRCUIT
5016064	25-Sep-09	QUANTUM WELL STRUCTURE HAVING ENHANCED CONDUCTIVITY
5019673	22-Aug-10	FLIP CHIP PACKAGE FOR IC'S
5020038	03-Jan-10	ANTIMETASTABLE STATE CIRCUIT
5020076	21-May-10	HYBRID MODULATION APPARATUS
5020910	05-Mar-10	A MONOLITHIC DIFFRACTION SPECTROMETER
5021991	04-Jun-08	COPROCESSOR INSTRUCTION FORMAT
5025387	06-Sep-08	POWER SAVING ARRANGEMENT FOR A CLOCKED DIGITAL CIRCUIT
5027054	20-Oct-08	THRESHOLD DEPENDENT VOLTAGE SOURCE
5028741	24-May-10	A HIGH FREQUENCY POWER SEMICONDUCTOR DEVICE
5032741	04-Jun-10	CDCFL LOGIC CIRCUITS HAVING SHARED LOADS
5034092	09-Oct-10	PLASMA ETCHING OF SEMICONDUCTOR SUBSTRATES
5034922	23-Jul-08	INTELLIGENT ELECTRICALLY ERASABLE, PROGRAMMABLE READ-ONLY MEMORY WITH IMPROVED READ LATENCY
5034923	23-Jul-08	STATIC RAM WITH SOFT DEFECT DETECTION
5036294	03-Dec-10	PHASE LOCKED LOOP HAVING LOW-FREQUENCY JITTER COMPENSATION
5037777	02-Jul-10	METHOD FOR FORMING A MULTI-LAYER SEMICONDUCTOR DEVICE USING SELECTIVE PLANARIZATION
5040144	28-Nov-09	INTEGRATED CIRCUIT WITH IMPROVED POWER SUPPLY DISTRIBUTION
5041740	30-Apr-10	PARALLEL CLOCKED LATCH
5042005	20-Aug-08	TIMER CHANNEL WITH MATCH RECOGNITION FEATURES
5043652	01-Oct-10	DIFFERENTIAL VOLTAGE TO DIFFERENTIAL CURRENT CONVERSION CIRCUIT HAVING LINEAR OUTPUT
5043943	18-Jun-10	CACHE MEMORY WITH A PARITY WRITE CONTROL CIRCUIT
5043993	30-Apr-10	OPTICAL SIGNAL FREQUENCY CONVERTER AND MIXER
5047812	27-Feb-09	INSULATED GATE FIELD EFFECT DEVICE
5047987	17-Nov-09	LOW VOLTAGE INHIBIT CONTROL APPARATUS
5049831	29-Mar-10	SINGLE-ENDED INPUT TO DIFFERENTIAL OUTPUT AMPLIFIER WITH INTEGRAL TWO-POLE FILTER

5050114	17-Sep-10	SIMULATION OF TWO-PHASE LIQUID COOLING FOR THERMAL PREDICTION OF DIRECT LIQUID COOLING SCHEMES
5053357	27-Dec-09	METHOD OF ALIGNING AND MOUNTING AN ELECTRONIC DEVICE ON A PRINTED CIRCUIT BOARD USING A FLEXIBLE SUBSTRATE HAVING FIXED LEAD ARRAYS THEREON
5053949	03-Apr-09	NO-CHIP DEBUG PERIPHERAL WHICH USES EXTERNALLY PROVIDED INSTRUCTIONS TO CONTROL A CORE PROCESSING UNIT
5054194	02-Nov-09	METHOD FOR CONTROLLING WIRE LOOP HEIGHT
5057707	05-Jul-09	CHARGE PUMP INCLUDING FEEDBACK CIRCUITRY FOR ELIMINATING THE REQUIREMENT OF A SEPARATE OSCILLATOR
5057893	28-Sep-10	STATIC RAM CELL WITH SOFT ERROR IMMUNITY
5057999	26-Jun-09	MICROPROCESSOR HAVING A PROTECTION CIRCUIT TO INSURE PROPER INSTRUCTION FETCHING
5058070	12-Feb-10	HIGH SPEED MEMORY WITH ROW REDUNDANCY
5060031	18-Sep-10	COMPLEMENTARY HETEROJUNCTION FIELD EFFECT TRANSISTOR WITH AN ANISOTYPE N+ GATE FOR P-CHANNEL DEVICES
5061911	03-Apr-10	SINGLE FAULT/TOLERANT MMIC SWITCHES
5063533	10-Apr-09	RECONFIGURABLE DEINTERLEAVER/ INTERLEAVER FOR BLOCK ORIENTED DATA
5064781	31-Aug-10	A METHOD OF FABRICATING INTEGRATED SILICON AND NON-SILICON SEMICONDUCTOR DEVICES
5067002	19-Nov-08	INTEGRATED CIRCUIT STRUCTURES HAVING POLYCRYSTALLINE ELECTRODE CONTACTS
5067218	21-May-10	VACUUM WAFER TRANSPORT AND PROCESSING SYSTEM AND METHOD USING A PLURALITY OF WAFER TRANSPORT ARMS
5070031	14-Dec-10	COMPLEMENTARY SEMICONDUCTOR REGION FABRICATION
5070382	18-Aug-09	SEMICONDUCTOR STRUCTURE FOR HIGH POWER INTEGRATED CIRCUITS
5072193	01-Oct-10	WIRE SHIELDING FOR RF CIRCUIT BOARDS AND AMPLIFIERS
5072365	27-Dec-09	DIRECT MEMORY ACCESS CONTROLLER USING PRIORITIZED INTERRUPTS FOR VARYING BUS MASTERSHIP
5074139	04-Mar-11	ROLL FORMING OF SEMICONDUCTOR COMPONENTS LEADFRAMES
5074152	24-Dec-10	PIEZORESISTIVE TRANSDUCER WITH LOW DRIFT OUTPUT VOLTAGE
5075638	26-Dec-10	STANDBY SYSTEM FOR A FREQUENCY SYNTHESIZER
5075744	03-Dec-10	GAAS HETEROSTRUCTURE HAVING A GAASYP1-Y STRESS-COMPENSATION LAYER
5077633	01-May-09	METHOD OF GROUNDING AN ULTRA HIGH DENSITY ARRAY CHIP CARRIER
5077693	01-Aug-10	DYNAMIC RANDOM ACCESS MEMORY
5079517	04-Feb-11	CIRCUIT FOR DC CONTROL OF A COMPRESSOR
5079519	14-Feb-11	DIGITAL PHASE LOCK LOOP FOR A GATE ARRAY
5080933	04-Sep-10	SELECTIVE DEPOSITION OF POLYCRYSTALLINE SILICON
5081454	04-Sep-10	AUTOMATIC A/D CONVERTER OPERATION USING PROGRAMMABLE SAMPLE TIME
5081511	06-Sep-10	HETEROJUNCTION FIELD EFFECT TRANSISTOR WITH MONOLAYERS IN CHANNEL REGION

5083261	21-Jan-09	DYNAMICALLY ALTERABLE INTERRUPT PRIORITY CIRCUIT
5084407	03-Jun-11	METHOD FOR PLANARIZING ISOLATED REGIONS
5084665	04-Jun-10	VOLTAGE REFERENCE CIRCUIT WITH POWER SUPPLY COMPENSATION
5084814	28-Jan-09	DATA PROCESSOR WITH DEVELOPMENT SUPPORT FEATURES
5086407	05-Jun-09	DATA PROCESSOR INTEGRATED CIRCUIT WITH SELECTABLE MULTIPLEXED/NON-MULTIPLEXED ADDRESS AND DATA MODES OF OPERATION
5086966	05-Nov-10	PALLADIUM-COATED SOLDER BALL
5089722	02-Apr-10	HIGH SPEED OUTPUT BUFFER CIRCUIT WITH OVERLAP CURRENT CONTROL
5093612	05-Jul-10	SECONDARY VOLTAGE SUPPLY AND VOLTAGE CLAMPING CIRCUIT
5095287	24-Jan-11	PHASE LOCKED LOOP HAVING A CHARGE PUMP WITH RESET
5099240	17-Sep-10	SUBRANGING ADC WITH ERROR CORRECTION THROUGH INCREASED FINE STEP SPAN AND NOISE REDUCING LAYOUT
5099445	26-Dec-09	VARIABLE LENGTH SHIFTER FOR PERFORMING MULTIPLE SHIFT AND SELECT FUNCTIONS
5103123	17-Sep-10	PHASE DETECTOR HAVING ALL NPN TRANSISTORS
5105175	12-Mar-11	RESONANT CIRCUIT ELEMENT HAVING INSIGNIFICANT MICROPHONIC EFFECTS
5105250	09-Oct-10	HETEROJUNCTION BIPOLAR TRANSISTOR WITH A THIN SILICON EMITTER
5107148	12-Apr-11	BIDIRECTIONAL BUFFER HAVING TRI-STATE BUFFERS FOR CIRCUIT ISOLATION
5107190	22-Jun-10	MEANS AND METHOD FOR OPTIMIZING THE SWITCHING PERFORMANCE OF POWER AMPLIFIERS
5112772	27-Sep-11	METHOD OF FABRICATING A TRENCH STRUCTURE
5113156	22-Apr-11	LOW POWER CRYSTAL OSCILLATOR WITH AUTOMATIC GAIN CONTROL
5113189	21-Jun-11	FREQUENCY TRANSLATING COHERENT ANALOG TO DIGITAL CONVERSION SYSTEM FOR MODULATED SIGNALS
5115475	04-Jun-10	AUTOMATIC SEMICONDUCTOR PACKAGE INSPECTION METHOD
5115506	05-Jan-10	METHOD AND APPARATUS FOR PREVENTING RECURSION JEOPARDY
5116460	12-Apr-11	METHOD FOR SELECTIVELY ETCHING A FEATURE
5116774	22-Mar-11	HETEROJUNCTION METHOD AND STRUCTURE
5117274	26-May-09	MERGED COMPLEMENTARY BIPOLAR AND MOS MEANS AND METHOD
5117498	26-May-09	PROCESSOR WITH FLEXIBLE RETURN FROM SUBROUTINE
5119149	22-Oct-10	GATE-DRAIN SHIELD REDUCES GATE TO DRAIN CAPACITANCE
5119171	02-Jun-09	SEMICONDUCTOR DIE HAVING ROUNDED OR TAPERED EDGES AND CORNERS
5120678	05-Nov-10	ELECTRICAL COMPONENT PACKAGE COMPRISING POLYMER-REINFORCED SOLDER BUMP INTERCONNECTIONS
5121010	14-Feb-11	PHASE DETECTOR WITH DEADZONE WINDOW
5122799	24-Dec-10	MULTI-MODULATOR DIGITAL-TO-ANALOG CONVERTER
5123826	13-Feb-10	MOLDING POT HAVING CONFIGURED BOTTOM
5124632	01-Jul-11	LOW-VOLTAGE PRECISION CURRENT GENERATOR

5124639	20-Nov-10	PROBE CARD APPARATUS HAVING A HEATING ELEMENT AND PROCESS FOR USING THE SAME
5124704	17-Sep-10	A/D CONVERTER WITH TEST CIRCUIT
5126283	21-May-10	PROCESS FOR THE SELECTIVE ENCAPSULATION OF AN ELECTRICALLY CONDUCTIVE STRUCTURE IN A SEMICONDUCTOR DEVICE
5126594	17-Jul-11	VOLTAGE SPIKE DETECTION CIRCUIT FOR USE IN DETECTING CLOCK EDGE TRANSITIONS WITHIN A SERIAL COMMUNICATION SYSTEM
5126596	18-Mar-11	TRANSMISSION GATE HAVING A PASS TRANSISTOR WITH FEEDBACK
5126691	17-Jun-11	VARIABLE CLOCK DELAY CIRCUIT
5127002	17-Jul-11	TIME SLOT ASSIGNER FOR USE IN A SERIAL COMMUNICATION SYSTEM
5127089	03-Jul-09	SYNCHRONOUS BUS LOCK MECHANISM PERMITTING BUS ARBITER TO CHANGE BUS MASTER DURING A PLURALITY OF SUCCESSIVE LOCKED OPERAND TRANSFER SEQUENCES AFTER COMPLETION OF CURRENT SEQUENCE
5128630	03-May-11	HIGH SPEED FULLY DIFFERENTIAL OPERATIONAL AMPLIFIER
5128632	16-May-11	ADAPTIVE LOCK TIME CONTROLLER FOR A FREQUENCY SYNTHESIZER AND METHOD THEREFOR
5128890	06-May-11	APPARATUS FOR PERFORMING MULTIPLICATIONS WITH REDUCED POWER AND A METHOD THEREFOR
5129009	04-Jun-10	METHOD FOR AUTOMATIC SEMICONDUCTOR WAFER INSPECTION
5129078	07-Jul-09	DEDICATED SERVICE PROCESSOR WITH INTER-CHANNEL COMMUNICATION FEATURES
5130276	16-May-11	METHOD OF FABRICATING SURFACE MICROMACHINED STRUCTURES
5130635	19-Aug-11	VOLTAGE REGULATOR HAVING BIAS CURRENT CONTROL CIRCUIT
5130674	30-Sep-11	VOLTAGE CONTROLLED OSCILLATOR HAVING CONTROLLED BIAS VOLTAGE, AGC AND OUTPUT AMPLIFIER
5130769	16-May-11	NONVOLATILE MEMORY CELL
5130947	22-Oct-10	MEMORY SYSTEM FOR RELIABLY WRITING ADDRESSES WITH REDUCED POWER CONSUMPTION
5134089	30-Sep-11	MOS TRANSISTOR ISOLATION METHOD
5134448	29-Jan-10	MOSFET WITH SUBSTRATE SOURCE CONTACT
5136366	05-Nov-10	OVERMOLDED SEMICONDUCTOR PACKAGE WITH ANCHORING MEANS
5137362	26-Mar-10	AUTOMATIC PACKAGE INSPECTION METHOD
5138709	11-Apr-10	SPURIOUS INTERRUPT MONITOR
5139571	24-Apr-11	NON-CONTAMINATING WAFER POLISHING SLURRY
5139728	13-Feb-10	METHOD OF USING MOLDING POT HAVING CONFIGURED BOTTOM
5140286	02-Aug-11	AN OSCILLATOR
5142239	20-May-11	HIGH FREQUENCY LINEAR AMPLIFIER ASSEMBLY
5142450	12-Apr-11	NON-CONTACT LEAD DESIGN AND PACKAGE
5143858	02-Apr-10	METHOD OF FABRICATING BURIED INSULATING LAYERS
5146389	22-Jul-11	DIFFERENTIAL CAPACITOR STRUCTURE AND METHOD

5147815	14-May-10	METHOD FOR FABRICATING A MULT ICHIP SEMICONDUCTOR DEVICE HAV ING TWO INTERDIGITATED LEAD FR AMES
5147821	28-Sep-10	METHOD FOR MAKING A THERMALLY ENHANCED SEMICONDUCTOR DEVICE BY HOLDING A LEADFRAME AGAINST A HEATSINK THROUGH VACUUM SUC TION IN A MOLDING OPERATION
5148047	11-Jun-10	CMOS BUS DRIVER CIRCUIT WITH I MPROVED SPEED
5148436	15-Oct-10	CIRCUIT FOR DETECTING FALSE RE AD DATA FROM EPROM
5148968	11-Feb-11	SOLDER BUMP STRETCH DEVICE
5149674	17-Jun-11	METHOD FOR MAKING A PLANAR MUL TI-LAYER METAL BONDING PAD
5150075	03-Jun-11	POWER AMPLIFIER RAMP UP METHOD AND APPARATUS
5151879	27-Dec-10	SENSE AMPLIFIER WITH LATCH
5151986	29-Sep-09	MICROCOMPUTER WITH ON-BOARD CH IP SELECTS AND PROGRAMMABLE BU S STRETCHING
5153451	19-Aug-11	FAIL SAFE TYPE LEVEL SHIFTER
5153854	06-Oct-09	EEPROM MEMORY SYSTEM HAVING SELECTABLE PROGRAMMING VOLTAGE FOR LOW POWER READABILITY
5154946	27-Sep-10	CMOS STRUCTURE FABRICATION
5155065	16-Mar-12	UNIVERSAL PAD PITCH LAYOUT
5155386	03-Jun-11	PROGRAMMABLE HYSTERESIS COMPAR ATOR
5155390	25-Jul-11	PROGRAMMABLE BLOCK ARCHITECTED HETEROGENEOUS INTEGRATED CIRC UIT
5155392	05-Nov-10	LOW DI/DT BICMOS OUTPUT BUFFER WITH IMPROVED SPEED
5155398	21-Dec-10	CONTROL CIRCUIT FOR HIGH POWER SWITCHING TRANSISTOR
5155451	18-Feb-12	CIRCUIT AND METHOD FOR DYNAMIC ALLY GENERATING A CLOCK SIGNAL
5155563	18-Mar-11	SEMICONDUCTOR DEVICE HAVING LO W SOURCE INDUCTANCE
5155824	13-Oct-09	SYSTEM FOR TRANSFERRING SELECT ED DATA WORDS BETWEEN MAIN MEM ORY AND CACHE WITH MULTIPLE DA TA WORDS AND MULTIPLE DIRTY BI TS FOR EACH ADDRESS
5159572	24-Dec-10	DRAM ARCHITECTURE HAVING DISTR IBUTED ADDRESS DECODING AND TI MING CONTROL
5160409	05-Aug-11	SOLDER PLAT REFLOW METHOD FOR A SOLDER BUMP ON A CIRCUIT TRACE INTERSECTION
5160894	17-Jan-12	DIGITAL FREQUENCY SYNTHESIZER AND METHOD OF FREQUENCY SYNTHE SIS
5162672	24-Dec-10	DATA PROCESSOR HAVING AN OUTPU T TERMINAL WITH SELECTABLE OUT PUT IMPEDANCES
5164328	25-Jun-10	METHOD OF BUMP BONDING AND SEA LING AN ACCELEROMETER CHIP ONT O AN INTERGRATED CIRCUIT CHIP
5164659	29-Aug-11	SWITCHING CIRCUIT
5164683	21-Oct-11	RF AMPLIFIER ASSEMBLY
5164885	21-Nov-11	ELECTRONIC PACKAGE HAVING A NO N-OXIDE CERAMIC BONDED TO META L AND METHOD FOR MAKING
5165002	27-Nov-11	METHOD OF COUPLING AN ELECTRIC AL SIGNAL TO AN OPTICAL FIBER
5165058	05-Jul-10	VOLTAGE COMPARATOR WITH SAMPLE HOLD CIRCUIT

5166084	03-Sep-11	PROCESS FOR FABRICATING A SILICON ON INSULATOR FIELD EFFECT TRANSISTOR
5166685	04-Sep-10	AUTOMATIC SELECTION OF EXTERNAL MULTIPLEXER CHANNELS BY AN A/D CONVERTER INTEGRATED CIRCUIT
5166741	29-Apr-11	OPTOELECTRONIC SPEED/DIRECTION DETECTOR
5168180	20-Apr-12	LOW FREQUENCY FILTER IN A MONOLITHIC INTEGRATED CIRCUIT
5168276	04-Sep-10	AUTOMATIC A/D CONVERTER OPERATION USING A PROGRAMMABLE CONTROL TABLE
5168466	04-Mar-11	BIAS CURRENT GENERATOR CIRCUIT FOR A SENSE AMPLIFIER
5170475	08-Dec-09	DATA PROCESSOR HAVING INSTRUCTIONS FOR INTERPOLATING BETWEEN MEMORY-RESIDENT DATA VALUES RESPECTIVELY
5172050	15-Feb-11	MICROMACHINED SEMICONDUCTOR PROBE CARD
5172078	06-Feb-12	DIFFERENTIAL OSCILLATOR WITH COMMON MODE CONTROL
5172214	06-Feb-11	LEADLESS SEMICONDUCTOR DEVICE AND METHOD FOR MAKING THE SAME
5172409	02-Jul-10	PRECISION FET CONTROL LOOP
5173449	22-Dec-09	METALLIZATION PROCESS
5173617	22-Dec-09	DIGITAL PHASE LOCK CLOCK GENERATOR WITHOUT LOCAL OSCILLATOR
5173764	08-Apr-11	SEMICONDUCTOR DEVICE HAVING A PARTICULAR LID MEANS AND ENCAPSULANT TO REDUCE DIE STRESS
5173836	05-Mar-12	A HERMETICALLY SEALED INTERFACE
5174942	29-May-11	METHOD AND APPARATUS FOR PRODUCING FLASHLESS AXIAL LEADED DEVICES
5175007	28-May-11	MOLD ASSEMBLY WITH SEPARATE ENCAPSULATING CAVITIES
5175117	23-Dec-11	METHOD FOR MAKING BURIED ISOLATION
5175124	25-Mar-11	PROCESS FOR FABRICATING A SEMICONDUCTOR DEVICE USING RE-IONIZED RINSE WATER
5175129	01-Mar-11	METHOD OF FABRICATING A SEMICONDUCTOR STRUCTURE HAVING AN IMPROVED POLYSILICON LAYER
5175729	05-Jun-11	RADIO AND FAST LOCK PHASE LOCKED LOOP
5177376	10-Jan-12	ZERO TEMPERATURE COEFFICIENT COMPARATOR CIRCUIT WITH HYSTERESIS
5177438	02-Aug-11	LOW RESISTANCE PROBE FOR SEMICONDUCTORS
5177439	30-Aug-11	PROBE-CARD FOR TESTING UNENCAPSULATED SEMICONDUCTOR DEVICES
5179429	30-Mar-12	MAGNETIC FIELD SENSOR WITH SPLIT COLLECTOR CONTACTS FOR HIGH SENSITIVITY
5180935	09-Nov-10	DIGITAL TIMING DISCRIMINATOR
5181156	14-May-12	MICROMACHINED CAPACITOR STRUCTURE AND METHOD FOR MAKING
5184028	15-Jun-12	CURRENT COMPENSATING CHARGE PUMP CIRCUIT
5184768	29-Nov-10	SOLDER INTERCONNECTION VERIFICATION
5185278	22-Oct-10	METHOD OF MAKING SELF-ALIGNED GATE PROVIDING IMPROVED BREAKDOWN VOLTAGE
5185689	29-Apr-12	CAPACITOR HAVING A RUTHENATE ELECTRODE AND METHOD OF FORMATION
5185694	09-Feb-10	DATA PROCESSING SYSTEM UTILIZES BLOCK MOVE

		INSTRUCTION FOR BURST TRANSFERRING BLOCKS OF DATA ENTRIES WHERE WIDTH OF DATA BLOCK VARIES
5186383	02-Oct-11	METHOD FOR FORMING SOLDER BUMP INTERCONNECTIONS TO A SOLDER-PLATED CIRCUIT TRACE
5187347	28-Jan-11	SUSCEPTOR ELECTRODE AND METHOD FOR MAKING SAME
5187394	13-Jan-12	CONFIGURABLE ROW DECODER DRIVER CIRCUIT
5187445	28-Oct-11	TUNING CIRCUIT FOR CONTINUOUS-TIME FILTERS AND METHOD THEREFOR
5187448	03-Feb-12	DIFFERENTIAL AMPLIFIER WITH COMMON-MODE STABILITY ENHANCEMENT
5187811	29-Jul-11	ERROR DETECTION
5188979	26-Aug-11	METHOD FOR FORMING A NITRIDE LAYER USING PREHEATED AMMONIA
5191321	09-May-10	SINGLE CELL BIMOS ELECTROLUMINESCENT DISPLAY DRIVER
5192871	15-Oct-11	VOLTAGE VARIABLE CAPACITOR HAVING AMORPHOUS DIELECTRIC FILM
5193177	09-Mar-10	FAULT INDICATING MICROCOMPUTER INTERFACE UNITS
5193217	23-Apr-10	LOW VOLTAGE IC FOR VOICE OPERATED TRANSCEIVER
5194137	05-Aug-11	SOLDER PLATE REFLOW METHOD FOR FORMING SOLDER-BUMPED TERMINALS
5194831	18-Feb-12	FULLY-DIFFERENTIAL RELAXATION-TYPE VOLTAGE CONTROLLED OSCILLATOR AND METHOD THEREFOR
5194833	15-Nov-11	AIRBRIDGE COMPENSATED MICROWAVE CONDUCTORS
5195655	24-May-11	INTEGRATED FLUID DISPENSE APPARATUS TO REDUCE CONTAMINATION
5197032	06-Jul-10	BICMOS BIT LINE LOAD FOR A MEMORY WITH IMPROVED RELIABILITY AND A METHOD THEREFOR
5198264	30-Mar-10	METHOD OF ADHERING POLYIMIDE TO A SUBSTRATE
5198375	23-Mar-12	METHOD FOR FORMING A BIPOLAR TRANSISTOR STRUCTURE
5198963	21-Nov-11	MULTIPLE INTEGRATED CIRCUIT MODULE WHICH SIMPLIFIES HANDLING AND TESTING
5199032	04-Sep-10	MICROCONTROLLER HAVING AN EPROM WITH A LOW VOLTAGE PROGRAM INHIBIT CIRCUIT
5199035	01-Oct-10	LOGIC CIRCUIT FOR RELIABILITY AND YIELD ENHANCEMENT
5200352	25-Nov-11	TRANSISTOR HAVING A LIGHTLY DOPED REGION AND METHOD OF FORMATION
5200362	31-Aug-10	METHOD OF ATTACHING CONDUCTIVE TRACES TO AN ENCAPSULATED SEMICONDUCTOR DIE USING A REMOVABLE TRANSFER FILM
5201056	02-May-10	RISC MICROPROCESSOR ARCHITECTURE WITH MULTI-BIT TAG EXTENDED INSTRUCTIONS FOR SELECTIVELY ATTACHING TAG FROM EITHER INSTRUCTION ON INPUT DATA TO A RITHMETIC OPERATION OUTPUT
5202626	25-Oct-11	ON CHIP SELF TEST CIRCUIT
5203076	23-Dec-11	VACUUM INFILTRATION OF UNDER FILL MATERIAL FOR FLIP CHIP DEVICES
5204277	03-Feb-12	METHOD OF FORMING BIPOLAR TRANSISTOR HAVING SUBSTRATE TO POLYSILICON EXTRINSIC BASE CONTACT
5206181	03-Jun-11	METHOD FOR MANUFACTURING A SEMICONDUCTOR DEVICE WITH A SLOTTED METAL TEST PAD TO PREVENT LIFT-OFF DURING WAFER SCRIBING

5206609	15-May-12	CURRENT CONTROLLED OSCILLATOR WITH LINEAR OUTPUT FREQUENCY
5207866	17-Jan-11	ANISOTROPIC SINGLE CRYSTAL SILICON ETCHING SOLUTION AND METHOD
5208168	26-Nov-10	SEMICONDUCTOR DEVICE HAVING PUNCH-THROUGH PROTECTED BURIED CONTACTS AND METHOD FOR MAKING THE SAME
5208189	30-Sep-11	PROCESS FOR PLUGGING DEFECTS IN A DIELECTRIC LAYER OF A SEMICONDUCTOR DEVICE
5210759	19-Nov-10	DATA PROCESSING SYSTEM HAVING SCAN TESTING USING SET LATCHES FOR SELECTIVELY OBSERVING TEST DATA
5210842	04-Feb-11	DATA PROCESSOR HAVING INSTRUCTION VARIED SET ASSOCIATIVE CACHE BOUNDARY ACCESSING
5212390	04-May-12	LEAD INSPECTION METHOD USING A PLANE OF LIGHT FOR PRODUCING REFLECTED LEAD IMAGES
5214389	06-Jan-12	MULTI-DIMENSIONAL HIGH-RESOLUTION PROBE FOR SEMICONDUCTOR MEASUREMENTS INCLUDING PIEZOELECTRIC TRANSDUCER ARRANGEMENT FOR CONTROLLING PROBE
5214705	01-Oct-11	CIRCUIT AND METHOD FOR COMMUNICATING DIGITAL AUDIO INFORMATION
5216278	04-Dec-10	SEMICONDUCTOR DEVICE HAVING A PAD ARRAY CARRIER PACKAGE
5216283	01-Jun-10	SEMICONDUCTOR DEVICE HAVING AN INSERTABLE HEAT SINK AND METHOD FOR MOUNTING THE SAME
5217568	03-Feb-12	SILICON ETCHING PROCESS USING POLYMERIC MASK FOR EXAMPLE TO FORM V-GROOVE FOR AN OPTICAL FIBER COUPLING
5217589	03-Oct-11	METHOD OF ADHERENT METAL COATING FOR ALUMINUM NITRIDE SURFACES
5217597	01-Apr-11	SOLDER BUMP TRANSFER METHOD
5218234	23-Dec-11	SEMICONDUCTOR DEVICE WITH CONTROLLED SPREAD POLYMERIC UNDERFILL
5218759	18-Mar-11	METHOD OF MAKING A TRANSFER MOLDED SEMICONDUCTOR DEVICE
5219117	01-Nov-11	METHOD OF TRANSFERRING SOLDER BALLS ONTO A SEMICONDUCTOR DEVICE
5219793	03-Jun-11	METHOD FOR FORMING PITCH INDEPENDENT CONTACTS AND A SEMICONDUCTOR DEVICE HAVING THE SAME
5220195	19-Dec-11	SEMICONDUCTOR DEVICE HAVING A MULTILAYER LEADFRAME WITH FULL POWER AND GROUND PLANES
5220326	30-Mar-12	DIGITAL-TO-ANALOG CONVERTER WITH IMPROVED PERFORMANCE AND METHOD THEREFOR
5220489	11-Oct-11	MULTI-COMPONENT INTEGRATED CIRCUIT PACKAGE
5220526	01-Mar-11	METHOD AND APPARATUS FOR INDICATING A DUPLICATION OF ENTRIES IN A CONTENT ADDRESSABLE STORAGE DEVICE
5221639	20-Dec-11	METHOD OF FABRICATING RESISTIVE CONDUCTIVE PATTERNS ON ALUMINUM NITRIDE SUBSTRATES
5221849	16-Jun-12	SEMICONDUCTOR DEVICE WITH ACTIVE QUANTUM WELL GATE
5221926	01-Jul-12	CIRCUIT AND METHOD FOR CANCELLING NONLINEARITY ERROR ASSOCIATED WITH COMPONENT VALUE MISMATCHES IN A DATA CONVERTER
5222014	02-Mar-12	THREE-DIMENSIONAL MULTI-CHIP PAD ARRAY CARRIER

5222066	26-Dec-10	MODULAR SELF-TEST FOR EMBEDDED SRAMS
5223728	02-Apr-12	OPTICAL SWITCH INTEGRATED CIRCUIT
5224198	30-Sep-11	WAVEGUIDE VIRTUAL IMAGE DISPLAY
5225365	30-Mar-12	METHOD OF MAKING A SUBSTANTIALLY PLANAR SEMICONDUCTOR SURFACE
5227340	13-Jul-10	PROCESS FOR FABRICATING SEMICONDUCTOR DEVICES USING A SOLID REACTANT SOURCE
5228039	13-Jul-10	SOURCE-LEVEL IN-CIRCUIT SOFTWARE CODE DEBUGGING INSTRUMENT
5229070	02-Jul-12	LOW TEMPERATURE WETTING TIN BASE SOLDER PASTE
5230184	05-Jul-11	DISTRIBUTED POLISHING HEAD
5231395	30-Mar-12	SIGMA-DELTA DIGITAL-TO-ANALOG CONVERTER WITH REDUCED DISTORTION
5232143	19-Jul-11	MULTI-CHIP DIE BONDER
5233222	27-Jul-12	SEMICONDUCTOR DEVICE HAVING WINDOW-FRAME FLAG TAPERED EDGE IN OPENING
5233504	06-Dec-10	NONCOLLAPSING MULTISOLDER INTERCONNECTION
5233510	27-Sep-11	CONTINUOUSLY SELF CONFIGURING DISTRIBUTED CONTROL SYSTEM
5233565	26-Dec-10	LOW POWER BICMOS MEMORY USING ADDRESS TRANSITION DETECTION AND A METHOD THEREFOR
5233573	01-Jul-12	DIGITAL DATA PROCESSOR INCLUDING APPARATUS FOR COLLECTING TIME-RELATED INFORMATION
5235215	13-Dec-11	MEMORY DEVICE FOR USE IN POWER CONTROL CIRCUITS
5235334	30-Mar-12	DIGITAL-TO-ANALOG CONVERTER WITH A LINEAR INTERPOLATOR
5239198	30-Apr-12	OVERMOLDED SEMICONDUCTOR DEVICE HAVING SOLDER BALL AND EDGE LEAD CONNECTIVE STRUCTURE
5240165	06-Jul-12	METHOD AND APPARATUS FOR CONTROLLED DEFORMATION BONDING
5241133	21-Dec-10	LEADLESS PAD ARRAY CHIP CARRIER
5241492	06-May-11	APPARATUS FOR PERFORMING MULTIPLY AND ACCUMULATE INSTRUCTIONS WITH REDUCED POWER AND A METHOD THEREFOR
5241503	25-Feb-11	DYNAMIC RANDOM ACCESS MEMORY WITH IMPROVED PAGE-MODE PERFORMANCE AND METHOD THEREFOR HAVING ISOLATOR BETWEEN MEMORY CELLS AND SENSE AMPLIFIERS
5243348	27-Apr-12	PARTITIONED DIGITAL ENCODER AND METHOD FOR ENCODING BIT GROUPS IN PARALLEL
5243498	26-May-12	A MULTI-CHIP SEMICONDUCTOR MODULE AND METHOD FOR MAKING AND TESTING
5245273	30-Oct-11	BANDGAP VOLTAGE REFERENCE CIRCUIT
5245646	01-Jun-12	TUNING CIRCUIT FOR USE WITH AN INTEGRATED CONTINUOUS TIME ANALOG FILTER
5247423	26-May-12	STACKING THREE DIMENSIONAL LEADLESS MULTI-CHIP MODULE AND METHOD FOR MAKING THE SAME
5249280	28-Sep-10	MICROCOMPUTER HAVING A MEMORY BANK SWITCHING APPARATUS FOR ACCESSING A SELECTED MEMORY BANK IN AN EXTERNAL MEMORY
5249465	11-Dec-10	ACCELEROMETER UTILIZING AN ANNUAL MASS
5251304	05-Oct-10	INTEGRATED CIRCUIT MICROCONTROLLER WITH ON-CHIP

		MEMORY AND EXTERNAL BUS INTERFACE AND PROGRAMMABLE MECHANISM FOR SECURING THE CONTENTS OF ON-CHIP MEMORY
5252848	03-Feb-12	LOW ON RESISTANCE FIELD EFFECT TRANSISTOR
5254217	27-Jul-12	METHOD FOR FABRICATING A SEMICONDUCTOR DEVICE HAVING A CONDUCTIVE METAL OXIDE
5254491	23-Sep-11	SEMICONDUCTOR DEVICE HAVING IMPROVED FREQUENCY RESPONSE
5255839	02-Jan-12	METHOD AND APPARATUS FOR SOLDER APPLICATION AND REFLOW
5256588	23-Mar-12	METHOD FOR FORMING A TRANSISTOR AND A CAPACITOR FOR USE IN A VERTICALLY STACKED DYNAMIC RANDOM ACCESS MEMORY CELL
5256599	01-Jun-12	SEMICONDUCTOR WAFER WAX MOUNTING AND THINNING PROCESS
5257357	22-Jan-11	METHOD AND APPARATUS FOR IMPLEMENTING A PRIORITY ADJUSTMENT OF AN INTERRUPT IN A DATA PROCESSOR
5258648	27-Jun-11	COMPOSITE FLIP CHIP SEMICONDUCTOR DEVICE WITH AN INTERPOSER HAVING TEST CONTACTS FORMED ALONG ITS PERIPHERY
5258703	03-Aug-12	TEMPERATURE COMPENSATED VOLTAGE REGULATOR HAVING BETA COMPENSATION
5258948	03-Feb-12	MEMORY CELL SENSE TECHNIQUE
5258985	12-Nov-11	COMBINATIONAL DATA GENERATOR AND ANALYZER FOR BUILT-IN SELF TEST
5258999	03-Oct-11	CIRCUIT AND METHOD FOR RECEIVING AND TRANSMITTING CONTROL AND STATUS INFORMATION
5259001	17-Dec-11	ADPCM DECODER WITH AN INTEGRAL DIGITAL RECEIVE GAIN AND METHOD THEREFOR
5260596	08-Apr-11	MONOLITHIC CIRCUIT WITH INTEGRATED BULK STRUCTURE RESONATOR
5262352	31-Aug-12	METHOD FOR FORMING AN INTERCONNECTION STRUCTURE FOR CONDUCTIVE LAYERS
5262353	03-Feb-12	PROCESS FOR FORMING A STRUCTURE WHICH ELECTRICALLY SHIELDS CONDUCTORS
5263125	17-Jun-12	CIRCUIT AND METHOD FOR EVALUATING FUZZY LOGIC RULES
5263168	03-Jun-11	CIRCUITRY FOR AUTOMATICALLY ENTERING AND TERMINATING AN INITIALIZATION MODE IN A DATA PROCESSING SYSTEM IN RESPONSE TO A CONTROL SIGNAL
5263196	19-Nov-10	METHOD AND APPARATUS FOR COMPENSATION OF IMBALANCE IN ZERO-IF DOWNCONVERTERS
5265256	01-Jul-11	DATA PROCESSING SYSTEM HAVING A PROGRAMMABLE MODE FOR SELECTING OPERATION AT ONE OF A PLURALITY OF POWER SUPPLY POTENTIALS
5265258	19-Mar-11	PARTIAL-SIZED PRIORITY ENCODER CIRCUIT HAVING LOOK-AHEAD CAPABILITY
5266512	23-Oct-11	METHOD FOR FORMING A NESTED SURFACE CAPACITOR
5268065	21-Dec-12	METHOD FOR THINNING A SEMICONDUCTOR WAFER
5268312	22-Oct-12	METHOD OF FORMING ISOLATED WELLS IN THE FABRICATION OF BICMOS DEVICES
5268590	07-Dec-10	CMOS DEVICE AND PROCESS
5268863	06-Jul-12	MEMORY HAVING A WRITE ENABLE CONTROLLED WORD LINE

5268866	02-Mar-12	MEMORY WITH COLUMN REDUNDANCY AND LOCALIZED COLUMN REDUNDANCY CONTROL SIGNALS
5268951	22-Dec-12	X-RAY BEAM SCANNING METHOD FOR PRODUCING LOW DISTORTION OR CONSTANT DISTORTION IN X-RAY PROXIMITY PRINTING
5268995	07-Dec-10	METHOD FOR EXECUTING GRAPHICS Z-COMPARE AND PIXEL MERGE INSTRUCTIONS IN A DATE PROCESSOR
5272117	07-Dec-12	METHOD FOR PLANARIZING A LAYER OF MATERIAL
5272453	03-Aug-12	METHOD AND APPARATUS FOR SWITCHING BETWEEN GAIN CURVES OF A VOLTAGE CONTROLLED OSCILLATOR
5272531	07-Dec-12	AN AUTOMATIC GAIN CONTROL SYSTEM FOR USE IN POSITIVE MODULATION WHICH DETECTS THE PEAK WHITE VOLTAGE LEVEL SLOWLY WHILE SIMULTANEOUSLY ADJUSTING BLACK VOLTAGE LEVEL FLUCTUATIONS QUICKLY
5273615	06-Apr-12	APPARATUS AND METHOD FOR HANDLING FRAGILE SEMICONDUCTOR WAFERS
5273850	04-Nov-11	CHROMELESS PHASE-SHIFT MASK AND METHOD FOR MAKING
5273915	05-Oct-12	METHOD FOR FABRICATING BIPOLAR JUNCTION AND MOS TRANSISTORS ON SOI
5273922	11-Sep-12	HIGH SPEED LOW GATE/DRAIN CAPACITANCE DMOS DEVICE
5273930	03-Sep-12	METHOD OF FORMING A NON-SELECTIVE SILICON-GERMANIUM EPITAXIAL FILM
5273938	09-Sep-11	METHOD FOR ATTACHING CONDUCTIVE TRACES TO PLURAL, STACKED, ENCAPSULATED SEMICONDUCTOR DIE USING A REMOVABLE TRANSFER FILM
5273940	15-Jun-12	MULTIPLE CHIP PACKAGE WITH THINNED SEMICONDUCTOR CHIPS
5275964	04-Nov-11	METHOD FOR COMPACTLY LAYING OUT A PAIR OF TRANSISTORS
5275971	20-Apr-12	METHOD OF FORMING AN OHMIC CONTACT TO III-V SEMICONDUCTOR MATERIALS
5275973	01-Mar-13	METHOD FOR FORMING METALLIZATION IN AN INTEGRATED CIRCUIT
5276366	02-Oct-12	DIGITAL VOLTAGE LEVEL TRANSLATOR CIRCUIT
5276824	04-Jan-11	DATA PROCESSOR HAVING A MULTI-STAGE INSTRUCTION PIPE AND SELECTION LOGIC RESPONSIVE TO AN INSTRUCTION DECODER FOR SELECTING ONE STAGE OF THE INSTRUCTION PIPE
5278726	22-Jan-12	METHOD AND APPARATUS FOR PARTIALLY OVERMOLDED INTEGRATED CIRCUIT PACKAGE
5278874	02-Sep-12	PHASE LOCK LOOP FREQUENCY CORRECTION CIRCUIT
5278994	03-Jun-11	POWER AMPLIFIER SATURATION DETECTION AND CORRECTION METHOD AND APPARATUS
5279978	18-Jan-11	PROCESS FOR MAKING BICMOS DEVICE HAVING AN SOI SUBSTRATE
5280193	04-May-12	REPAIRABLE SEMICONDUCTOR MULTI-PACKAGE MODULE HAVING INDIVIDUALIZED PACKAGE BODIES ON A PC BOARD SUBSTRATE
5281684	30-Apr-12	SOLDER BUMPING OF INTEGRATED CIRCUIT DIE
5281864	10-Apr-12	IMPLEMENTATION OF THE IEEE 11491 BOUNDARY-SCAN ARCHITECTURE
5281867	23-Feb-13	MULTIPLE CHANNEL SAMPLING CIRCUIT HAVING MINIMIZED CROSSTALK INTERFERENCE

5283454	11-Sep-12	A SEMICONDUCTOR DEVICE INCLUDING VERY LOW SHEET RESISTIVITY BURIED LAYER AND METHOD OF FORMATION
5283484	13-Oct-12	VOLTAGE LIMITER AND SINGLE-ENDED TO DIFFERENTIAL CONVERTER USING SAME
5283580	28-Sep-12	CURRENT/RESISTOR DIGITAL-TO-ANALOG CONVERTER HAVING ENHANCED INTEGRAL LINEARITY AND METHOD OF OPERATION
5283753	13-Oct-09	FIRM FUNCTION BLOCK FOR A PROGRAMMABLE BLOCK ARCHITECTED HETEROGENEOUS INTEGRATED CIRCUIT
5284287	31-Aug-12	METHOD FOR ATTACHING CONDUCTIVE BALLS TO A SUBSTRATE
5285352	15-Jul-12	PAD ARRAY SEMICONDUCTOR DEVICE WITH THERMAL CONDUCTOR AND PROCESS FOR MAKING THE SAME
5286674	02-Mar-12	METHOD FOR FORMING A VIA STRUCTURE AND SEMICONDUCTOR DEVICE HAVING THE SAME
5287002	17-Jun-11	A PLANAR MULTI-LAYER METAL BONDING PAD
5289415	17-Apr-12	SENSE AMPLIFIER AND LATCHING CIRCUIT FOR AN SRAM
5291053	06-Jul-12	SEMICONDUCTOR DEVICE HAVING AN OVERLAPPING MEMORY CELL
5291062	01-Mar-13	AREA ARRAY SEMICONDUCTOR DEVICE HAVING A LID WITH FUNCTIONAL CONTACTS
5291076	31-Aug-12	DECODER/COMPARATOR AND METHOD OF OPERATION
5291438	23-Mar-12	TRANSISTOR AND A CAPACITOR USED FOR FORMING A VERTICALLY STACKED DYNAMIC RANDOM ACCESS MEMORY CELL
5291455	08-May-12	MEMORY HAVING DISTRIBUTED REFERENCE AND BIAS VOLTAGES
5291607	01-Mar-11	MICROPROCESSOR HAVING ENVIRONMENTAL SENSING CAPABILITY
5293067	23-May-11	INTEGRATED CIRCUIT CHIP CARRIER
5293167	08-Mar-11	AUTOMATIC A/D CONVERTER OPERATION WITH SELECTABLE RESULT FOR MAT
5293628	04-Nov-11	DATA PROCESSING SYSTEM WHICH GENERATES A WAVEFORM WITH IMPROVED PULSE WIDTH RESOLUTION
5294827	14-Dec-12	SEMICONDUCTOR DEVICE HAVING THIN PACKAGE BODY AND METHOD FOR MAKING THE SAME
5294845	15-Mar-11	DATA PROCESSOR HAVING AN OUTPUT TERMINAL WITH SELECTABLE OUTPUT IMPEDANCES
5295229	17-Jun-12	CIRCUIT AND METHOD FOR DETERMINING MEMBERSHIP IN A SET DURING A FUZZY LOGIC OPERATION
5296736	21-Dec-12	LEVELED NON-COPLANAR SEMICONDUCTOR DIE CONTACTS
5296738	08-Jul-11	MOISTURE RELIEF FOR CHIP CARRIER
5298441	03-Jun-11	METHOD OF MAKING HIGH TRANSDUCTANCE HETEROSTRUCTURE FIELD EFFECT TRANSISTOR
5299460	24-Feb-12	PRESSURE SENSOR
5300454	24-Nov-12	METHOD FOR FORMING DOPED REGIONS WITHIN A SEMICONDUCTOR SUBSTRATE
5300808	04-May-12	EPROM PACKAGE AND METHOD OF OPTICALLY ERASING
5301335	01-Jul-12	REGISTER WITH SELECTIVE WAIT FEATURE
5301345	03-May-11	DATA PROCESSING SYSTEM FOR PERFORMING A SHIFTING OPERATION AND A CONSTANT GENERATION OPERATION AND METHOD THEREFOR

5302849	01-Mar-13	PLASTICLAND GRID ARRAY SEMICONDUCTOR DEVICE AND METHOD FOR MAKING THE SAME
5302952	28-Aug-12	AUTOMATIC A/D CONVERTER OPERATION WITH PAUSE CAPABILITY
5303191	23-Jan-12	MEMORY WITH COMPENSATION FOR VOLTAGE, TEMPERATURE, AND PROCESSING VARIATIONS
5303355	12-Apr-11	PIPELINED DATA PROCESSOR WHICH CONDITIONALLY EXECUTES A PREDETERMINED LOOPING INSTRUCTION IN HARDWARE
5304855	08-Sep-12	BI-LEVEL PULSE ACCUMULATOR
5304860	29-Apr-12	A METHOD FOR POWERING DOWN A MICROPROCESSOR EMBEDDED WITHIN A GATE ARRAY
5304953	01-Jun-13	LOCK RECOVERY CIRCUIT FOR A PHASE LOCKED LOOP
5308741	31-Jul-12	LITHOGRAPHIC METHOD USING DOUBLE EXPOSURE TECHNIQUES, MASK POSITION SHIFTING AND LIGHT PHASE SHIFTING
5308778	02-Mar-12	METHOD OF FORMATION OF TRANSISTOR AND LOGIC GATES
5308788	13-Sep-11	TEMPERATURE CONTROLLED PROCESS FOR THE EPITAXIAL GROWTH OF A FILM OF MATERIAL
5309014	02-Apr-12	A TRANSISTOR PACKAGE
5309019	26-Feb-13	LOW INDUCTANCE LEAD FRAME FOR A SEMICONDUCTOR PACKAGE
5309322	13-Oct-12	LEADFRAME STRIP FOR SEMICONDUCTOR PACKAGES AND METHOD
5309484	01-Sep-12	METHOD AND APPARATUS FOR ASYNCHRONOUS TIMING RECOVERY USING INTERPOLATION FILTER
5310626	01-Mar-13	METHOD FOR FORMING A PATTERNED LAYER USING DIELECTRIC MATERIALS AS A LIGHT-SENSITIVE MATERIAL
5310689	10-May-11	METHOD OF FORMING A SIMOX STRUCTURE
5311057	27-Nov-12	LEAD-ON-CHIP SEMICONDUCTOR DEVICE AND METHOD FOR MAKING THE SAME
5311059	24-Jan-12	BACK-PLANE GROUNDING FOR FLIP CHIP INTEGRATED CIRCUIT
5311061	19-May-13	ALIGNMENT KEY FOR A SEMICONDUCTOR DEVICE HAVING A SEAL AGAINST IONIC CONTAMINATION
5312764	28-May-13	METHOD OF DOPING A SEMICONDUCTOR SUBSTRATE
5313089	26-May-12	CAPACITOR AND A MEMORY CELL FORMED THEREFROM
5313365	30-Jun-12	POLYMER COMPOSITE-GLOTTOPPED PAD ARRAY CARRIER
5313835	19-Dec-11	INTEGRATED MONOLITHIC GYROSCOPES/ACCELEROMETERS WITH LOGIC CIRCUITS
5316205	05-Apr-13	METHOD FOR FORMING GOLD BUMP CONNECTION USING TIN-BISMUTH SOLDER
5317185	31-May-11	SEMICONDUCTOR DEVICE HAVING STRUCTURES TO REDUCE STRESS NOTCHING EFFECTS IN CONDUCTIVE LINES AND METHOD FOR MAKING THE SAME
5317522	15-Jan-12	METHOD AND APPARATUS FOR NOISE BURST DETECTION IN A SIGNAL PROCESSOR
5319232	26-Oct-12	TRANSISTOR HAVING A LIGHTLY DOPED REGION
5319242	18-Mar-12	SEMICONDUCTOR PACKAGE HAVING AN EXPOSED DIE SURFACE
5319573	15-Jan-12	METHOD AND APPARATUS FOR NOISE BURST DETECTION IN A SIGNAL PROCESSOR

5319763	07-Jun-11	DATA PROCESSOR WITH CONCURRENT STATIC AND DYNAMIC MASKING OF OPERAND INFORMATION AND METHOD THEREFOR
5320272	02-Apr-13	PROCESS FOR FORMING TIN- BISMUTH SOLDER CONNECTION HAVING IMPROVED HIGH TEMPERATURE PROPERTIES
5321605	14-Jun-11	PROCESS FLOW INFORMATION MANAG EMENT SYSTEM
5323051	16-Dec-11	SEMICONDUCTOR WAFER LEVEL PACK AGE
5323157	15-Jan-13	SIGMA-DELTA DIGITAL-TO-ANALOG CONVERTER WITH REDUCED NOISE
5323360	03-May-13	LOCALIZED ATD SUMMATION FOR A MEMORY
5323947	03-May-13	METHOD AND APPARATUS FOR USE I N FORMING PRE-POSITIONED SOLDE R BUMPS ON A PAD ARRANGEMENT
5324690	01-Feb-13	SEMICONDUCTOR DEVICE HAVING A TERNARY BORON NITRIDE FILM AND A METHOD FOR FORMING THE SAME
5325065	18-May-12	DETECTION CIRCUIT UTILIZING A DUMMY INTEGRATOR
5325341	31-Aug-12	DIGITAL TIMER APPARATUS AND ME THOD
5326453	19-Feb-13	METHOD AND SOLUTION FOR ELECTR ODEPOSITION OF A DENSE, REFLEC TIVE TIN OR TIN-LEAD ALLOY
5326985	29-Mar-11	BIPOLAR DOPED SEMICONDUCTOR ST RUCTURE AND METHOD FOR MAKING
5327008	22-Mar-13	SEMICONDUCTOR DEVICE HAVING UN IVERSAL LOW-STRESS DIE SUPPORT AND METHOD FOR MAKING THE SAM E
5327013	30-Apr-12	SOLDER BUMPING OF INTEGRATED CIRCUIT DIE
5327103	29-Jun-13	A LOCK DETECTION CIRCUIT FOR A PHASE LOCK LOOP
5327133	16-Feb-13	DIGITAL INTEGRATOR WITH REDUCE D CIRCUIT AREA AND ANALOG-TO-D IGITAL CONVERTER USING SAME
5329246	25-Oct-13	CIRCUIT AND METHOD OF SETTING A BIAS POINT FOR A SINGLE-ENDE D AMPLIFIER DURING POWER-UP
5329282	02-Mar-12	MULTI-BIT SIGMA-DELTA ANALOG-T O-DIGITAL CONVERTER WITH REDUC ED SENSITIVITY TO DAC NONLINEA RITIES
5329486	24-Apr-12	FERROMAGNETIC MEMORY DEVICE
5329621	12-Jul-11	MICROPROCESSOR WHICH OPTIMIZES BUS UTILIZATION BASED UPON BU S SPEED
5329734	30-Apr-13	POLISHING PADS USED TO CHEMICA L-MECHANICAL POLISH A SEMICOND UCTOR SUBSTRATE
5329815	19-Dec-11	VIBRATION MONOLITHIC GYROSCOPE
5331205	21-Feb-12	MOLDED PLASTIC PACKAGE WITH WI RE PROTECTION
5332653	01-Jul-12	PROCESS FOR FORMING A CONDUCTI VE REGION WITHOUT PHOTORESIST- RELATED REFLECTIVE NOTCHING DA MAGE
5334857	06-Apr-12	SEMICONDUCTOR DEVICE WITH TEST -ONLY CONTACTS AND METHOD FOR MAKING THE SAME
5336327	01-Jun-12	CVD REACTOR WITH UNIFORM LAYER DEPOSITING ABILITY
5336921	27-Jan-12	VERTICAL TRENCH INDUCTOR
5337007	04-Nov-13	HIGH EFFICIENCY CLASS AB TRANS CONDUCTANCE AMPLIFIER
5337207	21-Dec-12	HIGH-PERMITTIVITY DIELECTRIC C APACITOR FOR USE IN A SEMICOND UCTOR DEVICE AND PROCESS FOR M AKING THE SAME
5337606	10-Aug-12	LATERALLY SENSITIVE ACCELEROME TER AND METHOD FOR MAKING

5338397	01-Oct-13	METHOD OF FORMING A SEMICONDUCTOR DEVICE
5338932	04-Jan-13	METHOD AND APPARATUS FOR MEASURING THE TOPOLOGY OF A SEMICONDUCTOR DEVICE
5338999	05-May-13	PIEZOELECTRIC LEAD ZIRCONIUM TITANATE DEVICE AND METHOD FOR FORMING SAME
5339079	30-Mar-12	DIGITAL-TO-ANALOG CONVERTER WITH A FLEXIBLE DATA INTERFACE
5339266	29-Nov-13	PARALLEL METHOD AND APPARATUS FOR DETECTING AND COMPLETING FLOATING POINT OPERATIONS INVOLVING SPECIAL OPERANDS
5339278	12-Apr-13	METHOD AND APPARATUS FOR STAND BY RECOVERY IN A PHASE LOCKED LOOP
5339279	07-May-13	BLOCK ERASABLE FLASH EEPROM APPARATUS AND METHOD THEREOF
5340993	30-Apr-13	OPTOCOUPLER PACKAGE WITH INTEGRAL VOLTAGE ISOLATION BARRIER
5341113	30-Jun-13	VOLTAGE CONTROLLED OSCILLATOR
5341320	01-Mar-13	METHOD FOR RAPIDLY PROCESSING FLOATING-POINT OPERATIONS WHICH INVOLVE EXCEPTIONS
5341500	23-Aug-11	DATA PROCESSOR WITH COMBINED STATIC AND DYNAMIC MASKING OF OPERAND FOR BREAKPOINT OPERATION
5341684	07-Dec-12	PRESSURE SENSOR BUILT INTO A CABLE CONNECTOR
5343437	19-Feb-13	MEMORY HAVING NONVOLATILE AND VOLATILE MEMORY BANKS
5345105	03-Feb-12	STRUCTURE FOR SHIELDING CONDUCTORS
5345190	02-Aug-13	MODULAR LOW VOLTAGE FILTER WITH COMMON MODE FEEDBACK
5345235	12-Apr-13	SCALED REFERENCE ANALOG TO DIGITAL CONVERSION CIRCUITRY AND METHOD OF OPERATION
5346857	28-Sep-12	METHOD FOR FORMING A FLIP-CHIP BOND FROM A GOLD-TIN EUTECTIC
5347181	29-Apr-12	INTERFACE CONTROL LOGIC FOR EMBEDDING A MICROPROCESSOR IN A GATE ARRAY
5349214	13-Sep-13	COMPLEMENTARY HETEROJUNCTION DEVICE
5352631	16-Dec-12	METHOD FOR FORMING A TRANSISTOR HAVING SILICIDED REGIONS
5352926	04-Jan-13	FLIP CHIP PACKAGE AND METHOD FOR MAKING
5354717	29-Jul-13	METHOD FOR MAKING A SUBSTRATE STRUCTURE WITH IMPROVED HEAT DISSIPATION
5355005	04-Nov-12	SELF-DOPED COMPLEMENTARY FIELD EFFECT TRANSISTOR
5357237	04-Sep-12	IN A DATA PROCESSOR A METHOD AND APPARATUS FOR PERFORMING A FLOATING-POINT COMPARISON OPERATION
5357252	22-Mar-13	SIGMA-DELTA MODULATOR WITH IMPROVED TONE REJECTION AND METHOD THEREFOR
5358883	03-Feb-12	LATERAL BIPOLAR TRANSISTOR
5358890	19-Apr-13	PROCESS FOR FABRICATING ISOLATION REGIONS IN A SEMICONDUCTOR DEVICE
5358901	01-Mar-13	PROCESS FOR FORMING AN INTERMETALLIC LAYER
5359297	28-Oct-13	VCO POWER-UP CIRCUIT FOR PLL AND METHOD THEREOF
5359564	04-May-13	CONTENT ADDRESSABLE MEMORY SYSTEM AND METHOD OF OPERATION

5359626	02-Sep-12	SERIAL INTERFACE BUS SYSTEM FOR TRANSMITTING AND RECEIVING DIGITAL AUDIO INFORMATION
5359893	19-Dec-11	MULTI-AXES GYROSCOPE
5360986	05-Oct-13	CARBON DOPED SILICON SEMICONDUCTOR DEVICE HAVING A NARROWED BANDGAP CHARACTERISTIC AND METHOD
5361490	08-Nov-11	METHOD FOR MAKING TAPE AUTOMATED BONDING (TAB) SEMICONDUCTOR DEVICE
5362990	02-Jun-13	CHARGE PUMP WITH A PROGRAMMABLE PUMP CURRENT AND SYSTEM
5363071	04-May-13	APPARATUS AND METHOD FOR VARYING THE COUPLING OF A RADIO FREQUENCY SIGNAL
5365120	21-Sep-12	DATA SLICER WITH HOLD
5365121	08-Mar-13	CHARGE PUMP WITH CONTROLLED RAMP RATE
5365199	02-Aug-13	AMPLIFIER WITH FEEDBACK HAVING HIGH POWER SUPPLY REJECTION
5367477	29-Nov-13	METHOD AND APPARATUS FOR PERFORMING PARALLEL ZERO DETECTION IN A DATA PROCESSING SYSTEM
5367494	22-Nov-11	RANDOMLY ACCESSIBLE MEMORY HAVING TIME OVERLAPPING MEMORY ACCESSSES
5371038	21-Oct-13	METHOD OF FORMING A QUANTUM MULTI-FUNCTION SEMICONDUCTOR DEVICE
5371043	06-Nov-12	METHOD FOR FORMING A POWER CIRCUIT PACKAGE
5371394	15-Nov-13	DOUBLE IMPLANTED Laterally Diffused MOS Device and Method Thereof
5371404	04-Feb-13	THERMALLY CONDUCTIVE INTEGRATED CIRCUIT PACKAGE WITH RADIO FREQUENCY SHIELDING
5372612	28-Jun-13	SEMICONDUCTOR MATERIAL CONTACTING MEMBER
5373255	28-Jul-13	LOW-POWER, JITTER-COMPENSATED PHASE LOCKED LOOP AND METHOD THEREFOR
5373457	29-Mar-13	METHOD FOR DERIVING A PIECEWISE LINEAR MODEL
5373463	06-Jul-13	FERROELECTRIC NONVOLATILE RANDOM ACCESS MEMORY HAVING DRIVE LINE SEGMENTS
5375081	22-Sep-12	HIGH SPEED ADDER USING VARIED CARRY SCHEME AND RELATED METHOD
5375216	28-Feb-12	APPARATUS AND METHOD FOR OPTIMIZING PERFORMANCE OF A CACHE MEMORY IN A DATA PROCESSING SYSTEM
5375229	13-Apr-12	SYSTEM AND METHOD FOR ADDING A CONTROL FUNCTION TO A SEMICONDUCTOR EQUIPMENT SYSTEM
5376819	29-Nov-13	INTEGRATED CIRCUIT HAVING AN ON CHIP THERMAL CIRCUIT REQUIRING ONLY ONE DEDICATED INTEGRATED CIRCUIT PIN AND METHOD OF OPERATION
5377072	10-Jan-14	SINGLE METAL-PLATE BYPASS CAPACITOR
5377139	11-Dec-12	PROCESS FORMING AN INTEGRATED CIRCUIT
5377544	03-Jan-12	ROTATIONAL VIBRATION GYROSCOPE
5378659	06-Jul-13	METHOD AND STRUCTURE FOR FORMING AN INTEGRATED CIRCUIT PATTERN ON A SEMICONDUCTOR SUBSTRATE
5379186	06-Jul-13	ENCAPSULATED ELECTRONIC COMPONENT HAVING A HEAT DIFFUSING LAYER
5381036	03-Feb-12	LEAD-ON CHIP SEMICONDUCTOR DEVICE HAVING PERIPHERAL BOND PADS
5381051	08-Mar-13	HIGH VOLTAGE CHARGE PUMP

5381055	29-Jul-13	CMOS DRIVER USING OUTPUT FEEDBACK PRE-DRIVE
5381085	06-Jul-13	PHASE LOCK LOOP WITH SELF TEST CIRCUITRY AND METHOD FOR USING THE SAME
5381105	12-Feb-13	METHOD OF TESTING A SEMICONDUCTOR DEVICE
5381114	04-Apr-14	A CONTINUOUS TIME COMMON MODE FEEDBACK AMPLIFIER
5381116	13-Dec-13	METHOD AND APPARATUS FOR PERFORMING FREQUENCY TRACKING IN AN ALL DIGITAL PHASE LOCK LOOP
5381307	19-Jun-12	SELF ALIGNING ELECTRICAL CONTACT ARRAY
5382471	17-Jan-12	ADHERENT METAL COATING FOR ALUMINUM NITRIDE SURFACES
5383354	27-Dec-13	PROCESS FOR MEASURING SURFACE TOPOGRAPHY USING ATOMIC FORCE MICROSCOPY
5384269	09-Dec-12	METHODS FOR MAKING AND USING A SHALLOW SEMICONDUCTOR JUNCTION
5384737	08-Mar-14	PIPELINED MEMORY HAVING SYNCHRONOUS AND ASYNCHRONOUS OPERATING MODES
5385869	22-Jul-13	SEMICONDUCTOR CHIP BONDED TO A SUBSTRATE AND METHOD OF MAKING
5386201	26-Nov-13	TEMPERATURE STABLE SQUARE WAVE OSCILLATOR
5386341	01-Nov-13	SEMICONDUCTOR CARRIER ASSEMBLY
5386534	27-Oct-12	DATA PROCESSING SYSTEM FOR GENERATING SYMMETRICAL RANGE OF ADDRESSES OF INSTRUCTING-ADDRESS-VALUE WITH THE USE OF INVERTING SIGN VALUE
5386624	06-Jul-13	METHOD AND APPARATUS FOR UNDER ENCAPSULATING COMPONENTS ON CIRCUIT SUPPORTING SUBSTRATES
5386715	06-Dec-13	GAS VAPOR SENSOR ELEMENT AND METHOD OF FORMING
5387540	30-Sep-13	METHOD OF FORMING TRENCH ISOLATION STRUCTURE IN AN INTEGRATED CIRCUIT
5387547	07-Feb-12	PROCESS FOR ADJUSTING THE IMPEDANCE OF A MICROWAVE CONDUCTOR USING AN AIR BRIDGE
5387548	22-Jun-12	METHOD OF FORMING AN ETCHED OHMIC CONTACT
5387913	09-Nov-13	RECEIVER WITH DIGITAL TUNING AND METHOD THEREFOR
5388323	04-May-13	PROBE FOR PROVIDING SURFACE IMAGES
5389160	01-Jun-13	TIN BISMUTH SOLDER PASTE AND METHOD USING PASTE TO FORM CONNECTION HAVING IMPROVED HIGH TEMPERATURE PROPERTIES
5389564	22-Jun-12	METHOD OF FORMING A GAAS FET
5389566	23-Jul-13	METHOD OF FORMING A FERROMAGNETIC MEMORY DEVICE
5389569	03-Mar-12	VERTICAL AND LATERAL ISOLATION FOR A SEMICONDUCTOR DEVICE
5389576	28-Dec-12	METHOD OF PROCESSING A POLYCID E STRUCTURE
5389579	05-Apr-13	METHOD FOR SINGLE SIDED POLISHING OF A SEMICONDUCTOR WAFER
5391285	25-Feb-14	ADJUSTABLE PLATING CELL FOR UNIFORM BUMP PLATING OF SEMICONDUCTOR WAFERS
5391397	05-Apr-14	METHOD OF ADHESION TO A POLYIMIDE SURFACE BY FORMATION OF COVALENT BONDS
5391517	13-Sep-13	PROCESS FOR FORMING COPPER INTERCONNECT STRUCTURE
5391999	02-Dec-14	GLITCHLESS SWITCHED-CAPACITOR BIQUAD LOW PASS

		FILTER
5392348	21-Feb-12	DTMF DETECTION HAVING SAMPLE RATE DECIMATION AND ADAPTIVE TO NE DETECTION
5394007	22-Oct-12	ISOLATED WELL AND METHOD OF MAKING
5394027	01-Nov-13	HIGH VOLTAGE CHARGE PUMP AND RELATED CIRCUITRY
5394028	26-Jun-12	APPARATUS FOR TRANSITIONING BETWEEN POWER SUPPLY LEVELS
5394036	04-Jan-14	CIRCUIT AND METHOD OF ZERO GENERATION IN A REAL-TIME FILTER
5394444	12-Jul-13	LOCK DETECT CIRCUIT FOR DETECTING A LOCK CONDITION IN A PHASE LOCKED LOOP AND METHOD THEREFOR
5396128	13-Sep-13	OUTPUT CIRCUIT FOR INTERFACING INTEGRATED CIRCUITS HAVING DIFFERENT POWER SUPPLY POTENTIALS
5396296	08-Nov-13	VIDEO FEEDBACK MATCHING CIRCUIT AND METHOD THERE
5397717	12-Jul-13	A METHOD OF FABRICATING A SILICON CARBIDE VERTICAL MOSFET AND DEVICE
5397917	26-Apr-13	SEMICONDUCTOR PACKAGE CAPABLE OF SPREADING HEAT AND METHOD OF FORMING
5399505	23-Jul-13	METHOD AND APPARATUS FOR PERFORMING WAFER LEVEL TESTING OF INTEGRATED CIRCUIT DICE
5399507	27-Jun-14	FABRICATION OF MIXED THIN-FILM AND BULK SEMICONDUCTOR SUBSTRATE FOR INTEGRATED CIRCUIT APPLICATIONS
5399887	03-May-14	MODULATION DOPED FIELD EFFECT TRANSISTOR
5399893	24-Aug-13	DIODE PROTECTED SEMICONDUCTOR DEVICE
5400274	02-May-14	MEMORY HAVING LOOPED GLOBAL DATA LINES FOR PROPAGATION DELAY MATCHING
5402389	08-Mar-14	SYNCHRONOUS MEMORY HAVING PARALLEL OUTPUT DATA PATHS
5404386	26-Nov-13	PROGRAMMABLE CLOCK FOR AN ANALOG CONVERTER IN A DATA PROCESSOR AND METHOD THEREFOR
5405796	26-May-12	CAPACITOR AND METHOD OF FORMATION AND A MEMORY CELL FORMED THEREFROM
5407855	07-Jun-13	PROCESS FOR FORMING A SEMICONDUCTOR DEVICE HAVING A REDUCING/OXIDIZING CONDUCTIVE MATERIAL
5407870	07-Jun-13	PROCESS FOR FABRICATING A SEMICONDUCTOR DEVICE HAVING A HIGH RELIABILITY DIELECTRIC MATERIAL
5408130	29-Jul-13	INTERCONNECTION STRUCTURE FOR CONDUCTIVE LAYERS
5409567	28-Apr-14	METHOD OF ETCHING COPPER LAYERS
5410158	22-Jan-13	BIPOLAR TRANSISTOR APPARATUS WITH ISO-TERMINALS
5410181	20-Jun-14	METHOD AND ASSEMBLY FOR MOUNTING AN ELECTRONIC DEVICE HAVING AN OPTICALLY ERASABLE SURFACE
5410184	04-Oct-13	MICROELECTRONIC PACKAGE COMPRISING TIN-COPPER BUMP INTERCONNECTIONS, AND METHOD FOR FORMING SAME
5410595	12-Nov-12	APPARATUS AND METHOD FOR NOISE REDUCTION FOR A FULL-DUPLEX SPEAKERPHONE OR THE LIKE
5413965	13-Sep-13	MICROELECTRONIC DEVICE PACKAGE CONTAINING A LIQUID AND METHOD
5414701	22-Jul-14	METHOD AND DATA STRUCTURE FOR PERFORMING ADDRESS COMPRESSION IN AN ASYNCHRONOUS TRANSFER MODE (ATM) SYSTEM

5416356	03-Sep-13	INTEGRATED CIRCUIT AND METHOD OF FORMING
5416744	08-Mar-14	MEMORY HAVING BIT LINE LOAD WITH AUTOMATIC BIT LINE PRECHARGE AND EQUALIZATION
5416783	09-Aug-13	METHOD AND APPARATUS FOR GENERATING PSEUDORANDOM NUMBERS OR FOR PERFORMING DATA COMPRESSION IN A DATA PROCESSOR
5418485	20-Dec-13	CLOCK SIGNAL CONDITIONING CIRCUIT
5418786	17-Jun-14	ASYNCHRONOUS TRANSFER MODE (ATM) METHOD AND APPARATUS FOR COMMUNICATING STATUS BYTES IN A MANNER COMPATIBLE WITH THE UTPIA PROTOCOL
5418945	23-May-12	FILE BASED AND HIGHLY AVAILABLE HYBRID DATABASE
5420808	13-May-13	CIRCUITRY AND METHOD FOR REDUCING POWER CONSUMPTION WITHIN AN ELECTRONIC CIRCUIT
5424245	04-Jan-14	CIRCUIT AND METHOD OF FORMING VIAS THROUGH TWO-SIDED SUBSTRATE
5424576	22-Mar-13	SEMICONDUCTOR DEVICE HAVING X-SHAPED DIE SUPPORT MEMBER AND METHOD FOR MAKING THE SAME
5424689	22-Dec-13	FILTERING DEVICE IN A PHASE LOCKED LOOP CONTROLLER
5424698	06-Dec-13	A FERRITE/SEMICONDUCTOR RESONATOR/FILTER AND METHOD OF FABRICATION
5426263	23-Dec-13	ELECTRONIC ASSEMBLY HAVING A DOUBLE-SIDED LEADLESS COMPONENT
5426381	23-May-14	LATCHING ECL TO CMOS INPUT BUFFER CIRCUIT
5427964	04-Apr-14	INSULATED GATE FIELD EFFECT TRANSISTOR AND METHOD FOR FABRICATING
5427965	13-Sep-13	METHOD OF FABRICATING A COMPLEMENTARY HETEROJUNCTION FET
5428504	27-Jun-12	COOLING COVER FOR RF POWER DEVICES
5428574	27-Jun-12	STATIC RAM WITH TEST FEATURES
5428639	25-Feb-14	TWO'S COMPLEMENT PULSE WIDTH MODULATOR AND METHOD FOR PULSE WIDTH MODULATING A TWO'S COMPLEMENT NUMBER
5429292	01-Jun-13	TIN BISMUTH SOLDER PASTE AND METHOD USING PASTE TO FORM CONNECTION HAVING IMPROVED HIGH TEMPERATURE PROPERTIES
5429293	19-Dec-14	SOLDERING PROCESS
5430327	04-Jul-12	OHMIC CONTACT FOR III-V SEMICONDUCTOR MATERIALS
5430416	23-Feb-14	POWER AMPLIFIER HAVING NESTED AMPLITUDE MODULATION CONTROLLER AND PHASE MODULATION CONTROLLER
5431332	07-Feb-14	METHOD AND APPARATUS FOR SOLDER SPHERE PLACEMENT USING AN AIR KNIFE
5432731	08-Mar-13	FERROELECTRIC MEMORY CELL AND METHOD OF SENSING AND WRITING THE POLARIZATION STATE THEREOF
5432950	11-Jul-12	SYSTEM FOR SECURING A DATA PROCESSING SYSTEM AND METHOD OF OPERATION
5434739	14-Jun-13	REVERSE BATTERY PROTECTION CIRCUIT
5435481	18-Jan-14	SOLDERING PROCESS
5436180	28-Feb-14	METHOD FOR MAKING A SEMICONDUCTOR STRUCTURE
5436203	05-Jul-14	SHIELDED LIQUID ENCAPSULATED SEMICONDUCTOR DEVICE AND METHOD FOR MAKING THE SAME
5436860	26-May-14	COMBINED MULTIPLIER/SHIFTER AND METHOD THEREFOR

5437189	03-May-14	DUAL ABSOLUTE PRESSURE SENSOR AND METHOD THEREOF
5438877	13-Jun-14	PRESSURE SENSOR PACKAGE FOR REDUCING STRESS-INDUCED MEASUREMENT ERROR
5439162	28-Jun-13	DIRECT CHIP ATTACHMENT METHOD AND STRUCTURE
5439840	02-Aug-13	METHOD OF FORMING A NONVOLATILE RANDOM ACCESS MEMORY CAPACITOR CELL HAVING A METAL-OXIDE DIELECTRIC
5440514	08-Mar-14	WRITE CONTROL FOR A MEMORY USING A DELAY LOCKED LOOP
5441901	05-Oct-13	CARBON DOPED SILICON SEMICONDUCTOR DEVICE HAVING A NARROWED BANDGAP CHARACTERISTIC AND METHOD
5441914	02-May-14	METHOD OF FORMING CONDUCTIVE INTERCONNECT STRUCTURE
5442228	15-Aug-12	MONOLITHIC SHIELDED INTEGRATED CIRCUIT
5442240	05-Apr-14	METHOD OF ADHESION TO A POLYIMIDE SURFACE BY FORMATION OF COVALENT BONDS
5442353	25-Oct-13	BANDPASS SIGMA-DELTA ANALOG-TO-DIGITAL CONVERTER (ADC), METHOD THEREFOR, AND RECEIVER USING SAME
5442576	26-May-14	MULTIBIT SHIFTING APPARATUS, DATA PROCESSOR USING SAME, AND METHOD THEREFOR
5442628	15-Nov-13	LOCAL AREA NETWORK DATA PROCESSING SYSTEM CONTAINING A QUAD ELASTIC BUFFER AND LAYER MANAGEMENT (ELM) INTEGRATED CIRCUIT AND METHOD OF SWITCHING
5442738	03-Dec-13	A COMPUTER DISPLAY REPRESENTING STRUCTURE
5444016	25-Jun-13	METHOD OF MAKING OHMIC CONTACTS TO A COMPLEMENTARY SEMICONDUCTOR DEVICE
5444289	22-Aug-12	VERTICAL AND LATERAL ISOLATION FOR A SEMICONDUCTOR DEVICE
5446247	19-Nov-13	AN ELECTRICAL CONTACT AND METHOD FOR MAKING AN ELECTRICAL CONTACT
5446455	02-Dec-13	AUTO-CALIBRATED CURRENT-MODE DIGITAL-TO-ANALOG CONVERTER AND METHOD THEREFOR
5446625	10-Nov-13	CHIP CARRIER AND METHOD FOR MANUFACTURE
5447874	29-Jul-14	A METHOD FOR MAKING A SEMICONDUCTOR DEVICE GATE
5447887	01-Apr-14	METHOD FOR CAPPING COPPER IN SEMICONDUCTOR DEVICES
5448744	05-Sep-12	INTEGRATED CIRCUIT MICROPROCESSOR WITH PROGRAMMABLE CHIP SELECT LOGIC
5448770	05-Apr-13	TEMPERATURE-COEFFICIENT CONTROLLED RADIO FREQUENCY SIGNAL DETECTING CIRCUITRY
5449922	03-May-14	BIPOLAR HETEROJUNCTION DIODE
5450283	03-Nov-12	THERMALLY ENHANCED SEMICONDUCTOR DEVICE HAVING EXPOSED BACKSIDE AND METHOD FOR MAKING THE SAME
5451274	31-Jan-14	REFLOW OF MULTI-LAYER METAL BUMPS
5451538	09-Nov-13	METHOD FOR FORMING A VERTICALLY INTEGRATED DYNAMIC MEMORY CELL
5451543	25-Apr-14	STRAIGHT SIDEWALL PROFILE CONTACT OPENING TO UNDERLYING INTERCONNECT AND METHOD FOR MAKING THE SAME
5452245	07-Sep-13	MEMORY EFFICIENT GATE ARRAY CELL
5452368	02-Aug-13	METHOD OF DETECTING DEFECTS IN SEMICONDUCTOR

		PACKAGE LEADS
5454097	25-Jan-13	CASCADABLE PERIPHERAL DATA INTERFACE INCLUDING A SHIFT REGISTER, COUNTER, AND RANDOMLY-ACCESSED REGISTERS OF DIFFERENT BIT LENGTH
5454270	06-Jun-14	HERMETICALLY SEALED PRESSURE SENSOR AND METHOD THEREOF
5455200	03-Oct-12	METHOD FOR MAKING A LEAD-ON-CHIP SEMICONDUCTOR DEVICE HAVING PERIPHERAL BOND PADS
5455446	30-Jun-14	LEADED SEMICONDUCTOR PACKAGE HAVING TEMPERATURE CONTROLLED LEAD LENGTH
5457424	06-Oct-14	QUADRATURE DEMODULATOR OPERABLE OVER DIFFERENT FREQUENCIES
5457605	23-Nov-13	ELECTRONIC DEVICE HAVING COPLANAR HEATSINK AND ELECTRICAL CONTACTS
5457660	05-Oct-13	RESET SIGNAL GENERATOR CIRCUIT HAVING A FUNCTION FOR PROTECTION WRITE DATA
5457776	10-Oct-12	COMPACT MEMORY FOR MIXED TEXT IN GRAPHICS
5457802	17-May-13	INTEGRATED CIRCUIT PIN CONTROL APPARATUS AND METHOD THEREOF IN A DATA PROCESSING SYSTEM
5457879	04-Jan-14	METHOD OF SHAPING INTER-SUBSTRATE PLUG AND RECEPTACLE INTERCONNECTS
5459407	09-Dec-13	APPARATUS AND METHOD OF CORRECTING LOSS OF TEST CONTACT CONTINUITY
5460704	28-Sep-14	FERRITE FILM AND METHOD OF DEPOSITING SAME
5460922	27-Jan-14	METHOD FOR FABRICATING ELECTRODE PATTERNS
5461007	02-Jun-14	PROCESS FOR POLISHING AND ANALYZING A LAYER OVER A PATTERNED SEMICONDUCTOR SUBSTRATE
5461260	01-Aug-14	SEMICONDUCTOR DEVICE INTERCONNECT LAYOUT METHOD AND STRUCTURE FOR REDUCING PREMATURE ELECTROMIGRATION FAILURE DUE TO HIGH LOCALIZED CURRENT DENSITY
5461488	12-Sep-14	COMPUTERIZED FACSIMILE (FAX) SYSTEM AND METHOD OPERATION
5461575	24-Oct-12	METHOD FOR OBTAINING TIMING PARAMETERS OF A SEQUENTIAL CIRCUIT TO DESIGN A LOGIC NETWORK
5463306	19-Jul-13	APPARATUS FOR DETECTING COMPLETION OF ENERGY TRANSFER IN AN INDUCTIVE DC TO DC CONVERTER
5463353	06-Sep-14	RESISTORLESS VCO INCLUDING CURRENT SOURCE AND SINK CONTROLLING A CURRENT CONTROLLED OSCILLATOR
5464711	01-Aug-14	PROCESS FOR FABRICATING AN X-RAY ABSORBING MASK
5465217	16-Aug-13	METHOD FOR AUTOMATIC TAB ARTWORK BUILDING
5465626	04-Apr-14	PRESSURE SENSOR WITH STRESS ISOLATION PLATFORM HERMETICALLY SEALED TO PROTECT SENSOR DIE
5466484	29-Sep-13	RESISTOR STRUCTURE AND METHOD OF SETTING A RESISTANCE VALUE
5467024	01-Nov-13	METHOD AND APPARATUS FOR TESTING INTEGRATED DEVICES
5467141	12-Oct-13	A MODULATOR CIRCUIT
5467253	30-Jun-14	SEMICONDUCTOR DEVICE AND METHOD OF FORMING
5467451	14-Nov-12	METHOD OF DISPLAYING A BOUNDING BOX USING A SET ASPECT RATIO AND THE COORDINATE RATIO OF A GRAPHICAL POINTER

5467455	03-Nov-13	DATA PROCESSING SYSTEM AND METHOD FOR PERFORMING DYNAMIC BUS TERMINATION
5468999	26-May-14	LIQUID ENCAPSULATED BALL GRID ARRAY SEMICONDUCTOR DEVICE WITH FINE PITCH WIRE BONDING
5469072	01-Nov-13	INTEGRATED CIRCUIT TEST SYSTEM AND METHOD FOR MAKING AN INTEGRATED CIRCUIT
5469263	01-Jul-14	METHOD FOR ALIGNMENT IN PHOTOLITHOGRAPHIC PROCESSES
5469476	21-Mar-14	SPIKE FILTER CIRCUIT AND METHOD THEREFOR
5470787	02-May-14	SEMICONDUCTOR DEVICE SOLDER BUMPS HAVING INTRINSIC POTENTIAL FOR FORMING AN EXTENDED EUTECTIC REGION AND METHOD FOR MAKING AND USING THE SAME
5471422	11-Apr-14	EEPROM CELL WITH ISOLATION TRANSISTOR AND METHODS FOR MAKING AND OPERATING THE SAME
5471665	18-Oct-14	DIFFERENTIAL DC OFFSET COMPENSATION CIRCUIT
5472911	02-Sep-14	METHOD FOR CONTROLLING ELECTROMIGRATION AND ELECTRICALLY CONDUCTIVE INTERCONNECT STRUCTURE THEREFOR
5473569	01-Aug-14	A METHOD FOR OPERATING A FLASH MEMORY
5474958	04-May-13	METHOD FOR MAKING SEMICONDUCTOR DEVICE HAVING NO DIE SUPPORTING SURFACE
5475255	30-Jun-14	CIRCUIT DIE HAVING IMPROVED SUBSTRATE NOISE ISOLATION
5475778	21-Oct-13	SMART OPTICAL COUPLER AND SMART OPTICAL COUPLER SYSTEM
5476566	19-Dec-12	METHOD FOR THINNING A SEMICONDUCTOR WAFER
5476816	28-Mar-14	PROCESS FOR ETCHING AN INSULATING LAYER AFTER A METAL ETCHING STEP
5477084	13-Sep-13	MICROELECTRONIC DEVICE PACKAGE CONTAINING A LIQUID AND METHOD
5477169	20-Jun-14	LOGIC CIRCUIT WITH NEGATIVE DIFFERENTIAL RESISTANCE DEVICE AND METHODS OF FABRICATION
5477176	02-Jun-14	POWER-ON RESET CIRCUIT FOR PREVENTING MULTIPLE WORD LINE SELECTIONS DURING POWER-UP OF AN INTEGRATED CIRCUIT MEMORY
5477467	19-Dec-12	SHRINKABLE BICMOS CIRCUIT LAYOUT
5478436	27-Dec-14	SELECTIVE CLEANING PROCESS FOR FABRICATING A SEMICONDUCTOR DEVICE
5478437	01-Aug-14	SELECTIVE PROCESSING USING A HYDROCARBON AND HYDROGEN
5478773	28-Apr-14	METHOD OF MAKING AN ELECTRONIC DEVICE HAVING AN INTEGRATED INDUCTOR
5479092	30-Aug-13	CURVATURE CORRECTION CIRCUIT FOR A VOLTAGE REFERENCE
5479445	26-Dec-12	MODE DEPENDENT SERIAL TRANSMISSION OF DIGITAL AUDIO INFORMATION
5480829	25-Jun-13	METHOD OF MAKING A III-V COMPLEMENTARY HETEROSTRUCTURE DEVICE WITH COMPATIBLE NON-GOLD OHMIC CONTACTS
5480835	06-May-13	AN ELECTRICAL INTERCONNECT AND METHOD FOR FORMING THE SAME
5481131	03-Sep-13	INTEGRATED CIRCUIT HAVING PASSIVE CIRCUIT ELEMENTS
5481226	25-Oct-14	LOW-VOLTAGE INTERMEDIATE FREQUENCY AMPLIFIER

		PROVIDING AUTO Matic GAIN CONTROL OF A SOURCE AMPLIFIER
5482878	04-Apr-14	INSULATED GATE FIELD EFFECT TRANSISTOR HAVING SUBTHRESHOLD SWING AND METHOD FOR FABRICATING
5483085	09-May-14	ELECTRO-OPTIC INTEGRATED CIRCUIT WITH DIODE DECODER
5483406	30-Sep-13	OVERVOLTAGE PROTECTION CIRCUIT
5483476	26-Jan-14	MANTISSA ADDITION SYSTEM FOR A FLOATING POINT ADDER
5483558	08-Aug-14	METHOD AND APPARATUS FOR DETECTING PHASE OR FREQUENCY LOCK
5483660	29-Nov-13	METHOD AND APPARATUS FOR PERFORMING MULTIPLEXED AND NON-MULTIPLEXED BUS CYCLES IN A DATA PROCESSING SYSTEM
5483817	08-Jun-14	SHORT CIRCUIT DETECTOR FOR SENSORS
5484740	06-Jun-14	III-V SEMICONDUCTOR GATE STRUCTURE AND METHOD OF MANUFACTURE
5485456	21-Oct-14	AN ASYNCHRONOUS TRANSFER MODE (ATM) SYSTEM HAVING AN ATM DEVICE COUPLED TO MULTIPLE PHYSICAL LAYER DEVICES
5485487	25-Feb-14	RECONFIGURABLE COUNTER AND PULSE WIDTH MODULATOR (PWM) USING SAME
5485602	27-Dec-13	INTEGRATED CIRCUIT HAVING A CONTROL SIGNAL FOR IDENTIFYING COINCIDING ACTIVE EDGES OF TWO CLOCK SIGNALS
5486770	27-Jun-14	HIGH FREQUENCY WAFER PROBE APPARATUS AND METHOD
5486792	06-Mar-15	METHOD AND APPARATUS FOR CALCULATING A DIVIDER IN A DIGITAL PHASE LOCK LOOP
5486824	05-Oct-14	DATA PROCESSOR WITH A HARDWARE KEYS CAN CIRCUIT, HARDWARE KEYS CAN CIRCUIT, AND METHOD THEREOF
5487305	30-Jan-13	THREE AXES ACCELEROMETERS
5487355	03-Mar-15	SEMICONDUCTOR CRYSTAL GROWTH METHOD AND STRUCTURE
5488320	04-Apr-14	COMPARATOR HAVING LATCHED OUTPUT WHEN DISABLED FROM THE POWER SUPPLY
5488688	30-Mar-14	DATA PROCESSOR WITH REAL-TIME DIAGNOSTIC CAPABILITY
5489800	31-Aug-14	DUAL CHANNEL SMALL OUTLINE OPTOCOUPLER PACKAGE AND METHOD THEREOF
5489988	03-Jan-15	ENVIRONMENTAL SENSOR AND METHOD THEREFOR
5491303	21-Mar-14	SURFACE MOUNT INTERPOSER
5491491	31-Oct-14	PORTABLE ELECTRONIC EQUIPMENT WITH BINOCULAR VIRTUAL DISPLAY
5491691	16-Aug-14	METHOD AND APPARATUS FOR PACKAGING ASYNCHRONOUS TRANSFER MODE (ATM) DATA CELL TRANSMISSION
5492223	04-Feb-14	INTERLOCKING AND INVERTIBLE SEMICONDUCTOR DEVICE TRAY AND TEST CONTACTOR MATING THERETO
5492266	31-Aug-14	FINE PITCH SOLDER BALLS ON PRINTED CIRCUIT BOARD PROCESS AND PRODUCT
5492863	19-Oct-14	METHOD FOR FORMING CONDUCTIVE BUMPS ON A SEMICONDUCTOR DEVICE
5493248	20-Feb-13	INTEGRATED CIRCUIT FOR SENSING AN ENVIRONMENTAL CONDITION AND PRODUCING A HIGH POWER CIRCUIT
5493669	03-Mar-13	A DATA PROCESSOR FOR SIMULTANEOUSLY SEARCHING TWO FIELDS OF THE RENAMED BUFFER HAVING FIRST AND

		SECOND MOST RECENTLY ALLOCATED BITS
5493700	29-Oct-13	AUTOMATIC FREQUENCY CONTROL APPARATUS
5495437	05-Jul-14	NON-VOLATILE RAM TRANSFERRING DATA BETWEEN FERRO-ELECTRIC CAPACITORS AND A MEMORY CELL
5496438	09-Dec-14	METHOD OF REMOVING PHOTO RESIST
5497123	23-Dec-14	AMPLIFIER CIRCUIT HAVING HIGH LINEARITY FOR CANCELLING THIRD ORDER HARMONIC DISTORTION
5498767	11-Oct-14	METHOD FOR POSITIONING BOND PADS IN A SEMICONDUCTOR DIE LAYOUT
5498883	12-Mar-13	SUPERLUMINESCENT EDGE EMITTING DEVICE WITH APPARENT VERTICAL LIGHT EMISSION AND METHOD OF MAKING
5499338	12-Mar-13	A BUS SYSTEM HAVING A SYSTEM BUS, AN INTERNAL BUS WITH FUNCTIONAL UNITS COUPLED THEREBETWEEN AND A LOGIC UNIT FOR USE IN SUCH SYSTEM
5500543	04-Apr-14	SENSOR FOR DETERMINING A RATIO OF MATERIALS IN A MIXTURE AND METHOD
5500943	02-Nov-13	DATA PROCESSOR WITH RENAMABLE BUFFER AND FIFO BUFFER FOR IN-ORDER INSTRUCTION COMPLETION
5501006	22-Sep-13	METHOD FOR CONNECTION OF SIGNALS TO AN INTEGRATED CIRCUIT
5501943	21-Feb-15	METHOD OF PATTERNING AN INORGANIC OVERCOAT FOR A LIQUID CRYSTAL DISPLAY ELECTRODE
5502406	06-Mar-15	LOW POWER LEVEL SHIFT CIRCUIT AND METHOD THEREFOR
5502410	14-Mar-14	CIRCUIT FOR PROVIDING A VOLTAGE RAMP SIGNAL
5502717	01-Aug-14	METHOD AND APPARATUS FOR ESTIMATING ECHO CANCELLATION TIME
5502835	31-Aug-14	METHOD FOR SYNCHRONOUSLY ACCESSING MEMORY
5504039	29-Jul-14	A METHOD FOR MAKING A SELF-ALIGNED OXIDE GATE CAP
5504369	23-Jul-13	METHOD AND APPARATUS FOR PERFORMING WAFER LEVEL TESTING OF INTEGRATED CIRCUIT
5504694	28-Oct-13	A METHOD OF CELL CHARACTERIZATION ENERGY DISSIPATION
5504751	07-Nov-14	METHOD AND APPARATUS FOR EXTRACTING DIGITAL INFORMATION FROM AN ASYNCHRONOUS DATA STREAM
5506450	04-May-15	SEMICONDUCTOR DEVICE WITH IMPROVED ELECTROMIGRATION RESISTANCE AND METHOD FOR MAKING THE SAME
5506544	10-Apr-15	BIAS CIRCUIT FOR DEPLETION MODE FIELD EFFECT TRANSISTORS
5506971	09-Apr-13	METHOD AND APPARATUS FOR PERFORMING A SNOOP-RETRY PROTOCOL IN A DATA PROCESSING SYSTEM
5508230	29-Jul-13	METHOD OF MAKING A SEMICONDUCTOR DEVICE WITH DIAMOND HEAT DISSIPATION
5508539	29-Apr-14	ELEVATED-GATE FIELD EFFECT TRANSISTOR STRUCTURE AND FABRICATION METHOD
5508559	16-Apr-13	METHOD FOR FORMING A POWER CIRCUIT PACKAGE
5508644	28-Sep-14	SENSE AMPLIFIER FOR DIFFERENTIAL VOLTAGE DETECTION WITH LOW INPUT CAPACITANCE
5508651	16-Apr-13	SENSOR SIGNAL CONDITIONING CIRCUIT
5510645	07-Jan-14	SEMICONDUCTOR STRUCTURE HAVING AN AIR REGION AND METHOD OF FORMING THE SEMICONDUCTOR STRUCTURE

5510651	07-Jun-13	SEMICONDUCTOR DEVICE HAVING A REDUCING/OXIDIZING CONDUCTIVE MATERIAL
5510739	28-Mar-14	CIRCUIT AND METHOD FOR ENHANCING LOGIC TRANSITIONS APPEARING ON A LINE
5511170	02-Aug-13	DIGITAL BUS DATA RETENTION
5511182	31-Aug-14	PROGRAMMABLE PIN CONFIGURATION LOGIC CIRCUIT FOR PROVIDING A CHIP SELECT SIGNAL AND RELATED METHOD
5511235	02-May-14	APPARATUS FOR DETECTING A SIGNALING CHANNEL DURING SCANNING INCLUDING A CONTROLLED FREQUENCY CONVERTER CIRCUIT AND A CONTROLLED FILTER BANDWIDTH, AND A METHOD THEREFOR
5511419	30-Apr-13	ROTATIONAL VIBRATION GYROSCOPE
5512499	30-Apr-13	METHOD OF MAKING SYMMETRICAL AND ASYMMETRICAL MESFETS
5512518	06-Jun-14	METHOD OF MANUFACTURE OF MULTI LAYER DIELECTRIC ON A III-V SUBSTRATE
5513358	04-Feb-14	METHOD AND APPARATUS FOR POWER -UP STATE INITIALIZATION IN A DATA PROCESSING SYSTEM
5513382	30-Apr-13	MULTI-CERAMIC LAYER SWITCH CIRCUIT
5513430	19-Aug-14	METHOD OF MANUFACTURING A PROBE
5514891	02-Jun-15	N-TYPE HIGFET AND METHOD
5514892	30-Sep-14	ELECTROSTATIC DISCHARGE PROTECTION DEVICE
5515232	22-Sep-14	STATIC PROTECTION CIRCUIT FOR A SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE
5515735	03-Jan-15	MICROMACHINED FLOW SENSOR DEVICE USING A PRESSURE DIFFERENCE AND METHOD OF MANUFACTURING THE SAME
5517141	05-Nov-13	DIFFERENTIAL HIGH SPEED TRACK AND HOLD AMPLIFIER
5517506	28-Mar-14	METHOD AND DATA PROCESSING SYSTEM FOR TESTING CIRCUITS USING BOOLEAN DIFFERENCES
5517637	09-Dec-14	METHOD FOR TESTING A TEST ARCHITECTURE WITHIN A CIRCUIT
5517667	14-Jun-13	NEURAL NETWORK AND METHOD OF USING SAME
5519340	01-Nov-14	LINE DRIVER HAVING MAXIMUM OUTPUT VOLTAGE CAPACITY
5519848	18-Nov-13	A METHOD OF CELL CHARACTERIZATION IN A DISTRIBUTED SIMULATION SYSTEM
5520785	04-Jan-14	METHOD FOR ENHANCING ALUMINUM NITRIDE
5521428	28-May-13	FLAGLESS SEMICONDUCTOR DEVICE
5521906	26-Jan-15	METHOD AND APPARATUS FOR UPDATING CARRIER CHANNEL ALLOCATIONS
5523920	03-Jan-14	PRINTED CIRCUIT BOARD COMPRISING ELEVATED BOND PADS
5524215	05-Oct-13	BUS PROTOCOL AND METHOD FOR CONTROLLING A DATA PROCESSOR
5524223	31-Jan-14	INSTRUCTION ACCELERATOR FOR PROCESSING LOOP INSTRUCTIONS WITH ADDRESS GENERATOR USING MULTIPLE STORED INCREMENT VALUES
5525191	25-Jul-14	PROCESS FOR POLISHING A SEMICONDUCTOR SUBSTRATE
5525542	24-Feb-15	METHOD FOR MAKING A SEMICONDUCTOR DEVICE HAVING ANTI-REFLECTIVE COATING
5525920	01-May-15	COMPARATOR CIRCUIT AND METHOD THEREFOR
5527424	30-Jan-15	PRECONDITIONER FOR A POLISHING PAD AND METHOD FOR

		USING THE SAME
5527723	15-Mar-14	A METHOD FOR FORMING A DYNAMIC CONTACT WHICH CAN BE EITHER ON OR OFF OR SWITCHED THEREBETWEEN
5527739	23-Dec-13	PROCESS FOR FABRICATING A SEMI CONDUCTOR DEVICE HAVING AN IMPROVED METAL INTERCONNECT STRUCTURE
5528069	01-Aug-14	SENSING TRANSDUCER USING A SCHOTTKY JUNCTION AND HAVING AN INCREASED OUTPUT SIGNAL VOLTAGE
5528202	18-Jun-13	DISTRIBUTED CAPACITANCE TRANSMISSION LINE
5528254	31-May-14	ANTENNA AND METHOD FOR FORMING SAME
5528692	03-May-14	FREQUENCY INVERSION SCRAMBLER WITH INTEGRATED HIGH-PASS FILTER
5529682	26-Jun-15	METHOD FOR MAKING SEMICONDUCTOR DEVICES HAVING ELECTROPLATED LEADS
5530383	05-Dec-14	METHOD AND APPARATUS FOR A FREQUENCY DETECTION CIRCUIT FOR USE IN A PHASE LOCKED LOOP
5530399	27-Dec-14	TRANSCONDUCTANCE SCALING CIRCUIT AND METHOD RESPONSIVE TO A RECEIVED DIGITAL CODE WORD FOR USE WITH AN OPERATIONAL TRANSCONDUCTANCE CIRCUIT
5530659	29-Aug-14	METHOD AND APPARATUS FOR DECODING INFORMATION WITHIN A PROCESSING DEVICE
5530676	27-Jan-15	METHOD AND APPARATUS FOR REDUCING POWER CONSUMPTION IN MEMORY CIRCUITS
5530804	16-May-14	SUPERSCALAR PROCESSOR WITH PLURAL PIPELINED EXECUTION UNITS EACH UNIT SELECTIVELY HAVING BOTH NORMAL AND DEBUG MODES
5530824	04-Apr-14	ADDRESS TRANSLATION CIRCUIT
5530825	15-Apr-14	DATA PROCESSOR WITH BRANCH TARGET ADDRESS CACHE AND METHOD OF OPERATION
5531861	29-Sep-13	CHEMICAL-MECHANICAL-POLISHING PAD CLEANING PROCESS FOR USED DURING THE FABRICATION OF SEMICONDUCTOR DEVICES
5532175	17-Apr-15	METHOD OF ADJUSTING A THRESHOLD VOLTAGE FOR A SEMICONDUCTOR DEVICE FABRICATED ON A SEMICONDUCTOR ON INSULATOR SUBSTRATE
5532509	16-Dec-14	SEMICONDUCTOR INVERTER LAYOUT HAVING IMPROVED ELECTROMIGRATION CHARACTERISTICS IN THE OUTPUT MODE
5532899	21-Mar-14	VOLTAGE PROTECTION STRUCTURE FOR SEMICONDUCTOR DEVICES
5534462	24-Feb-15	METHOD FOR FORMING A PLUG AND SEMICONDUCTOR DEVICE HAVING THE SAME
5534784	02-May-14	METHOD FOR PROBING A SEMICONDUCTOR WAFER
5534819	13-Apr-15	CIRCUIT AND METHOD FOR REDUCING VOLTAGE ERROR WHEN CHARGING AND DISCHARGING A VARIABLE CAPACITOR THROUGH A SWITCH
5534947	16-May-14	ADVANCED COMB FILTER
5535101	09-Jul-13	INTEGRATED CIRCUIT PACKAGE
5535346	05-Jul-14	DATA PROCESSOR WITH FUTURE FILE WITH PARALLEL UPDATE AND METHOD OF OPERATION
5535349	09-Jun-14	A DATA PROCESSING SYSTEM AND METHOD FOR PROVIDING CHIP SELECTS TO PERIPHERAL DEVICES
5535351	04-Apr-14	ADDRESS TRANSLATOR WITH BY-PASS CIRCUIT AND METHOD OF OPERATION

5535376	09-Jul-13	DATA PROCESSOR HAVING A TIMER CIRCUIT FOR PERFORMING A BUFFERED PULSE WIDTH MODULATION FUNCTION AND METHOD THEREFOR
5535398	09-Jul-13	METHOD AND APPARATUS FOR PROVIDING BOTH POWER AND CONTROL BY WAY OF AN INTEGRATED CIRCUIT TERMINAL
5536677	01-Dec-14	METHOD OF FORMING CONDUCTIVE BUMPS ON A SEMICONDUCTOR DEVICE USING A MASK STRUCTURE
5536962	07-Nov-14	SEMICONDUCTOR DEVICE HAVING A BURIED CHANNEL TRANSISTOR
5537562	16-Jul-13	DATA PROCESSING SYSTEM AND METHOD THEREOF
5538922	03-Jun-11	METHOD FOR FORMING CONTACT TO A SEMICONDUCTOR DEVICE
5539200	03-Nov-14	INTEGRATED OPTOELECTRONIC SUBSTRATE
5539216	27-Oct-14	MONOLITHIC SEMICONDUCTOR BODY WITH CONVEX STRUCTURE
5539249	23-Jul-13	METHOD AND STRUCTURE FOR FORMING AN INTEGRATED CIRCUIT PATTERN ON A SEMICONDUCTOR SUBSTRATE
5539351	03-Nov-14	CIRCUIT AND METHOD FOR REDUCING A GATE VOLTAGE OF A TRANSMISSION GATE WITHIN A CHARGE PUMP CIRCUIT
5539733	15-Nov-13	METHOD FOR SWITCHING DATA FLOW IN A FIBER DISTRIBUTED DATA INTERFACE (FDDI) SYSTEM
5539777	26-Jan-15	METHOD AND APPARATUS FOR A DMT RECEIVER HAVING A DATA DEFORMATTER COUPLED DIRECTLY TO A CONSTELLATION DECODER
5539892	02-Aug-14	ADDRESS TRANSLATION LOOKASIDE BUFFER REPLACEMENT APPARATUS AND METHOD WITH USER OVERRIDE
5541135	30-May-15	METHOD OF FABRICATING A FLIP CHIP SEMICONDUCTOR DEVICE HAVING AN INDUCTOR
5541450	02-Nov-14	LOW-PROFILE BALL-GRID ARRAY SEMICONDUCTOR PACKAGE
5542171	06-Aug-13	A METHOD OF SELECTIVELY RELEASING PLASTIC MOLDING MATERIAL FROM A SURFACE
5543339	29-Aug-14	PROCESS FOR FORMING AN ELECTRICALLY PROGRAMMABLE READ ONLY MEMORY CELL
5543724	03-Oct-14	METHOD AND APPARATUS FOR LOCATING CONDUCTIVE FEATURES AND TESTING SEMICONDUCTOR DEVICES
5544412	24-May-14	METHOD FOR COUPLING A POWER LEAD TO A BOND PAD IN AN ELECTRONIC MODULE
5545359	11-Aug-14	METHOD OF MAKING A PLASTIC MOLDED OPTOELECTRONIC INTERFACE
5545574	19-May-15	PROCESS FOR FORMING A SEMICONDUCTOR DEVICE HAVING A METAL-SEMICONDUCTOR COMPOUND
5545893	23-Dec-14	OPTOCOUPLER PACKAGE AND METHOD FOR MAKING
5545912	27-Oct-14	ELECTRONIC DEVICE ENCLOSURE INCLUDING A CONDUCTIVE CAP AND SUBSTRATE
5546040	13-Aug-13	POWER EFFICIENT TRANSISTOR AND METHOD THEREFOR
5546047	27-Feb-15	METHOD AND APPARATUS OF AN OPERATIONAL AMPLIFIER WITH A WIDE DYNAMIC RANGE
5546333	05-Dec-14	A DATA PROCESSOR HAVING A DATA TABLE FOR PERFORMING A DUAL FUNCTION OF ALPHANUMERIC NOTICE AND NUMERICAL CALCULATION
5546355	24-Feb-15	INTEGRATED CIRCUIT MEMORY HAVING A SELF-TIMED WRITE PULSE INDEPENDENT OF CLOCK FREQUENCY AND DUTY

		CYCLE
5546588	13-Aug-13	METHOD AND APPARATUS FOR PREVENTING A DATA PROCESSING SYSTEM FROM ENTERING A NON-RECOVERABLE STATE
5548233	28-Feb-15	CIRCUIT AND METHOD OF BIASING A DRIVE TRANSISTOR TO A DATA BUS
5548685	03-Jan-14	ARTIFICIAL NEURON USING ADDER CIRCUIT AND METHOD OF USING SAME
5548752	10-Aug-14	METHOD AND SYSTEM FOR STORING DATA IN A MEMORY DEVICE
5548794	05-Dec-14	DATA PROCESSOR AND METHOD FOR PROVIDING SHOW CYCLES ON A FAST MULTIPLEXED BUS
5550090	05-Sep-15	METHOD FOR FABRICATING A MONOLITHIC SEMICONDUCTOR DEVICE WITH INTEGRATED SURFACE MICROMACHINED STRUCTURES
5550480	05-Jul-14	METHOD AND MEANS FOR CONTROLLING MOVEMENT OF A CHUCK IN A TEST APPARATUS
5550503	28-Apr-15	CIRCUIT AND METHOD FOR REDUCING VOLTAGE ERROR WHEN CHARGING AND DISCHARGING A CAPACITOR THROUGH A TRANSMISSION GATE
5550774	05-Sep-15	MEMORY CACHE WITH LOW POWER CONSUMPTION AND METHOD OF OPERATION
5551076	06-Sep-14	CIRCUIT AND METHOD OF SERIES BIASING A SINGLE-ENDED MIXER
5551627	29-Sep-14	ALLOY SOLDER CONNECT ASSEMBLY AND METHOD OF CONNECTION
5552332	02-Jun-15	PROCESS FOR FABRICATING A MOSFET DEVICE HAVING REDUCED REVERSE SHORT CHANNEL EFFECTS
5553019	23-Jan-15	WRITE ONCE READ MANY MEMORY USING EEPROM CELLS
5553022	27-Dec-14	INTEGRATED CIRCUIT IDENTIFICATION APPARATUS AND METHOD
5553236	03-Mar-15	METHOD AND APPARATUS FOR TESTING A CLOCK STOPPING/STARTING FUNCTION OF A LOW POWER MODE IN A DATA PROCESSOR
5553255	20-Dec-13	DATA PROCESSOR WITH PROGRAMMABLE LEVELS OF SPECULATIVE INSTRUCTION FETCHING AND METHOD OF OPERATION
5553566	22-Jun-15	HIGHLY DOPED N+ SUBSTRATES AND METHOD FOR MAKING
5554869	31-May-14	ELECTRICALLY PROGRAMMABLE READ-ONLY MEMORY AND ARRAY
5554889	10-Sep-13	STRUCTURE AND METHOD FOR METALLIZATION OF SEMICONDUCTOR DEVICES
5554940	05-Jul-14	BUMPED SEMICONDUCTOR DEVICE AND METHOD FOR PROBING THE SAME
5554942	13-Mar-15	INTEGRATED CIRCUIT MEMORY HAVING A POWER SUPPLY INDEPENDENT INPUT BUFFER
5555513	28-Jul-14	DATA PROCESSING SYSTEM HAVING A COMPENSATION CIRCUIT FOR COMPENSATING FOR CAPACITIVE COUPLING ON A BUS
5557142	17-Sep-13	SHIELDED SEMICONDUCTOR DEVICE PACKAGE
5557224	15-Apr-14	APPARATUS AND METHOD FOR GENERATING A PHASE-CONTROLLED CLOCK SIGNAL
5557615	03-Jan-15	METHOD AND APPARATUS FOR IDENTIFYING EMBEDDED

		FRAMING BITS
5559054	23-Dec-14	METHOD FOR BALL BUMPING A SEMI CONDUCTOR DEVICE
5559359	29-Jul-14	MICROWAVE INTEGRATED CIRCUIT P ASSIVE ELEMENT STRUCTURE AND M ETHOD FOR REDUCING SIGNAL PROP AGATION LOSSES
5559500	09-Mar-15	OVERCURRENT SENSE CIRCUIT
5559981	14-Feb-14	PSEUDO STATIC MASK OPTION REGI STER AND METHOD THEREFOR
5561302	26-Sep-14	ENHANCED PERFORMANCE MOSFET
5561391	31-Aug-15	CLAMP CIRCUIT AND METHOD FOR D ETECTING AN ACTIVATION OF SAME
5561473	18-Jan-15	A CONTOUR CORRECTION SIGNAL GE NERATING CIRCUIT
5561614	30-Jan-15	METHOD AND APPARATUS FOR TESTI NG PIN ISOLATION FOR AN INTEGR ATED CIRCUIT IN A LOW POWER MO DE OF OPERATION
5561738	25-Mar-14	A DATA PROCESSOR FOR EXECUTING A FUZZY LOGIC OPERATION AND M ETHOD THEREFOR
5563703	20-Jun-14	LEAD COPLANARITY INSPECTION AP PARATUS AND METHOD THEREOF
5563779	05-Dec-14	METHOD AND APPARATUS FOR A REG ULATED SUPPLY ON AN INTEGRATED CIRCUIT
5564089	08-Oct-13	CURRENT CONTROLLED VARIABLE FR EQUENCY OSCILLATOR HAVING AN I MPROVED OPERATIONAL TRANSCONDU CTANCE AMPLIFIER
5564091	24-Mar-15	METHOD AND APPARATUS FOR OPERA TING AN AUTOMATIC FREQUENCY CO NTROL IN A RADIO
5565386	14-Oct-14	METHOD FOR CONNECTING TO INTEG RATED CIRCUITRY
5565690	02-Feb-15	METHOD FOR DOPING STRAINED HET ERO JUNCTION SEMICONDUCTOR DEV ICES AND STRUCTURE
5565705	02-May-14	ELECTRONIC MODULE FOR REMOVING HEAT FROM A SEMICONDUCTOR DIE
5565813	15-May-15	APPARATUS FOR A LOW VOLTAGE DI FFERENTIAL AMPLIFIER INCORPORA TING SWITCHED CAPACITORS
5566127	15-Oct-13	METHOD FOR BUILDING A COMPILED STATIC RAM
5566322	19-Nov-13	METHOD AND APPARATUS FOR PERFO RMING READ ACCESSES FROM A COU NTER WHICH AVOID LARGE ROLLOVE R ERROR WHEN MULTIPLE READ ACC ESS CYCLES ARE USED
5566877	01-May-15	METHOD FOR INSPECTING A SEMICO NDUCTOR DEVICE
5567648	29-Aug-14	INTERCONNECT BUMP APPARATUS AN D METHOD FOR FORMING SAME
5568492	06-Jun-14	CIRCUIT AND METHOD OF JTAG TES TING MULTICHIP MODULES
5569937	28-Aug-15	HIGH BREAKDOWN VOLTAGE SILICON CARBIDE TRANSISTOR
5570310	05-Dec-14	METHOD AND DATA PROCESSOR FO R FINDING A LOGARITHM OF A NUM BER
5571374	02-Oct-15	METHOD OF ETCHING SILICON CARB IDE
5571734	03-Oct-14	METHOD FOR FORMING A DIELECTRI C HAVING IMPROVED PERFORMANCE
5572066	03-Jan-15	LEAD-ON-CHIP SEMICONDUCTOR DEV ICE AND METHOD FOR ITS FABRICA TION
5572467	24-Apr-15	ADDRESS COMPARISON IN AN INTEG RATED CIRCUIT MEMORY

5572482	15-Jan-12	BLOCK ARCHITECTED STATUS RAM C ONFIGURABLE FOR DIFFERENT WORD WIDTHS AND ASSOCIATED METHOD FOR FORMING A PHYSICAL LAYOUT OF THE STATIC RAM
5574457	12-Jun-15	SWITCHED CAPACITOR GAIN STAGE
5574515	20-Jun-15	VOLTAGE CONTROLLED OSCILLATOR CIRCUIT AND AUTOMATIC FINE TU NING CIRCUIT FOR TV
5574894	03-Nov-14	INTEGRATED CIRCUIT DATA PROCES SOR WHICH PROVIDES EXTERNAL SE NSIBILITY OF INTERNAL SIGNALS DURING RESET
5578167	31-Jan-16	SUBSTRATE HOLDER AND METHOD OF USE
5578529	02-Jun-15	RINSE SPRAY BAR FOR USE IN CHE MICAL MECHANICAL POLISHING
5578850	21-Apr-15	A VERTICALLY ORIENTED DRAM STR UCTURE
5578856	26-Nov-13	BIMOS DEVICE HAVING A BIPOLAR TRANSISTOR AND A MOS TRIGGERIN G TRANSISTOR
5578860	01-May-15	MONOLITHIC HIGH FREQUENCY INTE GRATED CIRCUIT STRUCTURE HAVIN G A GROUNDED SOURCE CONFIGURAT ION
5579257	31-Aug-15	METHOD FOR READING AND RESTORI NG DATA IN A DATA STORAGE ELEM ENT
5579492	26-Nov-13	DATA PROCESSING SYSTEM AND A M ETHOD FOR DYNAMICALLY IGNORING BUS TRANSFER TERMINATION CONTR OL SIGNALS FOR A PREDETERMINED AMOUNT OF TIME
5580815	03-Dec-13	PROCESS FOR FORMING FIELD ISOL ATION AND A STRUCTURE OVER A S EMICONDUCTOR SUBSTRATE
5581215	20-Jun-15	VOLTAGE CONTROLLED OSCILLATOR HAVING FREQUENCY AND AMPLITUD E CONTROLLING LOOPS
5581432	25-Jul-15	CLAMP CIRCUIT AND METHOD FOR I DENTIFYING A SAFE OPERATING AR EA
5581661	30-Jun-14	ARTIFICIAL NEURON USING ADDER CIRCUIT AND METHOD OF USING SA ME
5581695	03-Dec-13	SOURCE-LEVEL IN-CIRCUIT SOFTWARE CODE DEBUGGING INSTRUMENT
5581775	03-Oct-14	A HISTORY BUFFER SYSTEM
5583068	10-Dec-13	PROCESS FOR FORMING A CAPACITO R HAVING A METAL- OXIDE DIELECT RIC
5583350	02-Nov-15	FULL COLOR LIGHT EMITTING DIOD E DISPLAY ASSEMBLY
5583355	22-Jun-12	A SELF-ALIGNED FET HAVING ETCH ED OHMIC CONTACTS
5583370	04-Mar-14	TAB SEMICONDUCTOR DEVICE HAVIN G DIE EDGE PROTECTION
5583377	15-Jul-12	PAD ARRAY SEMICONDUCTOR DEVICE HAVING A HEAT SINK WITH DIE R ECEIVING CAVITY AND METHOD FOR MAKING THE SAME
5583747	10-Dec-13	THERMOPLASTIC INTERCONNECT FOR ELECTRONIC DEVICE AND METHOD FOR MAKING
5583787	08-Mar-14	A METHOD AND DATA PROCESSING S YSTEM FOR DETERMINING ELECTRIC AL CIRCUIT PATH DELAYS
5583891	31-Oct-14	NOISE ATTENUATION CIRCUIT FOR AMPLITUDE MODULATED RADIO AND METHOD THEREFOR
5583964	02-May-14	COMPUTER UTILIZING NEURAL NETW ORK AND METHOD OF USING SAME
5584027	31-Aug-14	A METHOD AND APPARATUS FOR FIN DING INDUCTION VARIABLES FOR U SE IN COMPILING COMPUTER INSTR

		CTIONS
5584031	10-Dec-13	SYSTEM AND METHOD FOR EXECUTING A LOW POWER DELAY INSTRUCTION
5584053	04-Aug-15	COMMONLY COUPLED HIGH FREQUENCY TRANSMITTING/RECEIVING SWITCHING MODULE
5584059	10-Dec-13	DC OFFSET REDUCTION IN A ZERO-IF TRANSMITTER
5586279	28-Jan-14	DATA PROCESSING SYSTEM AND METHOD FOR TESTING A DATA PROCESSOR HAVING A CACHE MEMORY
5586293	17-Dec-13	REAL TIME CACHE IMPLEMENTED BY ON-CHIP MEMORY HAVING STANDARD AND CACHE OPERATING MODES
5587342	03-Apr-15	METHOD OF FORMING AN ELECTRICAL INTERCONNECT
5587605	24-Dec-13	PACKAGE FOR MATING WITH A SEMICONDUCTOR DIE
5587883	03-Dec-12	LEAD FRAME ASSEMBLY FOR SURFACE MOUNT INTEGRATED CIRCUIT POWER PACKAGE
5589402	23-Nov-13	PROCESS FOR MANUFACTURING A PACKAGE FOR MATING WITH A BARE SEMICONDUCTOR DIE
5589423	03-Oct-14	PROCESS FOR FABRICATING A NON-SILICIDED REGION IN AN INTEGRATED CIRCUIT
5590055	15-Feb-14	METHOD FOR DESCRIBING MEASURED RESULTS
5590232	16-Feb-15	OPTIC PACKAGE AND METHOD OF MAKING
5590241	31-Dec-13	SPEECH PROCESSING SYSTEM AND METHOD FOR ENHANCING SPEECH SIGNALS IN A NOISY ENVIRONMENT
5591666	07-Aug-15	SEMICONDUCTOR DEVICE AND METHOD OF FABRICATION
5592025	19-Jul-14	PAD ARRAY SEMICONDUCTOR DEVICE
5592076	03-Jul-15	BASE CURRENT SUPPLY CIRCUIT FOR MULTIPLE CURRENT SOURCES
5592411	02-Nov-15	NON-VOLATILE REGISTER AND METHOD FOR ACCESSING DATA THEREIN
5592493	13-Sep-14	SERIAL SCAN CHAIN ARCHITECTURE FOR A DATA PROCESSING SYSTEM AND METHOD OF OPERATION
5592634	16-May-14	A ZERO-CYCLE MULTI-STATE BRANCH CACHE PREDICTION DATA PROCESSING SYSTEM AND METHOD THEREOF
5593538	29-Sep-15	METHOD FOR ETCHING A DIELECTRIC LAYER ON A SEMICONDUCTOR
5593903	04-Mar-16	METHOD OF FORMING CONTACT PADS FOR WAFER LEVEL TESTING AND BURN-IN OF SEMICONDUCTOR DEVICE
5594273	14-Jan-14	APPARATUS FOR PERFORMING WAFER LEVEL-TESTING OF INTEGRATED CIRCUITS WHERE TESTPADS LIE WITHIN INTEGRATED CIRCUIT DIE BUT OVERLY NO ACTIVE CIRCUITRY FOR IMPROVED YIELD
5594437	01-Aug-14	CIRCUIT AND METHOD OF UNPACKING A SERIAL BITSTREAM
5594880	14-Jan-14	METHOD AND APPARATUS FOR DETERMINING INSTRUCTION EXECUTION ORDERING IN A DATA PROCESSING SYSTEM
5595602	14-Aug-15	DIFFUSER FOR UNIFORM GAS DISTRIBUTION IN SEMICONDUCTOR PROCESSING AND METHOD FOR USING THE SAME
5596172	07-May-13	PLANAR ENCAPSULATION PROCESS
5597110	25-Aug-15	METHOD FOR FORMING A SOLDER BUMP BY SOLDER-SETTING OR THE LIKE
5597737	03-Nov-15	METHOD FOR TESTING AND BURNING-IN A SEMICONDUCTOR WAFER

5597768	21-Mar-16	METHOD OF FORMING A GA203 DIELECTRIC LAYER
5598362	22-Dec-14	APPARATUS AND METHOD FOR PERFORMING BOTH 24 BIT AND 16 BIT ARITHMETIC
5598550	28-Jan-14	CACHE CONTROLLER FOR PROCESSING SIMULTANEOUS CACHE ACCESSES
5598569	17-Oct-14	DATA PROCESSOR HAVING OPERATING MODES SELECTED BY AT LEAST ONE MASK OPTION BIT AND METHOD THEREFOR
5600071	05-Sep-15	VERTICALLY INTEGRATED SENSOR STRUCTURE AND METHOD
5600787	31-May-14	METHOD AND DATA PROCESSING SYSTEM FOR VERIFYING CIRCUIT TEST VECTORS
5602491	16-Mar-15	INTEGRATED CIRCUIT TESTING BOARD HAVING CONSTRAINED THERMAL EXPANSION CHARACTERISTICS
5603112	11-Feb-14	RECEIVED SIGNAL STRENGTH APPARATUS
5604160	29-Jul-16	METHOD FOR PACKAGING SEMICONDUCTOR DEVICES
5604373	03-Apr-15	CIRCUIT AND METHOD OF REVERSE VOLTAGE PROTECTION USING A LATERAL TRANSISTOR HAVING A COLLECTOR RING SURROUNDING ITS BASE REGION
5604454	29-Sep-15	INTEGRATED CIRCUIT WITH LOW OUTPUT BUFFER ENERGY CONSUMPTION AND RELATED METHOD
5604640	31-Jan-14	BUSINESS CARD SCANNER AND METHOD OF USE
5604700	28-Jul-15	NON-VOLATILE MEMORY CELL HAVING A SINGLE POLYSILICON GATE
5604926	07-Mar-15	PHASE LOCKED LOOP CIRCUIT WITH CURRENT MODE FEEDBACK
5605615	05-Dec-14	METHOD AND APPARATUS FOR PLATING METALS
5605865	03-Apr-15	METHOD FOR FORMING SELF-ALIGNED SILICIDE IN A SEMICONDUCTOR DEVICE USING VAPOR PHASE REACTION
5606184	04-May-15	HETEROSTRUCTURE FIELD EFFECT DEVICE HAVING REFRACTORY OHMIC CONTACT DIRECTLY ON CHANNEL LAYER AND METHOD FOR MAKING
5606275	05-Sep-15	BUFFER CIRCUIT HAVING VARIABLE OUTPUT IMPEDANCE
5606319	07-Aug-15	METHOD AND APPARATUS FOR INTERPOLATION AND NOISE SHAPING IN A SIGNAL CONVERTER
5606577	26-Jan-15	METHOD AND APPARATUS FOR A DMT TRANSMITTER HAVING A DATA FLOW MATTER COUPLED DIRECTLY TO A CONSTITUTION ENCODER
5606682	07-Apr-15	DATA PROCESSOR WITH BRANCH TARGET ADDRESS CACHE AND SUBROUTINE RETURN ADDRESS CACHE AND METHOD OF OPERATION
5606715	26-Jun-15	FLEXIBLE RESET CONFIGURATION OF A DATA PROCESSING SYSTEM AND METHOD THEREFOR
5606731	07-Mar-15	ZERO IF RECEIVER WITH TRACKING SECOND LOCAL OSCILLATOR AND DEMODULATOR PHASE LOCKED LOOP OSCILLATOR
5607000	31-Oct-14	NON-VENTING AUTOMATED LIQUID TRANSFER SYSTEM
5608345	04-Mar-14	PROGRAMMABLE SWITCHED CAPACITOR CIRCUIT
5608795	11-Apr-15	TELEPHONE LINE INTERFACE CIRCUIT
5610425	06-Feb-15	INPUT/OUTPUT ELECTROSTATIC DISCHARGE PROTECTION CIRCUIT FOR AN INTEGRATED CIRCUIT
5610543	11-Mar-14	DELAY LOCKED LOOP FOR DETECTING THE PHASE DIFFERENCE OF TWO SIGNALS HAVING DIFFERENT FREQUENCIES

5612228	24-Apr-16	METHOD OF MAKING CMOS WITH ORGANIC AND INORGANIC SEMICONDUCTING REGION
5612232	29-Mar-16	A METHOD OF FABRICATING SEMICONDUCTOR DEVICES AND THE DEVICES
5612576	18-Mar-14	SELF-OPENING VENT HOLE IN AN OVERMOLDED SEMICONDUCTOR DEVICE
5612861	19-Jul-15	METHOD AND APPARATUS FOR LOW VOLTAGE CMOS START CIRCUIT
5612974	01-Nov-14	CONVOLUTIONAL ENCODER FOR USE ON AN INTEGRATED CIRCUIT THAT PERFORMS MULTIPLE COMMUNICATION TASKS
5613119	25-Jul-14	DATA PROCESSOR INITIALIZATION PROGRAM AND METHOD THEREFOR
5614131	01-May-15	METHOD OF MAKING AN OPTO-ELECTRONIC DEVICE
5614739	02-Jun-15	HIGFET AND METHOD
5614816	20-Nov-15	LOW VOLTAGE REFERENCE CIRCUIT AND METHOD OF OPERATION
5615473	01-Apr-14	METHOD OF MAKING A FERRITE/SEMICONDUCTOR RESONATOR/FILTER
5616886	05-Jun-15	WIREBONDLESS MODULE PACKAGE
5617035	01-Nov-13	METHOD FOR TESTING INTEGRATED DEVICES
5617054	14-Dec-15	SWITCHED CAPACITOR VOLTAGE ERROR COMPENSATING CIRCUIT
5617348	24-Jul-15	LOW POWER DATA TRANSLATION CIRCUIT AND METHOD OF OPERATION
5617531	02-Nov-13	DATA PROCESSOR HAVING A BUILT-IN INTERNAL SELF TEST CONTROLLER FOR TESTING A PLURALITY OF MEMORIES INTERNAL TO THE DATA PROCESSOR
5617559	31-Aug-14	MODULAR CHIP SELECT CONTROL CIRCUIT AND METHOD FOR PERFORMING PIPELINED MEMORY ACCESSES
5618351	28-Nov-15	THERMAL PROCESSING APPARATUS AND PROCESS
5618688	08-Apr-14	METHOD OF FORMING A MONOLITHIC SEMICONDUCTOR INTEGRATED CIRCUIT HAVING AN N-CHANNEL JFET
5619064	06-Jun-14	III-V SEMICONDUCTOR GATE STRUCTURE AND METHOD OF MANUFACTURE
5619156	29-Aug-15	LOW VOLTAGE INHIBIT CIRCUIT AND INTEGRATED CIRCUIT USING SAME
5619408	10-Feb-15	METHOD AND SYSTEM FOR RECODING NONEFFECTIVE INSTRUCTIONS WITHIN A DATA PROCESSING SYSTEM
5619418	16-Feb-15	LOGIC GATE SIZE OPTIMIZATION PROCESS FOR AN INTEGRATED CIRCUIT WHEREBY CIRCUIT SPEED IS IMPROVED WHILE CIRCUIT AREA IS OPTIMIZED
5619505	26-Jan-15	METHOD FOR PRODUCING AND RECOVERING A DATA STREAM FOR A DMT TRANSCEIVER
5619687	08-Apr-14	QUEUE SYSTEM HAVING A TIME-OUT FEATURE AND METHOD THEREFOR
5621225	18-Jan-16	LED DISPLAY PACKAGING WITH SUBSTRATE REMOVAL AND METHOD OF FABRICATION
5621233	16-Sep-14	ELECTRICALLY PROGRAMMABLE READ-ONLY MEMORY CELL
5621680	01-Apr-16	DATA STORAGE ELEMENT AND METHOD FOR READING DATA THEREFROM
5621800	01-Nov-14	AN INTEGRATED CIRCUIT THAT PERFORMS MULTIPLE COMMUNICATION TASKS

5623159	03-Oct-14	INTEGRATED CIRCUIT ISOLATION S TRUCTURE FOR SUPPRESSING HIGH- FREQUENCY CROSS-TALK
5623234	04-Mar-16	CLOCK SYSTEM
5623519	06-Dec-13	APPARATUS FOR COMPARING THE WE IGH T OF A BINARY WORD TO A NUM BER
5623664	25-Jul-14	AN INTERACTIVE MEMORY ORGANIZA TION SYSTEM AND METHOD THEREFO R
5623687	26-Jun-15	RESET CONFIGURATION IN A DATA PROCESSING SYSTEM AND METHOD T HERFOR
5624854	29-Apr-14	SEMICONDUCTOR DEVICE AND METHO D OF FORMATION
5625224	10-Aug-14	METHOD AND APPARATUS FOR AN IN TEGRATED CIRCUIT CHIP CARRIER HAVING IMPROVED MOUNTING PAD D ENSITY
5625316	01-Jul-14	TUNING CIRCUIT FOR AN RC FILTE R
5625361	14-Nov-14	PROGRAMMABLE CAPACITOR ARRAY A ND METHOD OF PROGRAMMING
5626728	06-May-14	PIEZOELECTRIC LEAD ZIRCONIUM TITANATE DEVICE AND METHOD FOR FORMING SAME
5627385	28-Aug-15	LATERAL SILICON CARBIDE TRANSI STOR
5627492	03-Nov-15	CIRCUIT AND METHOD FOR ISOLATI NG CIRCUIT BLOCKS FOR REDUCING POWER DISSIPATION
5627890	10-Apr-15	TELEPHONE LINE INTERFACE CIRCU IT
5628026	05-Dec-14	MULTI-DIMENSIONAL DATA TRANSFE R IN A DATA PROCESSING SYSTEM AND METHOD THEREFOR
5628292	01-Apr-16	METHOD AND SYSTEM FOR GENERATING AN ENGINE POSITION DEPENDENT OUTPUT CONTROL SIGNAL
5628862	13-May-14	POLISHING PAD FOR CHEMICAL-MEC HANICAL POLISHING OF A SEMICON DUCTOR SUBSTRATE
5628922	14-Jul-15	ELECTRICAL FLAME-OFF WAND
5629215	01-Mar-16	METHOD OF FABRICATING AND CONT ACTING ULTRA-SMALL THREE TERMI NAL SEMICONDUCTOR DEVICES
5629533	06-Feb-15	OPTICAL SENSOR AND METHOD
5629630	27-Feb-15	SEMICONDUCTOR WAFER CONTACT SY STEM AND METHOD FOR CONTACTING A SEMICONDUCTOR WAFER
5629643	21-Nov-14	FEEDBACK LATCH AND METHOD THER EFOR
5629860	16-May-14	METHOD FOR DETERMINING TIMING DELAYS ASSOCIATED WITH PLACEME NT AND ROUTING OF AN INTEGRATE D CIRCUIT
5630001	22-May-15	IMAGE GENERATOR FOR USE IN IMA GE MANIFESTATION APPARATUS
5630222	04-Dec-15	METHOD AND APPARATUS FOR GENER ATING MULTIPLE SIGNALS AT MULT IPLE FREQUENCIES
5630228	24-Apr-15	DOUBLE BALANCE MIXER CIRCUIT W ITH ACTIVE FILTER LOAD FOR A P ORTABLE COMMUNICATION RECEIVER
5631175	29-Apr-14	ELEVATED-GATE FIELD EFFECT TRA NSISTOR STRUCTURE AND FABRICAT ION METHOD
5631178	31-Jan-15	METHOD FOR MANUFACTURING A STA BLE ARSENIC DOPED SEMICONDUCTO R DEVICE
5631192	02-Oct-15	SEMICONDUCTOR DEVICE ON AN OPP OSED LEADFRAME AND METHOD FOR MAKING
5631492	21-Jan-14	A STANDARD CELL HAVING A GROUN D CAPACITOR AND A POWER SUPPLY CAPACITOR FOR REDUCING NOISE AND METHOD OF FORMATION

5631548	30-Oct-15	POWER OFF-LOADING CIRCUIT AND METHOD FOR DISSIPATING POWER
5631592	30-Sep-13	PULSE GENERATION/SENSING ARRANGEMENT FOR USE IN A MICROPROCESSOR SYSTEM
5631962	23-Oct-15	CIRCUIT AND METHOD OF ENCRYPTING KEY VALIDATION
5632854	21-Aug-15	PRESSURE SENSOR METHOD OF FABRICATION
5633186	14-Aug-15	PROCESS FOR FABRICATING A NON-VOLATILE MEMORY CELL IN A SEMICONDUCTOR DEVICE
5633640	05-Dec-14	METHOD AND APPARATUS FOR A DATA CONVERTER WITH A SINGLE OPERATIONAL AMPLIFIER
5633895	03-Aug-15	COMMUNICATION DEVICE WITH SYNCHRONIZED ZERO CROSSING DEMODULATOR AND METHOD
5634202	27-May-14	METHOD AND APPARATUS FOR INTEGRATING A PLURALITY OF ANALOG INPUT SIGNALS PRIOR TO TRANSMITTING A COMMUNICATIONS SIGNAL
5635422	03-Jun-14	DIFFUSING DOPANTS INTO A SEMICONDUCTOR WAFER
5635732	03-Jun-14	SILICON CARBIDE LOCOS VERTICAL MOSFET DEVICE
5635767	02-Jun-15	SEMICONDUCTOR DEVICE HAVING BUILT-IN HIGH FREQUENCY BYPASS CAPACITOR AND METHOD FOR ITS FABRICATION
5635893	29-Sep-13	RESISTOR STRUCTURE AND INTEGRATED CIRCUIT
5636224	28-Apr-15	METHOD AND APPARATUS FOR INTERLEAVE/DE-INTERLEAVE ADDRESSING IN DATA COMMUNICATION CIRCUITS
5636228	16-Jan-16	SCAN REGISTER WITH DECOUPLED SCAN ROUTING
5636354	06-Sep-14	PROCESSOR WITH SERIAL ACCESS SET ASSOCIATIVE MEMORY CACHE INTERFACE AND METHOD
5637264	29-Sep-15	METHOD OF FABRICATING AN OPTICAL WAVEGUIDE
5637834	03-Feb-15	MULTILAYER CIRCUIT SUBSTRATE AND METHOD FOR FORMING SAME
5637951	10-Aug-15	ELECTRON SOURCE FOR MULTI-BEAM ELECTRON LITHOGRAPHY SYSTEM
5638020	08-Mar-16	SWITCHED CAPACITOR DIFFERENTIAL CIRCUITS
5638520	31-Mar-15	METHOD AND APPARATUS FOR DISTRIBUTING BUS LOADING IN A DATA PROCESSING SYSTEM
5638528	10-Jun-14	A DATA PROCESSING SYSTEM AND A METHOD FOR CYCLING LONGWORD ADDRESSES DURING A BURST BUS CYCLE
5638798	25-Mar-16	METHOD AND SYSTEM FOR GENERATING IGNITION COIL CONTROL PULSES
5639683	01-Dec-14	STRUCTURE AND METHOD FOR INTEGRATING MICROWAVE COMPONENTS ON A SUBSTRATE
5639687	20-Sep-14	METHOD AND STRUCTURE FOR FORMING AN INTEGRATED CIRCUIT PATTERN ON A SEMICONDUCTOR SUBSTRATE
5639695	02-Nov-14	LOW-PROFILE BALL-GRID ARRAY SEMICONDUCTOR PACKAGE AND METHOD
5639989	17-Jun-14	SHIELDED ELECTRONIC COMPONENT ASSEMBLY AND METHOD FOR MAKING THE SAME
5640025	01-Dec-15	HIGH FREQUENCY SEMICONDUCTOR TRANSISTOR
5640460	03-Apr-15	AMPLITUDE ADJUST CIRCUIT AND METHOD THEREOF
5640534	05-Oct-14	METHOD AND SYSTEM FOR CONCURRENT ACCESS IN A DATA CACHE ARRAY UTILIZING MULTIPLE MATCH LINE SELECTION PATHS

5640548	17-Jun-14	METHOD AND APPARATUS FOR UNSTACKING REGISTERS IN A DATA PROCESSING SYSTEM
5640681	17-Jun-14	BOOT-STRAPPED CASCODE CURRENT MIRROR
5641695	02-Oct-15	METHOD OF FORMING A SILICON CARBIDE JFET
5641712	07-Aug-15	METHOD AND STRUCTURE FOR REDUCING CAPACITANCE BETWEEN INTERCONNECT LINES
5642305	31-Jan-15	LOGARITHM/INVERSE-LOGARITHM CONVERTER AND METHOD OF USING SAME
5642480	28-Sep-15	METHOD AND APPARATUS FOR ENHANCED SECURITY OF A DATA PROCESSOR
5643405	31-Jul-15	METHOD FOR POLISHING A SEMICONDUCTOR SUBSTRATE
5644519	07-Apr-15	METHOD AND APPARATUS FOR A MULTIPLY AND ACCUMULATE CIRCUIT HAVING A DYNAMIC SATURATION RANGE
5644528	21-Nov-15	NON-VOLATILE MEMORY HAVING A CELL APPLYING TO MULTI-BIT DATA BY MULTI-LAYERED FLOATING GATE ARCHITECTURE AND PROGRAMMING METHOD FOR THE SAME
5644756	07-Apr-15	INTEGRATED CIRCUIT DATA PROCESSOR WITH SELECTABLE ROUTING OF DATA ACCESSES
5644899	26-Dec-15	METHOD FOR PACKAGING SEMICONDUCTOR COMPONENTS FOR SHIPMENT
5646055	01-May-16	METHOD FOR MAKING BIPOLAR TRANSISTOR
5646060	08-Jul-14	METHOD FOR MAKING AN EEPROM CELL WITH ISOLATION TRANSISTOR
5646550	22-Feb-16	HIGH RELIABILITY OUTPUT BUFFER FOR MULTIPLE VOLTAGE SYSTEM
5646609	03-Jan-15	CIRCUIT AND METHOD FOR SELECTING A CIRCUIT MODULE
5646946	30-Oct-15	APPARATUS AND METHOD FOR SELECTIVELY COMPANDING DATA ON A SLOT-BY-SLOT BASIS
5646949	04-Jun-16	METHOD AND APPARATUS FOR GENERATING INSTRUCTIONS FOR USE IN TESTING A MICROPROCESSOR
5647123	16-Oct-15	IMPROVED METHOD FOR UNDERFILLING EUTECTIC BUMPED FLIP CHIP DIE
5649008	02-Aug-14	CIRCUIT AND METHOD OF REDUCING SIDETONE IN A RECEIVE SIGNAL PATH
5649125	30-Oct-15	METHOD AND APPARATUS FOR PROVIDING VALID ADDRESSES ACROSS A MULTIPLEXED COMMUNICATIONS BUS
5649159	31-Aug-14	DATA PROCESSOR WITH A MULTI-LEVEL PROTECTION MECHANISM MULTI-LEVEL PROTECTION CIRCUIT, AND METHOD THEREFOR
5650356	01-May-11	METHOD FOR REDUCING CORROSION IN OPENINGS ON A SEMICONDUCTOR WAFER
5650749	10-Jun-16	FM DEMODULATOR USING INJECTION LOCKED OSCILLATOR HAVING TUNING FEEDBACK AND LINEARIZING FEEDBACK
5651138	31-Aug-14	DATA PROCESSOR WITH CONTROLLED BURST MEMORY ACCESSES AND METHOD THEREFOR
5652176	24-Feb-15	METHOD FOR PROVIDING TRENCH ISOLATION AND SEMICONDUCTOR DEVICE HAVING THE SAME
5652844	26-Jun-15	FLEXIBLE PIN CONFIGURATION FOR USE IN A DATA PROCESSING SYSTEM DURING A RESET OPERATION AND METHOD THEREFOR
5652903	01-Nov-14	A DSP CO-PROCESSOR FOR USE ON AN INTEGRATED CIRCUIT THAT PERFORMS MULTIPLE COMMUNICATIONS TASKS

5654588	21-Nov-14	APPARATUS FOR PERFORMING WAFER LEVEL TESTING OF INTEGRATED CIRCUITS WHERE THE WAFER USES A SEGMENTED CONDUCTIVE TOP-LAYER BUS STRUCTURE
5655042	03-Apr-15	MOLDED SLOTTED OPTICAL SWITCH STRUCTURE AND METHOD
5656549	19-Aug-16	METHOD OF PACKAGING A SEMICONDUCTOR DEVICE
5656844	27-Jul-15	SEMICONDUCTOR-ON-INSULATOR TRANSISTOR HAVING A DOPING PROFILE FOR FULLY-DEPLETED OPERATION
5656943	30-Oct-15	APPARATUS FOR FORMING A TEST STACK FOR SEMICONDUCTOR WAFER PROBING AND METHOD FOR USING THE SAME
5656951	05-Feb-16	INPUT CIRCUIT AND METHOD FOR HOLDING DATA IN MIXED POWER SUPPLY MODE
5657324	11-Jul-15	BIDIRECTIONAL COMMUNICATION SYSTEM
5658810	04-Apr-14	METHOD FOR MAKING A SENSOR FOR DETERMINING A RATIO OF MATERIALS IN A MIXTURE
5659179	07-Mar-15	ULTRA SMALL SEMICONDUCTOR DEVICES HAVING PATTERNED EDGE PLANAR SURFACES
5659180	13-Nov-15	HETEROJUNCTION INTERBAND TUNNEL DIODES WITH IMPROVED P/V CURRENT RATIOS
5659499	24-Nov-15	MAGNETIC MEMORY AND METHOD THEREFOR
5659648	29-Sep-15	POLYIMIDE OPTICAL WAVEGUIDE HAVING ELECTRICAL CONDUCTIVITY
5659698	01-Nov-14	METHOD AND APPARATUS FOR GENERATING A CIRCULAR BUFFER ADDRESS IN INTEGRATED CIRCUIT THAT PERFORMS MULTIPLE COMMUNICATION TASKS
5659950	23-Mar-15	ELECTRONIC DIE PACKAGE ASSEMBLY HAVING A SUPPORT AND METHOD
5661042	28-Aug-15	PROCESS FOR ELECTRICALLY CONNECTING ELECTRICAL DEVICES USING A CONDUCTIVE ANISOTROPIC MATERIAL
5661048	21-Mar-15	METHOD OF MAKING AN INSULATED GATE SEMICONDUCTOR DEVICE
5661082	20-Jan-15	SEMICONDUCTOR DEVICE HAVING A BOND PAD AND A PROCESS FOR FORMING THE DEVICE
5661088	11-Jan-16	ELECTRONIC COMPONENT AND METHOD OF PACKAGING
5661312	30-Mar-15	SILICON CARBIDE MOSFET
5663690	06-Mar-16	CONSTANT HIGH Q VOLTAGE CONTROLLED OSCILLATOR
5664168	29-Nov-13	METHOD AND APPARATUS IN A DATA PROCESSING SYSTEM FOR SELECTIVELY INSERTING BUS CYCLE IDLE TIME
5665202	24-Nov-15	MULTI-STEP PLANARIZATION PROCESS
5665633	06-Apr-15	SEMICONDUCTOR DEVICE HAVING FIELD ISOLATION AND A PROCESS FOR FORMING THE DEVICE
5665658	21-Mar-16	METHOD OF FORMING A DIELECTRIC LAYER STRUCTURE
5666063	23-Oct-16	METHOD AND APPARATUS FOR TESTING AN INTEGRATED CIRCUIT
5666288	21-Apr-15	METHOD AND APPARATUS FOR DESIGNING AN INTEGRATED CIRCUIT
5666300	22-Dec-14	POWER REDUCTION IN A DATA PROCESSING SYSTEM USING PIPELINE REGISTERS AND METHOD THEREFOR
5666509	09-Sep-14	A DATA PROCESSING SYSTEM FOR PERFORMING EITHER A PRECISE MEMORY ACCESS OR AN IMPRECISE MEMORY ACCESS BASED UPON A LOGICAL ADDRESS VALUE AND

		METHOD THE REOF
5667128	28-Sep-15	WORKSTATION FOR PROCESSING A FLEXIBLE MEMBRANCE
5667632	13-Nov-15	METHOD OF DEFINING A LINE WIDTH
5668021	04-Jun-16	PROCESS FOR FABRICATING A SEMI CONDUCTOR DEVICE HAVING A SEGMENTED CHANNEL REGION
5668749	04-May-15	CIRCUIT FOR PERFORMING ARITHMETIC OPERATIONS IN A DEMODULATOR
5668807	30-Nov-15	SYNCHRONIZATION OF TRANSPARENT TDM SUPERCHANNELS
5668972	05-Oct-14	METHOD AND SYSTEM FOR EFFICIENT MISS SEQUENCE CACHE LINE ALLOCATION
5668975	05-Apr-13	METHOD OF REQUESTING DATA BY INTERLACING CRITICAL AND NON-CRITICAL DATA WORDS OF MULTIPLE DATA REQUESTS AND APPARATUS THEREFOR
5670389	11-Jan-16	SEMICONDUCTOR-ON-INSULATOR DEVICE HAVING A Laterally-Graded Channel Region and Method of Making
5670417	25-Mar-16	METHOD OF FABRICATING SELF-ALIGNED SEMICONDUCTOR COMPONENT
5670815	23-Sep-14	LAYOUT FOR NOISE REDUCTION ON A REFERENCE VOLTAGE
5670829	20-Mar-15	PRECISION CURRENT LIMIT CIRCUIT
5670951	17-Jul-15	RADIO COMMUNICATION DEVICE AND METHOD FOR GENERATING THRESHOLD LEVELS IN A RADIO COMMUNICATION DEVICE FOR RECEIVING FOUR
5671223	30-Nov-15	MULTICHANNEL HDLC FRAMING/DEFRAMING MACHINE
5671332	22-Dec-14	DATA PROCESSING SYSTEM FOR PERFORMING EFFICIENT FUZZY LOGIC OPERATIONS AND METHOD THEREFOR
5671370	03-Oct-14	ALTERNATING DATA VALID CONTROL SIGNALS FOR HIGH PERFORMANCE DATA TRANSFER
5671375	26-Dec-15	METHOD FOR COMMUNICATING BETWEEN A MICROPROCESSOR AND AN INTERFACE CIRCUIT
5673001	07-Jun-15	METHOD AND APPARATUS FOR AMPLIFYING A SIGNAL
5673003	29-Mar-16	AMPLIFIER CIRCUIT HAVING A VARIABLE BANDWIDTH
5673130	02-Jan-16	CIRCUIT AND METHOD OF ENCODING AND DECODING DIGITAL DATA TRANSMITTED ALONG OPTICAL FIBERS
5673227	14-May-16	INTEGRATED CIRCUIT MEMORY WITH MULTIPLEXED REDUNDANT COLUMN DATA PATH
5673396	16-Dec-14	ADJUSTABLE DEPTH/WIDTH FIFO BUFFER FOR VARIABLE WIDTH DATA TRANSFERS
5674352	07-Oct-14	PROCESS RELATED TO A MODIFIED POLISHING PAD FOR POLISHING
5674762	28-Aug-15	METHOD OF FABRICATING AN EPROM WITH HIGH VOLTAGE TRANSISTORS
5674780	24-Jul-15	METHOD OF FORMING AN ELECTRICALLY CONDUCTIVE POLYMER BUMP OVER AN ALUMINUM ELECTRODE
5675166	07-Jul-15	FET WITH STABLE THRESHOLD VOLTAGE AND METHOD OF MANUFACTURING THE SAME
5675243	10-Apr-16	VOLTAGE SOURCE DEVICE FOR LOW-VOLTAGE OPERATION
5675469	12-Jul-15	INTEGRATED CIRCUIT WITH ELECTROSTATIC DISCHARGE (ESD) PROTECTION AND ESD PROTECTION CIRCUIT
5675749	02-Jun-15	METHOD AND APPARATUS FOR CONTROLLING SHOW CYCLES IN A DATA PROCESSING SYSTEM

5675822	07-Apr-15	METHOD AND APPARATUS FOR A DIGITAL SIGNAL PROCESSOR HAVING A MULTIPLIERLESS COMPUTATION BLOCK
5677245	14-Oct-14	DUAL CHANNEL SMALL OUTLINE OPTOCOUPLER PACKAGE AND METHOD THEREOF
5677917	29-Apr-16	INTEGRATED CIRCUIT MEMORY USING FUSIBLE LINKS IN A SCAN CHAIN
5678040	29-Oct-13	HIERARCHICAL DESIGN TRANSACTION METHOD
5678215	13-Jun-16	METHOD FOR FREQUENCY LOCKING ONTO A PILOT REFERENCE SIGNAL AFFECTED BY MULTIPATH FADING
5678223	07-Mar-15	METHOD AND APPARATUS FOR AN AUTOMATIC FREQUENCY CONTROL RECEIVER
5679275	03-Jul-15	CIRCUIT AND METHOD OF MODIFYING CHARACTERISTICS OF A UTILIZATION CIRCUIT
5680626	18-May-15	METHOD AND APPARATUS FOR PROVIDING ONLY THAT NUMBER OF CLOCK PULSES NECESSARY TO COMPLETE A TASK
5680632	21-Oct-14	METHOD FOR PROVIDING AN EXTENSIBLE REGISTER IN THE FIRST AND SECOND DATA PROCESSING SYSTEMS
5682340	03-Jul-15	LOW POWER CONSUMPTION CIRCUIT AND METHOD OF OPERATION FOR IMPLEMENTING SHIFTS AND BIT REVERSALS
5682495	09-Dec-14	FULLY ASSOCIATIVE ADDRESS TRANSLATION BUFFER HAVING SEPARATE SEGMENT AND PAGE INVALIDATION
5683548	22-Feb-16	INDUCTIVELY COUPLED PLASMA REACTOR AND PROCESS
5683569	28-Feb-16	METHOD OF SENSING A CHEMICAL AND SENSOR THEREFOR
5683918	01-Apr-16	METHOD OF MAKING SEMICONDUCTOR -ON-INSULATOR DEVICE WITH CLOSED-GATE ELECTRODE
5683934	26-Sep-14	ENHANCED MOBILITY MOSFET DEVICE AND METHOD
5683944	01-Sep-15	METHOD OF FABRICATING A THERMALLY ENHANCED LEAD FRAME
5684928	11-Dec-15	CIRCUIT AND METHOD FOR EVALUATING FUZZY LOGIC RULES
5686352	26-Jul-13	METHOD FOR MAKING A TAB SEMICONDUCTOR DEVICE WITH SELF-ALIGNING CAVITY AND INTRINSIC STAND OFF
5686698	11-Nov-14	PACKAGE FOR ELECTRICAL COMPONENTS AND METHOD FOR MAKING
5686860	25-Sep-15	AMPLIFIER AND CONTROLLING APPARATUS OPERATING FROM A UNIPOLAR POWER SUPPLY
5687104	17-Nov-15	METHOD AND APPARATUS FOR GENERATING DECOUPLED FILTER PARAMETERS AND IMPLEMENTING A BAND DECOUPLED FILTER
5687178	03-Sep-13	METHOD AND APPARATUS FOR TESTING A STATIC RAM
5687327	11-Nov-14	EFFICIENT ADDRESS TRANSFER TECHNIQUE FOR A DATA PROCESSING SYSTEM
5688703	05-Sep-15	METHOD OF MANUFACTURING A GATE STRUCTURE FOR A SEMICONDUCTOR DEVICE
5689217	14-Mar-16	DIRECTIONAL COUPLER AND METHOD OF FORMING SAME
5689659	30-Oct-15	METHOD AND APPARATUS FOR BURSTING OPERAND TRANSFERS DURING DYNAMIC BUS SIZING
5689714	28-Aug-15	METHOD AND APPARATUS FOR PROVIDING LOW POWER CONTROL OF PERIPHERAL DEVICES USING THE REGISTER FILE OF A MICROPROCESSOR
5690877	25-Nov-14	METHOD OF PROCESSING A SEMICONDUCTOR CHIP

		PACKAGE
5691242	26-Feb-16	METHOD FOR MAKING AN ELECTRONIC COMPONENT HAVING AN ORGANIC SUBSTRATE
5691253	25-Nov-14	PROCESS FOR POLISHING AND ANALYZING AN EXPOSED SURFACE OF A PATTERNED SEMICONDUCTOR SUBSTRATE
5691554	15-Dec-15	PROTECTION CIRCUIT
5692218	29-Jan-13	SYSTEM FOR TRANSFERRING DATA BETWEEN INPUT/OUTPUT DEVICES HAVING SEPARATE ADDRESS SPACES IN ACCORDANCE WITH INITIALIZING
5692279	17-Aug-15	MONOLITHIC THIN FILM RESONATOR LATTICE FILTER AND METHOD OF FABRICATION
5692873	31-Mar-15	APPARATUS FOR HOLDING A PIECE OF SEMICONDUCTOR
5693544	02-Jun-15	N-TYPE HIGFET AND METHOD
5693545	28-Feb-16	METHOD FOR FORMING A SEMICONDUCTOR SENSOR FET DEVICE AND SEMICONDUCTOR SENSOR FET DEVICE
5693955	29-Mar-16	TUNNEL TRANSISTOR
5693966	16-Jan-16	POWER MOS TRANSISTOR
5694308	03-Jul-15	METHOD AND APPARATUS FOR A REGULATED LOW VOLTAGE CHARGE PUMP
5694344	15-Jun-15	A METHOD FOR ELECTRICALLY MODELING A SEMICONDUCTOR PACKAGE
5695877	06-Jul-15	METHOD OF DEPOSITING FERRITE FILM (AS AMENDED)
5696394	02-Aug-13	CAPACITOR HAVING A METAL-OXIDE DIELECTRIC
5696666	11-Oct-15	LOW PROFILE EXPOSED DIE CHIP CARRIER PACKAGE
5696797	09-Dec-14	DEMULATOR WITH BASEBAND DOPPLER SHIFT COMPENSATION AND METHOD
5697088	05-Aug-16	BALUN TRANSFORMER
5698777	05-Feb-16	CAMSHAFT REVOLUTION SENSING ASSEMBLY
5698928	17-Aug-15	THIN FILM PIEZOELECTRIC ARRAYS WITH ENHANCED COUPLING AND FABRICATION METHOD
5699009	01-Apr-16	ELECTRONIC FILTERING DEVICE
5699309	13-May-16	METHOD AND APPARATUS FOR PROVIDING USER SELECTABLE LOW POWER AND HIGH PERFORMANCE MEMORY ACCESS MODES
5699422	16-Feb-15	TELECOMMUNICATIONS DEVICE
5699516	22-Dec-14	METHOD AND APPARATUS FOR IMPLEMENTING A IN-ORDER TERMINATION BUS PROTOCOL WITHIN A DATA PROCESSING SYSTEM
5700721	03-Apr-12	STRUCTURE AND METHOD FOR METALLIZATION OF SEMICONDUCTOR DEVICES
5701421	13-Nov-15	PIN AND STATUS BUS STRUCTURE FOR AN INTEGRATED CIRCUIT
5701600	17-Jul-15	RADIO RECEIVER AND METHOD OF CALIBRATING SAME
5702831	06-Nov-15	FERROMAGNETIC GMR MATERIAL
5702981	29-Sep-15	METHOD FOR FORMING A VIA IN A SEMICONDUCTOR DEVICE
5703405	25-Nov-14	INTEGRATED CIRCUIT CHIP FORMED FROM PROCESSING TWO OPPOSING SURFACES OF A WAFER
5703478	05-Apr-16	CURRENT MIRROR CIRCUIT
5703493	25-Oct-15	WAFER HOLDER FOR SEMICONDUCTOR APPLICATIONS

5703805	08-May-16	METHOD FOR DETECTING INFORMATION STORED IN A MRAM CELL HAVING TWO LAYERS IN DIFFERENT THICKNESSES
5703808	21-Feb-16	NON-VOLATILE MEMORY CELL AND METHOD OF PROGRAMMING
5704034	30-Aug-15	METHOD AND CIRCUIT FOR INITIALIZING A DATA PROCESSING SYSTEM
5704039	25-Jul-14	MASK PROGRAMMABLE SECURITY SYSTEM FOR A DATA PROCESSOR AND METHOD THEREFOR
5706036	06-Jan-15	METHOD AND APPARATUS FOR PROVIDING A VIDEO SYNCHRONISING SIGNAL OF A PREDETERMINED POLARITY
5706228	20-Feb-16	METHOD FOR OPERATING A MEMORY ARRAY
5706488	06-Jan-15	A DATA PROCESSING SYSTEM AND METHOD THEREOF
5707881	03-Sep-16	A TEST STRUCTURE AND METHOD FOR PERFORMING BURN-IN TESTING OF A SEMICONDUCTOR PRODUCT WAFER
5707901	29-Jun-14	METHOD UTILIZING AN ETCH STOP LAYER
5708288	02-Nov-15	THIN FILM SILICON ON INSULATOR SEMICONDUCTOR INTEGRATED CIRCUIT WITH ELECTROSTATIC DAMAGE PROTECTION AND METHOD
5708839	05-Jul-14	METHOD AND APPARATUS FOR PROVIDING BUS PROTOCOL SIMULATION
5709744	01-Mar-16	MASKING METHODS DURING SEMICONDUCTOR DEVICE FABRICATION
5710071	04-Dec-15	PROCESS FOR UNDERFILLING A FLIP-CHIP SEMICONDUCTOR DEVICE AND A DEVICE MADE THEREBY
5710455	29-Jul-16	LATERAL MOSFET WITH MODIFIED FIELD PLATES AND DAMAGE AREAS
5710944	09-Feb-16	MEMORY SYSTEM AND DATA COMMUNICATIONS SYSTEM
5712208	27-Jan-15	METHODS OF FORMATION OF SEMICONDUCTOR COMPOSITE GATE DIELECTRIC HAVING MULTIPLE INCORPORATED ATOMIC DOPANTS
5712501	10-Oct-15	GRADED-CHANNEL SEMICONDUCTOR DEVICE
5712589	30-May-15	APPARATUS AND METHOD FOR PERFORMING ADAPTIVE POWER REGULATION FOR AN INTEGRATED CIRCUIT
5712794	03-Nov-15	AUTOMATED METHOD FOR ADDING ATTRIBUTES IDENTIFIED ON A SCHEMATIC DIAGRAM TO AN INTEGRATED CIRCUIT LAYOUT
5714792	03-Feb-15	SEMICONDUCTOR DEVICE HAVING A REDUCED DIE SUPPORT AREA AND METHOD FOR MAKING THE SAME
5714800	21-Mar-16	INTEGRATED CIRCUIT ASSEMBLY HAVING A STEPPED INTERPOSER AND METHOD
5715014	03-Sep-16	CIRCUIT AND METHOD OF PROVIDING PARENTAL DISCRETIONARY CONTROL ON A PIPIC
5715184	03-Feb-15	METHOD OF PARALLEL SIMULATION OF STANDARD CELLS ON A DISTRIBUTED COMPUTER
5715393	16-Aug-13	A METHOD FOR REMOTE SYSTEM PROCESS MONITORING
5715427	26-Jan-16	SEMI-ASSOCIATIVE CACHE WITH MRU/LRU REPLACEMENT
5716760	16-May-15	METHOD FOR PLATING MASKLESS SUBSTRATE
5716866	30-Aug-15	METHOD OF FORMING A SEMICONDUCTOR DEVICE
5716875	01-Mar-16	METHOD FOR MAKING A FERROELECTRIC DEVICE
5717587	10-Feb-15	METHOD AND SYSTEM FOR RECODING NONEFFECTIVE INSTRUCTIONS WITHIN A DATA PROCESSING SYSTEM

5717700	04-Dec-15	METHOD OF CONSTRUCTION OF A SCANNABLE INTEGRATED CIRCUIT
5717772	07-Aug-15	METHOD AND APPARATUS FOR SUPPRESSING ACOUSTIC FEEDBACK IN AN AUDIO SYSTEM
5717858	10-Feb-15	METHOD AND STRUCTURE FOR PRIORITIZING PERFORMANCE MONITORING CELLS IN AN ASYNCHRONOUS TRANSFER MODE (ATM) SYSTEM
5717931	10-Feb-15	METHOD AND APPARATUS FOR COMMUNICATING BETWEEN MASTER AND SLAVE ELECTRONIC DEVICES WHERE THE SLAVE DEVICE MAY BE HAZARDOUS
5717947	10-Feb-15	DATA PROCESSING SYSTEM AND METHOD THEREOF
5719081	03-Nov-15	FABRICATION METHOD FOR A SEMICONDUCTOR DEVICE ON INSULATOR SUBSTRATE USING A TWO STAGE THRESHOLD ADJUST IMPLANT
5719088	13-Nov-15	METHOD OF FABRICATING SEMICONDUCTOR DEVICES WITH A PASSIVATED SURFACE
5719519	20-Nov-15	CIRCUIT AND METHOD FOR RECONSTRUCTING A PHASE CURRENT
5719856	07-Apr-15	TRANSMITTER/RECEIVER INTERFACE APPARATUS AND METHOD FOR A BI-DIRECTIONAL TRANSMISSION PATH
5719878	04-Dec-15	SCANNABLE STORAGE CELL AND METHOD OF OPERATION
5720002	17-Feb-15	NEURAL NETWORK AND METHOD OF USING SAME
5720100	29-Dec-15	ASSEMBLY HAVING A FRAME EMBEDDED IN A POLYMERIC ENCAPSULANT AND METHOD FOR FORMING SAME
5721438	31-Jan-16	HETEROJUNCTION SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURE
5721450	12-Jun-15	MOISTURE RELIEF FOR CHIP CARRIERS
5721451	02-Dec-16	INTEGRATED CIRCUIT ASSEMBLY ADHESIVE AND METHOD THEREOF
5721509	05-Feb-16	CHARGE PUMP HAVING REDUCED THRESHOLD VOLTAGE LOSSES
5721696	28-Aug-15	METHOD AND SYSTEM FOR PERFORMING AN FIR FILTERING OPERATION
5721704	23-Aug-16	CONTROL GATE DRIVER CIRCUIT FOR A NON-VOLATILE MEMORY AND MEMORY USING SAME
5721726	30-Nov-15	TRANSMISSION LOAD CONTROL FOR MULTICHANNEL HDLC TDM LINE
5721871	09-Feb-16	MEMORY SYSTEM AND A DATA COMMUNICATIONS SYSTEM
5721888	13-Nov-15	PERFORMING FLEXIBLE LOGICAL OPERATIONS WITH TIMER CHANNEL OUTPUTS
5721889	13-Nov-15	DATA TRANSFER BETWEEN INTEGRATED CIRCUIT TIMER CHANNELS
5722086	20-Feb-16	METHOD AND APPARATUS FOR REDUCING POWER CONSUMPTION IN A COMMUNICATIONS SYSTEM
5724038	12-Feb-16	NOISE CANCELLING CIRCUIT AND ARRANGEMENT
5724283	14-Jun-15	DATA STORAGE ELEMENT AND METHOD FOR RESTORING DATA
5724557	10-Jul-15	METHOD FOR DESIGNING A SIGNAL DISTRIBUTION NETWORK
5724604	02-Aug-15	DATA PROCESSING SYSTEM FOR ACCESSING AN EXTERNAL DEVICE AND METHOD THEREFOR
5725788	04-Mar-16	APPARATUS AND METHOD FOR PATTERNING A SURFACE
5726087	10-Mar-15	METHOD OF FORMATION OF SEMICONDUCTOR GATE

		DIELECTRIC
5726502	26-Apr-16	BUMPED SEMICONDUCTOR DEVICE WITH ALIGNMENT FEATURES AND METHOD FOR MAKING THE SAME
5726844	01-Apr-16	PROTECTION CIRCUIT AND A CIRCUIT FOR A SEMICONDUCTOR-ON-INSULATOR DEVICE
5726944	05-Feb-16	VOLTAGE REGULATOR FOR REGULATING AN OUTPUT VOLTAGE FROM A CHARGE PUMP AND METHOD THEREFOR
5727005	10-Mar-15	INTEGRATED CIRCUIT MICROPROCESSOR WITH PROGRAMMABLE MEMORY INTERFACE ACCESS TYPES
5727038	06-Sep-16	PHASE LOCKED LOOP USING DIGITAL LOOP FILTER AND DIGITALLY CONTROLLED OSCILLATOR
5727130	31-Aug-15	GENETIC ALGORITHM FOR CONSTRUCTING AND TUNING FUZZY LOGIC SYSTEM
5727172	01-May-15	METHOD AND APPARATUS FOR PERFORMING ATOMIC ACCESSSES IN A DATA PROCESSING SYSTEM
5727229	05-Feb-16	METHOD AND APPARATUS FOR MOVING DATA IN A PARALLEL PROCESSOR
5729149	29-Sep-15	APPARATUS FOR HOLDING A TESTING SUBSTRATE IN A SEMICONDUCTOR WAFER TESTER AND METHOD FOR USING THE SAME
5729151	11-Mar-16	SYSTEM AND METHOD FOR TESTING A PHASE LOCKED LOOP IN AN INTEGRATED CIRCUIT
5729166	10-Jun-16	DIGITALLY IMPLEMENTED FREQUENCY MULTIPLICATION CIRCUIT HAVING ADJUSTABLE MULTIPLICATION RATIO AND METHOD OF OPERATION
5729176	03-May-16	LINEAR DIFFERENTIAL GAIN STAGE
5729223	20-Mar-16	METHOD AND APPARATUS FOR DATA COMPRESSION AND RESTORATION
5729225	23-Sep-16	METHOD AND APPARATUS FOR ASYNCHRONOUS DIGITAL MIXING
5729438	07-Jun-16	DISCRETE COMPONENT PAD ARRAY CARRIER
5729493	23-Aug-16	MEMORY SUITABLE FOR OPERATION AT LOW POWER SUPPLY VOLTAGES AND SENSE AMPLIFIER THEREFOR
5729577	21-May-16	SIGNAL PROCESSOR WITH IMPROVED EFFICIENCY
5729721	13-Nov-15	TIMEBASE SYNCHRONIZATION IN SEPARATE INTEGRATED CIRCUITS OR SEPARATE MODULES
5731709	26-Jan-16	METHOD FOR TESTING A BALL GRID ARRAY SEMICONDUCTOR DEVICE AND A DEVICE FOR SUCH TESTING
5731769	04-Dec-15	MULTI-RATE DIGITAL FILTER APPARATUS AND METHOD FOR SIGMA-DELTA CONVERSION PROCESSES
5732016	02-Jul-16	A MEMORY CELL STRUCTURE IN A MAGNETIC RANDOM ACCESS MEMORY AND A METHOD FOR FABRICATING THEREOF
5732225	13-Nov-15	INTEGRATED CIRCUIT TIMER SYSTEM HAVING A GLOBAL BUS FOR TRANSFERRING INFORMATION BETWEEN LOCAL BUSES
5732381	25-Mar-16	METHOD AND SYSTEM FOR GENERATING A FUEL PULSE WAVEFORM
5732405	24-Mar-15	METHOD AND APPARATUS FOR PERFORMING A CACHE OPERATION IN A DATA PROCESSING SYSTEM
5733794	31-Mar-15	PROCESS FOR FORMING A SEMICONDUCTOR DEVICE WITH ESD PROTECTION
5733806	05-Sep-15	METHOD FOR FORMING A SELF-ALIGNED SEMICONDUCTOR DEVICE

5733827	13-Nov-15	METHOD OF FABRICATING SEMICONDUCTOR DEVICES WITH A PASSIVATED SURFACE
5734201	31-Mar-15	LOW PROFILE SEMICONDUCTOR DEVICE AND METHOD FOR MAKING THE SAME
5734317	01-Jul-16	CURRENT LIMIT CONTROLLER FOR AN AIR BAG DEPLOYMENT SYSTEM
5734605	10-Sep-16	MULTI-LAYER MAGNETIC TUNNELING JUNCTION MEMORY CELLS
5734606	13-Dec-16	A MULTI-PIECE CELL AND A MRAM ARRAY INCLUDING THE CELL
5734879	31-Mar-15	A SATURATION INSTRUCTION IN A DATA PROCESSOR
5734974	27-Mar-16	SELECTIVE CALL RECEIVERS WITH STEPWISE VARIABLE GAIN CONTROL
5737254	27-Oct-15	SYMMETRICAL FILTERING APPARATUS AND METHOD THEREFOR
5737327	29-Mar-16	METHOD AND APPARATUS FOR DEMODULATION AND POWER CONTROL BIT DETECTION IN A SPREAD SPECTRUM COMMUNICATION SYSTEM
5737337	30-Sep-16	METHOD AND APPARATUS FOR INTERLEAVING DATA IN AN ASYMMETRIC DIGITAL SUBSCRIBER LINE (ADSL) TRANSMITTER
5737493	11-Dec-15	AN INSTRUCTION SET FOR EVALUATING FUZZY LOGIC RULES
5737516	30-Aug-15	DATA PROCESSING SYSTEM FOR PERFORMING A DEBUG FUNCTION AND METHOD THEREFOR
5737566	07-Apr-15	DATA PROCESSING SYSTEM HAVING A MEMORY WITH BOTH A HIGH SPEED OPERATING MODE AND A LOW POWER OPERATING MODE AND METHOD THEREFOR
5737584	22-Jan-16	DATA PROCESSING SYSTEM HAVING PROGRAMMABLE EXTERNAL TERMINALS SUCH THAT THE EXTERNAL TERMINALS ARE SELECTIVELY SUBJECTED TO BUS ARBITRATION
5737586	07-Apr-15	A DATA PROCESSING SYSTEM AND METHOD THEREOF
5737751	26-Mar-16	CACHE MEMORY MANAGEMENT SYSTEM HAVING REDUCED RELOADS TO A SECOND LEVEL CACHE FOR ENHANCED MEMORY PERFORMANCE IN A DATA PROCESSING SYSTEM
5737760	06-Oct-15	MICROCONTROLLER WITH SECURITY LOGIC CIRCUIT WHICH PREVENTS READING OF INTERNAL MEMORY BY EXTERNAL PROGRAM
5737768	10-Aug-14	METHOD AND SYSTEM FOR STORING DATA BLOCKS IN A MEMORY DEVICE
5738269	19-Apr-16	SOLDER BUMP APPARATUS, ELECTRONIC COMPONENT AND METHOD FOR FORMING A SOLDER BUMP
5739557	14-Apr-15	REFRACTORY GATE HETEROSTRUCTURE FIELD EFFECT TRANSISTOR
5740083	01-Apr-16	DELTA TIME MEASUREMENT CIRCUIT FOR DETERMINING PARAMETER DERIVATIVES OF A ROTATIONAL VELOCITY SENSOR SIGNAL
5740109	23-Aug-16	NON-LINEAR CHARGE PUMP
5740325	02-May-14	COMPUTER UTILIZING NEURAL NETWORK AND METHOD OF USING SAME
5740382	28-Mar-16	METHOD AND APPARATUS FOR ACCESSING A CHIP-SELECTABLE DEVICE IN A DATA PROCESSING SYSTEM
5740417	05-Dec-15	PIPELINED PROCESSOR OPERATING IN DIFFERENT POWER MODE BASED ON BRANCH PREDICTION STATE OF BRANCH HISTORY BIT ENCODED AS TAKEN WEAKLY NOT TAKEN AND STRONGLY NOT TAKEN STATES

5740469	24-Apr-15	APPARATUS FOR DYNAMICALLY READING /WRITING MULTIPLE OBJECT FILE FORMATS THROUGH USE OF OBJECT CODE READER/WRITERS INTERFACING WITH GENERALIZED OBJECT FILE FORMAT INTERFACE AND APPLICATIONS PROGRAMMERS INTERFACE
5741626	15-Apr-16	METHOD FOR FORMING A DIELECTRIC TANTALUM NITRIDE LAYER AS AN ANTI-REFLECTIVE COATING (ARC)
5741724	27-Dec-16	METHOD OF GROWING GALLIUM NITRIDE ON A SPINEL SUBSTRATE
5742007	05-Aug-16	ELECTRONIC DEVICE PACKAGE AND METHOD FOR FORMING THE SAME
5742082	22-Nov-16	A STABLE FET AND METHOD OF FABRICATION
5742100	21-Apr-15	STRUCTURE HAVING FLIP-CHIP CONNECTED
5742210	12-Feb-17	NARROW-BAND OVERCOUPLED DIRECTIONAL COUPLER IN MULTILAYER PACKAGE
5742527	15-Mar-16	FLEXIBLE ASYMMETRICAL DIGITAL SUBSCRIBER LINE (ADSL) RECEIVER, CENTRAL OFFICE USING SAME, AND METHOD THEREFOR
5742621	02-Nov-15	METHOD FOR IMPLEMENTING AN ADD-COMPARE-SELECT BUTTERFLY OPERATION IN A DATA PROCESSING SYSTEM AND INSTRUCTION THEREFOR
5742650	04-Feb-14	POWER REDUCTION METHOD AND APPARATUS FOR PHASE-LOCKED LOOP BASED CLOCKS IN A DATA PROCESSING SYSTEM
5742786	21-Apr-15	METHOD AND APPARATUS FOR STORING VECTOR DATA IN MULTIPLE NON-CONSECUTIVE LOCATIONS IN A DATA PROCESSOR USING A MASK VALUE
5742799	18-Feb-17	METHOD AND APPARATUS FOR SYNCHRONIZING MULTIPLE CLOCKS
5743788	02-Dec-16	PLATEN COATING STRUCTURE FOR CHEMICAL MECHANICAL POLISHING AND METHOD
5744396	22-Jun-15	A SEMICONDUCTOR DEVICE FORMED ON A HIGHLY DOPED N+ SUBSTRATE
5744841	06-Feb-15	SEMICONDUCTOR DEVICE WITH ESD PROTECTION
5745408	09-Sep-16	MULTI-LAYER MAGNETIC MEMORY CELL WITH LOW SWITCHING CURRENT
5745411	07-Dec-15	SEMICONDUCTOR MEMORY DEVICE
5745698	31-Aug-14	SYSTEM AND METHOD FOR COMMUNICATING BETWEEN DEVICES
5747839	30-Sep-16	CHEMICAL SENSING TRENCH FIELD EFFECT TRANSISTOR
5747858	30-Sep-16	ELECTRONIC COMPONENT HAVING AN INTERCONNECT SUBSTRATE ADJACENT TO A SIDE SURFACE OF A DEVICE SUBSTRATE
5748042	26-Jul-16	METHOD FOR CONDITIONING A SIGNAL AND AMPLIFIER CIRCUIT THEREFOR
5748071	12-Nov-16	HIGH SPEED COMPARATOR WITH PROGRAMMABLE REFERENCE
5748161	04-Mar-16	INTEGRATED ELECTRO-OPTICAL PACKAGE WITH INDEPENDENT MENU BAR
5748475	30-May-15	APPARATUS AND METHOD OF ORIENTING ASYMMETRICAL SEMICONDUCTOR DEVICES IN A CIRCUIT
5748490	26-Oct-15	LOW POWER LOGIC MINIMIZATION FOR ELECTRICAL CIRCUITS
5748519	13-Dec-16	A METHOD OF SELECTING A MEMORY CELL IN A MAGNETIC

		RANDOM ACCESS MEMORY DEVICE
5748645	29-May-16	SINGLE CLOCK SCAN DESIGN CIRCUIT AND METHOD THEREFOR
5748878	11-Sep-15	METHOD AND APPARATUS FOR ANALYZING SOFTWARE EXECUTED IN EMBEDDED SYSTEMS
5748949	02-Jul-16	COUNTER HAVING PROGRAMMABLE PERIODS AND METHOD THEREFOR
5749090	05-May-15	CACHE TAG RAM HAVING SEPARATE VALID BIT ARRAY WITH MULTIPLE STEP INVALIDATION AND METHOD THEREFOR
5749614	30-Nov-15	VACUUM PICKUP TOOL FOR PLACING BALLS IN A CUSTOMIZED PATTERN
5750419	24-Feb-17	PROCESS FOR FORMING A SEMICONDUCTOR DEVICE HAVING A FERROELECTRIC CAPACITOR
5750440	20-Nov-15	APPARATUS AND METHOD FOR DYNAMICALLY MIXING SLURRY FOR CHEMICAL MECHANICAL POLISHING
5751128	12-Nov-16	METHOD AND APPARATUS FOR ELECTRONICALLY COMMUTING AN ELECTRIC MOTOR
5751159	05-Sep-15	SEMICONDUCTOR ARRAY AND SWITCHES FORMED ON A COMMON SUBSTRATE FOR ARRAY TESTING PURPOSES
5751166	04-Jun-16	INPUT BUFFER CIRCUIT AND METHOD
5751201	19-Jun-16	RESONATOR WITH METAL LAYER DEVICE OF CONNECTION AND SEMICONDUCTOR DEVICE IN SUBSTRATE
5751552	30-May-15	SEMICONDUCTOR DEVICE BALANCING THERMAL EXPANSION COEFFICIENT MISMATCH
5751555	19-Aug-16	ELECTRONIC COMPONENT HAVING REDUCED CAPACITANCE
5751593	10-Apr-16	ACCURATE DELAY PREDICTION BASED ON MULTI-MODEL ANALYSIS
5751741	20-Nov-16	RATE-ADAPTED COMMUNICATION SYSTEM AND METHOD FOR EFFICIENT BUFFER UTILIZATION THEREOF
5751938	17-Dec-16	PERIPHERAL MODULE AND MICROPROCESSOR SYSTEM
5751978	13-Nov-15	MULTI-PURPOSE PERIPHERAL BUS DRIVER APPARATUS AND METHOD
5752012	04-Mar-16	COMPUTATIONAL ARRAY WITH SELF-TIMED COMPUTATIONAL ELEMENT AND METHOD OF SELF-TIMED CALCULATION
5752074	12-May-15	DATA PROCESSING SYSTEM AND METHOD THEREOF
5752077	15-May-15	DATA PROCESSING SYSTEM HAVING A MULTI-FUNCTION INPUT/OUTPUT PORT WITH INDIVIDUAL PULL-UP AND PULL-DOWN CONTROL
5752267	27-Sep-15	A DATA PROCESSING SYSTEM FOR ACCESSING AN EXTERNAL DEVICE DURING A BURST MODE OF OPERATION AND METHOD THEREFOR
5753560	31-Oct-16	METHOD FOR FABRICATING A SEMICONDUCTOR DEVICE USING LATERAL GETTERING
5753904	29-Dec-15	TOOL FOR DETECTING MISSING BALLS USING A PHOTODETECTOR
5753929	28-Aug-16	MULTI-DIRECTIONAL OPTOCOUPLER AND METHOD OF MANUFACTURE
5754010	21-May-17	MEMORY CIRCUIT AND METHOD FOR SENSING DATA
5754454	03-Mar-17	METHOD FOR DETERMINING FUNCTIONAL EQUIVALENCE BETWEEN DESIGN MODELS
5754482	23-Aug-16	MEMORY USING UNDECODED PRECHARGE FOR HIGH SPEED DATA SENSING

5754805	19-May-15	INSTRUCTION IN A DATA PROCESSING SYSTEM UTILIZING EXTENSION BITS AND METHOD THEREFOR
5754839	28-Aug-15	APPARATUS AND METHOD FOR IMPLEMENTING WATCHPOINTS AND BREAKPOINTS IN A DATA PROCESSING SYSTEM
5754861	16-Aug-15	DYNAMIC PROGRAM INPUT/OUTPUT DETERMINATION
5754879	23-Sep-16	INTEGRATED CIRCUIT FOR EXTERNAL BUS INTERFACE HAVING PROGRAMMABLE MODE SELECT BY SELECTIVE LY BONDING ONE OF THE BOND PADS TO A RESET TERMINAL VIA A CONDUCTIVE WIRE
5756380	02-Nov-15	METHOD FOR MAKING A MOISTURE RESISTANT SEMICONDUCTOR DEVICE HAVING AN ORGANIC SUBSTRATE
5756885	04-Apr-16	METHOD FOR DETERMINING THE CLEANLINESS OF A SURFACE
5757208	01-May-16	PROGRAMMABLE ARRAY AND METHOD FOR ROUTING POWER BUSES THEREIN
5757236	01-Jul-16	AMPLIFIER BIAS CIRCUIT AND METHOD
5757237	28-Aug-16	METHOD FOR DYNAMICALLY BIASING AN AMPLIFIER AND CIRCUIT THEREOF
5757303	16-May-16	MULTI-BIT A/D CONVERTER HAVING REDUCED CIRCUITRY
5757695	05-Feb-17	MRAM WITH ALIGNED MAGNETIC VECTORS
5757875	01-Apr-16	METHOD OF TOOTH DETECTION
5758107	26-May-15	SYSTEM FOR OFFLOADING EXTERNAL BUS BY COUPLING PERIPHERAL DEVICE TO DATA PROCESSOR THROUGH INTERFACE LOGIC THAT EMULATE THE CHARACTERISTICS OF THE EXTERNAL BUS
5758275	29-Sep-15	METHOD AND APPARATUS FOR SCHEDULING ADAPTATION FOR A NOTCH FILTER
5759910	23-Dec-16	PROCESS FOR FABRICATING A SOLDER BUMP FOR A FLIP CHIP INTEGRATED CIRCUIT
5760459	22-Dec-14	HIGH PERFORMANCE HIGH VOLTAGE NON-EPI BIPOLAR TRANSISTOR
5760489	04-Oct-16	METHOD FOR TRANSMITTING SIGNALS BETWEEN A MICROPROCESSOR AND AN INTERFACE CIRCUIT
5760626	01-Apr-16	BIGMOS LATCH CIRCUIT WITH NO PROPAGATION DELAY AND METHOD OF USE
5760648	12-Aug-16	ELECTRONIC CIRCUIT FOR CONVERTING A DIFFERENTIAL SIGNAL INTO A SINGLE-ENDED SIGNAL WITH COMMON MODE VOLTAGE REJECTION BY RESISTOR NETWORK
5760728	03-Feb-17	INPUT STATE FOR AN ANALOG-TO-DIGITAL CONVERTER AND METHOD OF OPERATION THEREOF
5761106	24-Jun-16	MULTIPLIER CIRCUIT
5761215	03-Jun-17	SCAN BASED PATH DELAY TESTING OF INTEGRATED CIRCUITS CONTAINING EMBEDDED MEMORY ELEMENTS
5761489	02-Jun-15	METHOD AND APPARATUS FOR SCAN TESTING WITH EXTENDED TEST VECTOR STORAGE IN A MULTI-PURPOSE MEMORY SYSTEM
5761491	15-Apr-16	DATA PROCESSING SYSTEM AND METHOD FOR STORING AND RESTORING A STACK POINTER
5761690	21-Jul-14	ADDRESS GENERATION APPARATUS AND METHOD USING A PERIPHERAL ADDRESS GENERATION UNIT AND FAST INTERRUPTS
5761700	02-Jun-15	ROM MAPPING AND INVERSION APPARATUS AND METHOD

5761723	04-Feb-14	DATA PROCESSOR WITH BRANCH PRE D ICTION AND METHOD OF OPERATIO N
5762259	13-Jul-15	METHOD FOR FORMING BUMPS ON A SUBSTRATE
5763862	24-Jun-16	DUAL CARD SMART CARD READER WI TH VISUAL IMAGE DISPLAY
5764024	07-Apr-17	PULSE WIDTH MODULATOR (PWM) SY STEM WITH LOW COST DEAD TIME D ISTORTION CORRECTION
5764577	07-Apr-17	FUSELESS MEMORY REPAIR SYSTEM AND METHOD OF OPERATION
5764940	27-Nov-16	PROCESSOR AND METHOD FOR EXECU TING A BRANCH INSTRUCTION AND AN ASSOCIATED TARGET INSTRUCTI ON UTILIZING A SINGLE INSTRUCT
5765017	13-Jan-17	METHOD AND SYSTEM IN A DATA PR OCESSING SYSTEM FOR EFFICIENT MANAGEMENT OF AN INDICATION OF A STATUS OF EACH OF MULTIPLE
5765190	12-Apr-16	IMPROVED CACHE MEMORY IN A DAT A PROCESSING SYSTEM
5765208	29-Sep-15	METHOD OF SPECULATIVELY EXECUT ING STORE INSTRUCTIONS PRIOR T O PERFORMING SNOOP OPERATIONS
5765216	21-Jan-14	DATA PROCESSOR WITH AN EFFICIE NT BIT MOVE CAPABILITY AND MET HOD THEREFOR
5765217	01-Mar-15	METHOD AND APPARATUS TO PERFOR M BUS REFLECTION OPERATION USI NG A DATA PROCESSOR
5767662	21-Jun-16	AMPLIFIER HAVING SINGLE-ENDED INPUT AND DIFFERENTIAL OUTPUT AND METHOD FOR AMPLIFYING A SI GNAL
5768181	07-Apr-17	MAGNETIC DEVICE HAVING MULTI-L AYER WITH INSULATING AND CONDU CTIVE LAYERS
5768183	25-Sep-16	MULTI-LAYER MAGNETIC MEMORY CE LLS WITH IMPROVED SWITCHING CH ARACTERISTICS
5769699	23-Jun-15	POLISHING PAD FOR CHEMICAL-MEC HANICAL POLISHING OF A SEMICON DUCTOR SUBSTRATE
5769941	01-May-16	METHOD OF FORMING SEMICONDUCTO R MATERIAL
5770849	23-Aug-16	SMART CARD DEVICE WITH PAGER A ND VISUAL IMAGE DISPLAY
5770965	30-Sep-16	CIRCUIT AND METHOD OF COMPENSA TING FOR NON-LINEARITIES IN A SENSOR SIGNAL
5771247	03-Oct-14	LOW LATENCY ERROR REPORTING FO R HIGH PERFORMANCE BUS
5771391	28-Aug-15	COMPUTER PROCESSOR HAVING A PIPELINED ARCHITECTURE AND METHOD OF USING SAME
5773083	02-Aug-16	METHOD FOR COATING A SUBSTRATE WITH A COATING SOLUTION
5773314	25-Apr-17	PLUG PROTECTION PROCESS FOR US E IN THE MANUFACTURE OF EMBEDD ED DYNAMIC RANDOM ACCESS MEMOR Y (DRAM) CELLS
5773326	19-Sep-16	METHOD OF MAKING AN SOI INTEGR ATED CIRCUIT WITH ESD PROTECTI ON
5773359	26-Dec-15	INTERCONNECT SYSTEM AND METHOD OF FABRICATION
5773364	21-Oct-16	METHOD FOR USING AMMONIUM SALT SLURRIES FOR CHEMICAL MECHANI CAL POLISHING (CMP)
5773887	03-Jun-16	HIGH FREQUENCY SEMICONDUCTOR C OMPONENT
5773987	26-Feb-16	METHOD FOR PROBING A SEMICONDU CTOR WAFER USING A MOTOR CONTR OLLED SCRUB PROCESS

5773995	22-Apr-16	DIGITAL MULTIPLEXER CIRCUIT
5774358	01-Apr-16	METHOD AND APPARATUS FOR GENERATING INSTRUCTION/DATA STREAMS EMPLOYED TO VERIFY HARDWARE IMPLEMENTATIONS OF INTEGRATED CIRCUIT DESIGNS
5774703	05-Jan-16	DATA PROCESSING SYSTEM HAVING A REGISTER CONTROLLABLE SPEED
5776798	04-Sep-16	SEMICONDUCTOR PACKAGE AND METHOD THEREFOR
5777361	03-Jun-16	SINGLE GATE NONVOLATILE MEMORY CELL AND METHOD FOR ACCESSING THE SAME
5777516	13-Aug-16	HIGH FREQUENCY AMPLIFIER IN CMOS
5777522	03-Jan-17	ELECTRONIC DEVICE FOR CONTROLLING A REACTANCE VALUE FOR A REACTIVE ELEMENT
5777935	12-Mar-17	MEMORY DEVICE WITH FAST WRITE RECOVERY AND RELATED WRITE RECOVERY METHOD
5778153	03-Jan-14	NEURAL NETWORK UTILIZING LOGARITHMIC FUNCTION AND METHOD OF USING SAME
5778306	08-Nov-16	LOW LOSS HIGH FREQUENCY TRANSMITTING/RECEIVING SWITCHING MODULE
5778432	01-Jul-16	METHOD AND APPARATUS FOR PERFORMING DIFFERENT CACHE REPLACEMENT ALGORITHMS FOR FLUSH AND NON-FLUSH OPERATIONS IN RESPONSE TO A CACHE FLUSH CONTROL BIT REGISTER
5778444	06-May-16	METHOD AND APPARATUS FOR RESET-SENSITIVE AND CONTROLLED REGISTER WRITE ACCESSES IN A DATA PROCESSING SYSTEM WITH USER AND TEST MODES
5780352	23-Oct-15	METHOD OF FORMING AN ISOLATION OXIDE FOR SILICON-ON-INSULATOR TECHNOLOGY
5780878	29-Jul-16	LATERAL GATE VERTICAL DRIFT REGION TRANSISTOR
5781480	29-Jul-17	PIPELINED DUAL PORT INTEGRATED CIRCUIT MEMORY
5781566	24-Jun-16	CYCLIC REDUNDANCY CODER
5781728	15-Mar-16	FLEXIBLE ASYMMETRICAL DIGITAL SUBSCRIBER LINE (ADSL) TRANSMITTER, REMOTE TERMINAL USING SAME, AND METHOD THEREFOR
5781760	16-Nov-15	METHODS OF SIMULATING AN ELECTRONIC CIRCUIT DESIGN AND FORMING AN INTEGRATED CIRCUIT
5781765	03-Nov-15	SYSTEM FOR DATA SYNCHRONIZATION BETWEEN TWO DEVICES USING FOUR TIME DOMAINS
5781916	25-Mar-16	CACHE CONTROL CIRCUITRY AND METHOD THEREFOR
5783475	13-Nov-15	METHOD OF FORMING A SPACER
5784141	04-Oct-16	BI-STABLE NON-PIXELLATED PHASE SPATIAL LIGHT MODULATOR FOR ENHANCED DISPLAY RESOLUTION AND METHOD OF FABRICATION
5784427	24-Jun-16	FEEDBACK AND SHIFT UNIT
5784534	21-Jul-15	CIRCUIT AND METHOD FOR REPRESENTING FUZZY RULE WEIGHTS DURING A FUZZY LOGIC OPERATION
5784606	16-Dec-16	METHOD AND SYSTEM IN A SUPERSCALAR DATA PROCESSING SYSTEM FOR THE EFFICIENT HANDLING OF EXCEPTIONS
5786230	28-Jul-15	METHOD OF FABRICATING MULTI-CHIP PACKAGES
5786263	28-Jul-15	METHOD FOR FORMING A TRENCH ISOLATION STRUCTURE IN AN INTEGRATED CIRCUIT

5786608	26-Feb-17	A SEMICONDUCTOR CHEMICAL SENSOR WITH SPECIFIC HEATER STRUCTURE
5787125	06-May-16	METHOD AND APPARATUS FOR DERIVING IN-PHASE AND QUADRATURE-PHASE BASEBAND SIGNALS FROM A COMMUNICATION SIGNAL
5787407	22-Dec-14	DATA PROCESSING SYSTEM FOR EVALUATING FUZZY LOGIC RULES AND METHOD THEREFOR
5787479	29-Apr-16	METHOD AND SYSTEM FOR PREVENTING INFORMATION CORRUPTION IN A CACHE MEMORY CAUSED BY AN OCCURRENCE OF A BUS ERROR DURING A LINEFILL OPERATION
5789733	20-Sep-16	SMART CARD WITH CONTACTLESS OPTICAL INTERFACE
5789766	20-Mar-17	LED ARRAY WITH STACKED DRIVER CIRCUITS AND METHODS OF MANUFACTURE
5789815	23-Apr-16	THREE DIMENSIONAL SEMICONDUCTOR PACKAGE HAVING FLEXIBLE APPEXNDAGES AND METHOD
5789973	04-Sep-16	RESISTORLESS OPERATIONAL TRANSCONDUCTANCE AMPLIFIER CIRCUIT
5790063	16-Dec-16	ANALOG-TO-DIGITAL CONVERTER IN CMOS WITH MOS CAPACITOR
5790415	10-Apr-16	COMPLEMENTARY NETWORK REDUCTION FOR LOAD MODELING
5790416	18-Sep-15	UPDATING HIERARCHICAL DAG REPRESENTATIONS THROUGH A BOTTOM UP METHOD
5790728	28-Jun-16	OPTICAL COUPLING COMPONENT AND METHOD OF MAKING THE SAME
5792594	28-Mar-16	METALLIZATION AND TERMINATION PROCESS FOR AN INTEGRATED CIRCUIT CHIP
5793986	01-Nov-15	METHOD AND SYSTEM FOR ENHANCED EFFICIENCY OF DATA TRANSFERS FROM MEMORY TO MULTIPLE PROCESSORS IN A DATA PROCESSING SYSTEM
5795493	18-Aug-15	LASER ASSISTED PLASMA CHEMICAL ETCHING APPARATUS AND METHOD
5796256	24-Apr-16	ESO SENSOR AND METHOD OF USE
5796391	24-Oct-16	SCALEABLE REFRESH DISPLAY CONTROLLER
5796682	30-Oct-15	METHOD FOR MEASURING TIME AND STRUCTURE THEREFOR
5796787	21-May-16	RECEIVER AND METHOD THEREOF
5796985	29-Apr-16	METHOD AND APPARATUS FOR INCORPORATING A MILLER COMPENSATION FOR MODELING ELECTRICAL CIRCUITS
5796993	29-Oct-16	METHOD AND APPARATUS FOR SEMICONDUCTOR DEVICE OPTIMIZATION USING ON-CHIP VERIFICATION
5798556	25-Mar-16	SENSOR AND METHOD OF FABRICATION
5798568	26-Aug-16	SEMICONDUCTOR COMPONENT WITH MULTI-LEVEL INTERCONNECT SYSTEM AND METHOD OF MANUFACTURE
5798937	28-Sep-15	METHOD AND APPARATUS FOR FORMING REDUNDANT VIAS BETWEEN CONDUCTIVE LAYERS OF AN INTEGRATED CIRCUIT
5799011	29-Mar-16	CDMA POWER CONTROL CHANNEL ESTIMATION USING DYNAMIC COEFFICIENT SCALING
5799043	02-Jun-17	METHOD AND APPARATUS FOR DECODING A TWO-LEVEL RADIO SIGNAL
5799049	02-Apr-16	PHASE-INDEPENDENT CLOCK CIRCUIT AND METHOD
5799143	26-Aug-16	MULTIPLE CONTEXT SOFTWARE ANALYSIS
5799160	24-Jun-16	CIRCUIT AND METHOD FOR CONTROLLING BUS ARBITRATION

5799172	10-Sep-16	METHOD OF SIMULATING AN INTEGRATED CIRCUIT
5800747	02-Jul-16	METHOD FOR MOLDING USING AN ION IMPLANTED MOLD
5801098	03-Sep-16	METHOD OF DECREASING RESISTIVITY IN AN ELECTRICALLY CONDUCTIVE LAYER
5801108	11-Sep-16	LOW TEMPERATURE COFIREABLE DIELECTRIC PASTE AND METHOD OF FABRICATING SAME
5801523	11-Feb-17	CIRCUIT AND METHOD OF PROVIDING A CONSTANT CURRENT
5801552	30-Apr-17	VOLTAGE DETECTOR CIRCUIT
5801798	23-Dec-16	FAST SPEED LIQUID CRYSTAL PHASE SPATIAL LIGHT MODULATOR FOR ENHANCED DISPLAY RESOLUTION
5801799	23-Dec-16	MULTI-DIRECTIONAL LIQUID CRYSTAL PHASE SPATIAL LIGHT MODULATOR FOR ENHANCED DISPLAY RESOLUTION
5801987	17-Mar-17	AUTOMATIC TRANSITION CHARGE PUMP FOR NON-VOLATILE MEMORIES
5802317	04-Sep-16	ELECTRONIC CIRCUIT HAVING CASCADDED LOGIC BUSES
5802541	28-Feb-16	METHOD AND APPARATUS IN A DATA PROCESSING SYSTEM FOR USING CHIP SELECTS TO PERFORM A MEMORY MANAGEMENT FUNCTION
5802586	01-Sep-15	CACHE MEMORY HAVING A READ-MODIFY-WRITE OPERATION AND SIMULTANEOUS BURST READ AND WRITE OPERATIONS AND A METHOD THEREFOR
5804462	30-Nov-15	METHOD FOR FORMING A MULTIPLE-SENSOR SEMICONDUCTOR CHIP
5804881	22-Nov-15	METHOD AND ASSEMBLY FOR PROVIDING IMPROVED UNDERCHIP ENCAPSULATION
5804958	13-Jun-17	SELF-REFERENCED CONTROL CIRCUIT
5804985	02-Apr-16	PROGRAMMABLE OUTPUT BUFFER AND METHOD FOR PROGRAMMING
5805774	16-Dec-14	CIRCUIT AND METHOD FOR DETERMINING MEMBERSHIP IN A SET DURING A FUZZY LOGIC OPERATION
5805862	16-Nov-15	METHOD OF FORMING AN INTEGRATED CIRCUIT
5805874	08-Sep-15	METHOD AND APPARATUS FOR PERFORMING A VECTOR SKIP INSTRUCTION IN A DATA PROCESSOR
5805877	04-Feb-14	DATA PROCESSOR WITH BRANCH TARGET ADDRESS CACHE AND METHOD OF OPERATION
5805916	27-Nov-16	METHOD AND APPARATUS FOR DYNAMIC ALLOCATION OF REGISTERS FOR INTERMEDIATE FLOATING-POINT RESULTS
5805922	08-Sep-15	QUEUED SERIAL PERIPHERAL INTERFACE HAVING MULTIPLE QUEUES FOR USE IN A DATA PROCESSING SYSTEM
5806365	30-Apr-16	ACCELERATION SENSING DEVICE ON A SUPPORT SUBSTRATE AND METHOD OF OPERATION
5808362	29-Feb-16	INTERCONNECT STRUCTURE AND METHOD OF FORMING
5808873	30-May-17	ELECTRONIC COMPONENT ASSEMBLY HAVING AN ENCAPSULATION MATERIAL AND METHOD OF FORMING THE SAME
5809530	13-Nov-15	METHOD AND APPARATUS FOR EFFICIENTLY PROCESSING MULTIPLE CACHE MISSES
5809532	06-Dec-13	DATA PROCESSOR WITH CACHE AND METHOD OF OPERATION
5811341	09-Dec-16	DIFFERENTIAL AMPLIFIER HAVING UNILATERAL FIELD EFFECT TRANSISTORS AND PROCESS OF FABRICATING
5812027	13-Aug-16	SPIKE INSENSITIVE INTERMEDIATE FREQUENCY AMPLIFIER

5812561	03-Sep-16	SCAN BASED TESTING OF AN INTEGRATED CIRCUIT FOR COMPLIANCE WITH TIMING SPECIFICATIONS
5812595	01-Jul-16	WAVEFORM SHAPING CIRCUIT FOR A MULTIPLEXED INFORMATION BUS TRANSMITTER
5812798	26-Jan-16	DATA PROCESSING SYSTEM FOR ACCESSING AN EXTERNAL DEVICE AND METHOD THEREFOR
5812831	22-Apr-16	METHOD AND APPARATUS FOR PULSE WIDTH MODULATION
5812833	13-Nov-15	TIMER BUS STRUCTURE FOR AN INTEGRATED CIRCUIT
5812868	16-Sep-16	METHOD AND APPARATUS FOR SELECTING A REGISTER FILE IN A DATA PROCESSING SYSTEM
5812871	30-Jun-15	DATA PROCESSING SYSTEM AND A METHOD OF OPTIMIZING AN OPERATION OF THE DATA PROCESSING SYSTEM
5813041	06-Jun-16	METHOD FOR ACCESSING MEMORY BY ACTIVATING A PROGRAMMABLE CHIP SELECT SIGNAL
5814401	04-Feb-17	SELECTIVELY FILLED ADHESIVE FILM CONTAINING A FLUXING AGENT
5814545	02-Oct-15	METHOD OF MANUFACTURE A SEMICONDUCTOR DEVICE HAVING A PHOSPHORUS DOPED PECVD FILM
5814727	29-Sep-15	SEMICONDUCTOR ACCELEROMETER HAVING REDUCED SENSOR PLATE FLEXURE
5814733	12-Sep-16	METHOD OF CHARACTERIZING DYNAMICS OF A WORKPIECE HANDLING SYSTEM
5814893	20-Jan-15	SEMICONDUCTOR DEVICE HAVING A BOND PAD
5815017	03-Mar-17	FORCED OSCILLATOR CIRCUIT AND METHOD
5816478	06-Oct-15	FLUXLESS FLIP-CHIP BOND AND A METHOD FOR MAKING
5818276	04-Mar-16	NON-OVERLAPPING CLOCK GENERATOR CIRCUIT AND METHOD THEREFOR
5818316	15-Jul-17	NONVOLATILE PROGRAMMABLE SWITCH
5818404	04-Mar-16	INTEGRATED ELECTRO-OPTICAL PACKAGE
5818634	05-Aug-16	DUAL MODE OPTICAL MAGNIFIER SYSTEM
5819305	23-Aug-16	METHOD AND APPARATUS FOR CONFIGURING OPERATING MODES IN A MEMORY
5821160	06-Jun-16	METHOD FOR FORMING A LASER REPAIRABLE FUSE AREA OF A MEMORY CELL USING AN ETCH STOP LAYER
5821168	16-Jul-17	PROCESS FOR FORMING A SEMICONDUCTOR DEVICE
5821170	30-Sep-16	METHOD OF REMOVING ETCHING AN INSULATING MATERIAL
5821456	29-Apr-16	MICROELECTRONIC ASSEMBLY INCLUDING A DECOMPOSABLE ENCAPSULATION, AND METHOD FOR FORMING AND REWORKING SAME
5821911	13-Oct-15	MINIATURE VIRTUAL IMAGE COLOR DISPLAY
5822244	24-Sep-17	METHOD AND APPARATUS FOR SUSPENDING A PROGRAM/ERASE OPERATION IN A FLASH MEMORY
5822374	07-Jun-16	METHOD FOR FINE GAINS ADJUSTMENT IN AN ADSL COMMUNICATIONS SYSTEM
5822764	04-Mar-16	METHOD AND CIRCUIT FOR EFFICIENTLY REPLACING INVALID LOCKED PORTIONS OF A CACHE WITH VALID DATA
5824565	29-Feb-16	METHOD OF FABRICATING A SENSOR
5824579	15-Apr-16	METHOD OF FORMING SHARED CONTACT STRUCTURE
5824584	16-Jun-17	METHOD OF MAKING AND ACCESSING SPLIT GATE MEMORY DEVICE

5824601	30-Jun-17	CARBOXYLIC ACID ETCHING SOLUTION AND METHOD
5825091	25-Mar-17	SENSOR ASSEMBLY MOUNTED TO A LEADFRAME WITH ADHESIVE DEPOSITS AT SEPARATE LOCATIONS
5825093	31-Mar-17	ATTACHMENT SYSTEM AND METHOD THEREFOR
5825629	31-Aug-14	FINE PITCH SOLDER DEPOSITS ON PRINTED CIRCUIT BOARD PROCESS AND PRODUCT
5825640	30-Jun-17	CHARGE PUMP CIRCUIT AND METHOD
5825644	04-Mar-16	METHOD FOR ENCODING A STATE MACHINE
5825768	30-Sep-16	AN INTERFACE FOR AN ASYMMETRIC DIGITAL SUBSCRIBER LINE TRANSCEIVER
5825819	23-Apr-16	ASYMMETRICAL DIGITAL SUBSCRIBER LINE (ADSL) LINE DRIVER CIRCUIT
5825826	30-Sep-16	METHOD AND APPARATUS FOR FREQUENCY DOMAIN RIPPLE COMPENSATION FOR A COMMUNICATIONS TRANSMITTER
5826047	26-Aug-16	METHOD AND APPARATUS FOR EXTERNAL VIEWING OF AN INTERNAL BUS
5826058	20-Oct-15	METHOD AND APPARATUS FOR PROVIDING AN EXTERNAL INDICATION OF INTERNAL CYCLES IN A DATA PROCESSING SYSTEM
5826100	02-Mar-15	DIGITAL SIGNAL PROCESSOR FOR EXECUTING MULTIPLE INSTRUCTION WORDS
5827625	18-Aug-17	METHODS OF DESIGNING AND FORMING A RETICLE AND FORMING A SEMICONDUCTOR DEVICE THEREWITH
5828264	07-Dec-15	TWO-STAGE OPERATIONAL AMPLIFIER CIRCUIT
5828607	21-May-17	MEMORY PROGRAMMING CIRCUIT AND METHOD
5828612	27-Oct-17	METHOD AND CIRCUIT FOR CONTROLLING A PRECHARGE CYCLE OF A MEMORY DEVICE
5828827	25-Sep-15	DATA PROCESSING SYSTEM FOR PERFORMING A TEST FUNCTION AND METHOD THEREFOR
5828893	24-Dec-12	SYSTEM AND METHOD OF COMMUNICATING BETWEEN TRUSTED AND UNTRUSTED COMPUTER SYSTEMS
5829668	30-Aug-16	METHOD FOR FORMING SOLDER BUMPS ON BOND PADS
5829879	23-Dec-16	TEMPERATURE SENSOR
5830802	31-Aug-15	PROCESS FOR REDUCING HALOGEN CONCENTRATION IN A MATERIAL LAYER DURING SEMICONDUCTOR DEVICE FABRICATION
5831699	29-Apr-16	DISPLAY WITH INACTIVE PORTIONS AND ACTIVE PORTIONS AND HAVING DRIVERS IN THE INACTIVE PORTIONS
5831832	11-Aug-17	MOLDED PLASTIC BALL GRID ARRAY PACKAGE
5831920	14-Oct-17	GRM DEVICE HAVING A SENSE AMPLIFIER PROTECTED BY A CIRCUIT FOR DISSIPATING ELECTRIC CHARGES
5832370	26-Sep-16	CURRENT MODE TRANSCEIVER CIRCUIT AND METHOD
5834926	11-Aug-17	BANDGAP REFERENCE CIRCUIT
5835536	10-Nov-15	METHOD AND APPARATUS FOR REDUCING PEAK-TO-AVERAGE REQUIREMENTS IN MULTI-TONE COMMUNICATION CIRCUITS
5835746	21-Apr-17	METHOD AND APPARATUS FOR FETCHING AND ISSUING DUAL-WORD OR MULTIPLE INSTRUCTIONS IN A DATA PROCESSING SYSTEM
5835999	17-May-15	LOW POWER REGENERATIVE FEEDBACK DEVICE AND METHOD

5838416	13-Feb-17	DEVICE AND METHOD FOR ENHANCING THE VIEWING ANGLE OF A DISPLAY
5838608	16-Jun-17	MULTI-LAYER MAGNETIC RANDOM ACCESS MEMORY AND METHOD FOR FABRICATING THEREOF
5844315	22-Mar-16	LOW-PROFILE MICROELECTRIC PACKAGE, AND METHOD FOR FORMING SAME
5844319	28-Feb-17	MICROELECTRONIC ASSEMBLY WITH COLLAR SURROUNDING INTEGRATED CIRCUIT COMPONENT ON A SUBSTRATE
5845098	24-Jun-16	ADDRESS LINES LOAD REDUCTION
5846847	07-Nov-16	Ferroelectric semiconductor device, ferroelectric semiconductor substrate and methods of manufacture
5848025	30-Jun-17	METHOD AND APPARATUS FOR CONTROLLING A MEMORY DEVICE IN A PAGE MODE
5848289	08-Dec-15	EXTENSIBLE CENTRAL PROCESSING UNIT
5848466	19-Nov-16	METHOD FOR FORMING A MICROELECTRONIC ASSEMBLY AND ASSEMBLY FORMED THEREBY
5849440	02-Jul-16	PROCESS FOR PRODUCING AND INSPECTING A LITHOGRAPHIC RETICLE AND FABRICATING SEMICONDUCTOR DEVICES USING SAME
5851844	07-Nov-16	Ferroelectric semiconductor device and method of manufacture
5851927	29-Aug-17	METHOD OF FORMING A SEMICONDUCTOR DEVICE BY DUV RESIST PATTERNING
5852316	22-Dec-15	COMPLEMENTARY HETEROJUNCTION AMPLIFIER
5852574	24-Dec-17	HIGH DENSITY MAGNETORESISTIVE RANDOM ACCESS MEMORY DEVICE AND OPERATING METHOD THEREOF
5852633	07-Jun-16	METHOD FOR ALLOCATING DATA IN A DATA COMMUNICATION SYSTEM
5854920	07-Apr-17	METHOD AND APPARATUS FOR MANIPULATING A CARRY/BORROW BIT TO NUMERICALLY ADJUST AND IMMEDIATE VALUE OF AN INSTRUCTION DURING INSTRUCTION EXECUTION
5854944	09-May-16	METHOD AND APPARATUS FOR DETERMINING WAIT STATES ON A PER CYCLE BASIS IN A DATA PROCESSING SYSTEM
5856068	02-May-17	METHOD FOR FABRICATING A PRINTED CIRCUIT BOARD UNDER SUPERATMOSPHERIC PRESSURE
5856684	12-Sep-16	HIGH POWER HFET WITH IMPROVED CHANNEL INTERFACES
5856912	28-Feb-17	MICROELECTRONIC ASSEMBLY FOR CONNECTION TO AN EMBEDDED ELECTRICAL ELEMENT, AND METHOD FOR FORMING SAME
5859541	12-Jan-16	DATA PROCESSOR HAVING AN OUTPUT TERMINAL WITH SELECTABLE OUTPUT IMPEDANCES
5859649	12-Jan-16	DATA PROCESSING HAVING DISPLAY CONTROLLER WITH BURSTING DIRECT MEMORY ACCESS
5859849	06-May-17	MODULAR SWITCH ELEMENT FOR SHARED MEMORY SWITCH FABRIC
5859890	26-Feb-17	DUAL MODULUS PRESCALER
5860129	27-Sep-15	DATA PROCESSING SYSTEM FOR WRITING AN EXTERNAL DEVICE AND METHOD THEREFOR
5860585	31-May-16	SUBSTRATE FOR TRANSFERRING BUMPS AND METHOD OF USE
5861328	07-Oct-16	METHOD OF FABRICATING GMR DEVICES

5861347	03-Jul-17	METHOD FOR FORMING A HIGH VOLT AGE GATE DIELECTRIC FOR USE IN INTEGRATED CIRCUITS
5863838	22-Jul-16	SLURRY FOR CHEMICALLY-MECHANIC ALLY POLISHING A LAYER AND MET HOD OF USE
5867032	30-Nov-15	PROCESS FOR TESTING A SEMICONDUCTOR DEVICE
5867053	21-Mar-17	MULTIPLEXED OUTPUT CIRCUIT AND METHOD OF OPERATION THEREOF
5867063	05-Dec-16	GAIN DISTRIBUTION CIRCUIT
5867405	01-Mar-16	FERROELECTRIC SIMULATOR, FERRO ELECTRIC METHOD OF MANUFACTURE , AND METHOD OF SIMULATION
5867719	10-Jun-16	METHOD AND APPARATUS FOR TESTI NG ON-CHIP MEMORY ON A MICROCONTROLLER
5869899	18-Mar-16	HIGH DENSITY INTERCONNECT SUBSTRATE AND METHOD OF MANUFACTURING SAME
5870577	27-Nov-16	MECHANISM FOR DISPATCHING 2 INSTRUCTIONS TO THE SAME EXECUTION UNIT IN A SINGLE CYCLE WITH MINIMAL HARDWARE OVERHEAD
5870670	23-Sep-16	INTEGRATED IMAGE REJECT MIXER
5872374	29-Mar-16	VERTICAL SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING THE SAME
5872385	19-May-15	CONDUCTIVE INTERCONNECT STRUCTURE AND METHOD OF FORMATION
5872458	08-Jul-16	METHOD FOR ELECTRICALLY CONTACTING SEMICONDUCTOR DEVICES IN TRAYS AND TEST CONTACTOR USEFUL THEREFOR
5872460	04-Oct-16	FAST ACTING FET TEST CIRCUIT WITH CURRENT DETECTION FOR SIR DIAGNOSTICS
5872819	19-Feb-17	METHOD AND APPARATUS FOR FACILITATING SYMBOL TIMING ACQUISITION IN A DATA COMMUNICATION RECEIVER
5872940	01-Apr-16	PROGRAMMABLE READ/WRITE ACCESS SIGNAL AND METHOD THEREFOR
5872948	15-Mar-16	PROCESSOR AND METHOD FOR OUT-OF-ORDER EXECUTION OF INSTRUCTIONS BASED UPON AN INSTRUCTION PARAMETER
5872949	13-Nov-16	APPARATUS AND METHOD FOR MANAGING DATA FLOW DEPENDENCIES ARISING FROM OUT-OF-ORDER EXECUTION, BY AN EXECUTION UNIT, OF AN INSTRUCTION SERIES INPUT FROM AN INSTRUCTION SOURCE
5872992	24-Aug-15	SYSTEM AND METHOD FOR AVOIDING BUS CONTENTION ON A MULTIPLEXED BUS BY PROVIDING A TIME PERIOD SUBSEQUENT TO A READ OPERATION
5873123	25-Jun-16	PROCESSOR AND METHOD FOR TRANS LATING A NONPHYSICAL ADDRESS INTO A PHYSICAL ADDRESS UTILIZING A SELECTIVELY NON-SEQUENTIAL SEARCH OF PAGE TABLE ENTRIES
5874860	04-Dec-16	BUILT-IN GAAS FET NEGATIVE BIAS AND CONTROL CIRCUIT
5875143	15-Jul-16	DYNAMIC MEMORY DEVICE WITH REFRESH CIRCUIT AND REFRESH METHOD
5875482	06-Jun-16	METHOD AND APPARATUS FOR PROGRAMMABLE CHIP SELECT NEGATION IN A DATA PROCESSING SYSTEM
5875897	31-Mar-17	PACKAGING APPARATUS AND METHOD
5877047	15-Aug-17	LATERAL GATE, VERTICAL DRIFT REGION TRANSLATION
5877654	02-Dec-16	CLASS A AMPLIFIER WITH A DIGITALLY PROGRAMMABLE

		MILLER COMPENSATION NETWORK
5879630	13-Feb-17	SEMICONDUCTOR CHEMICAL SENSOR DEVICE AND METHOD OF FORMING A THERMOCOUPLE FOR A SEMICONDUCTOR CHEMICAL SENSOR DEVICE
5879999	30-Sep-16	INSULATED GATE SEMICONDUCTOR DEVICE HAVING A SPACER EXTENSION AND METHOD OF MANUFACTURE
5880018	07-Oct-16	A METHOD FOR MANUFACTURING A LOW DIELECTRIC CONSTANT INTERLEVEL INTEGRATED CIRCUIT STRUCTURE
5880029	27-Dec-16	METHOD OF PASSIVATING SEMICONDUCTOR DEVICES AND THE PASSIVATED DEVICES
5880041	09-Mar-16	METHOD FOR FORMING A DIELECTRIC LAYER USING HIGH PRESSURE
5880687	25-Feb-17	CASCADED INTEGRATOR-COMB INTERPOLATION FILTER
5882034	02-May-16	AUTOMOBILE AIRBAG SYSTEM
5882243	24-Apr-17	METHOD FOR POLISHING A SEMICONDUCTOR WAFER USING DYNAMIC CONTROL
5882961	11-Sep-15	METHOD OF MANUFACTURING A SEMICONDUCTOR DEVICE WITH REDUCED CHARGE TRAPPING
5883012	21-Dec-15	METHOD OF ETCHING A TRENCH INTO A SEMICONDUCTOR SUBSTRATE
5883305	29-Aug-17	TIRE PRESSURE MONITORING SYSTEM
5883404	16-Mar-16	COMPLEMENTARY HETEROJUNCTION SEMICONDUCTOR DEVICE
5883907	01-May-17	ASYMMETRICAL DIGITAL SUBSCRIBER LINE (ADSL) BLOCK ENCODER CIRCUIT AND METHOD OF OPERATION
5885856	21-Aug-16	INTEGRATED CIRCUIT HAVING A DUMMYPY STRUCTURE AND METHOD OF MAKING THE SAME
5885860	30-Jun-15	SILICON CARBIDE TRANSISTOR AND METHOD
5885870	03-Nov-15	METHOD FOR FORMING A SEMICONDUCTOR DEVICE HAVING A NITRIDED OXIDE DIELECTRIC LAYER
5886362	23-Mar-16	METHOD OF REFLOWING SOLDER BUMPS AFTER PROBE TEST
5886374	05-Jan-18	OPTICALLY SENSITIVE DEVICE AND METHOD
5886396	23-Mar-16	LEADFRAME ASSEMBLY FOR CONDUCTING THERMAL ENERGY FROM A SEMICONDUCTOR DIE DISPOSED IN A PACKAGE
5886547	16-Dec-16	CIRCUIT AND METHOD OF CONTROLLING MIXER LINEARITY
5886556	27-Jan-17	LOW POWER SCHMITT TRIGGER
5886562	26-Dec-16	METHOD AND APPARATUS FOR SYNCHRONIZING A PLURALITY OF OUTPUT CLOCK SIGNALS GENERATED FROM A CLOCK INPUT SIGNAL
5886926	20-Jan-18	CIRCUIT AND METHOD OF MEASURING THE NEGATIVE THRESHOLD VOLTAGE OF A NON-VOLATILE MEMORY CELL
5886928	21-Feb-16	NON-VOLATILE MEMORY CELL AND METHOD OF PROGRAMMING
5886998	28-Apr-15	METHOD FOR INTERLEAVE/DE-INTERLEAVE ADDRESSING IN DATA COMMUNICATIONS CIRCUITS
5887179	11-Jun-16	SYSTEM POWER SAVING MEANS AND METHOD
5888412	04-Mar-16	METHOD FOR MAKING A SCULPTURED DIAPHRAGM
5888588	31-Mar-17	PROCESS FOR FORMING A SEMICONDUCTOR DEVICE
5888901	05-Aug-16	MULTILEVEL INTERCONNECTION AND METHOD FOR MAKING
5889211	30-Mar-16	MEDIA COMPATIBLE MICROSENSOR STRUCTURE AND

		METHODS OF MANUFACTURING AND USING THE SAME
5889482	06-Oct-17	ANALOG-TO-DIGITAL CONVERTER USING DITHER AND METHOD FOR CONVERTING ANALOG SIGNALS TO DIGITAL SIGNALS
5889763	04-Jun-16	TRANSFER RATE CONTROLLER AND METHOD OF OPERATION
5889788	03-Feb-17	WRAPPER CELL ARCHITECTURE FOR PATH DELAY TESTING OF EMBEDDED CORE MICROPROCESSORS AND METHOD OF OPERATION
5889872	02-Jul-16	CAPACITIVE MICROPHONE AND METHOD THEREFOR
5889948	16-Dec-14	APPARATUS AND METHOD FOR INSERTING AN ADDRESS IN A DATA STREAM THROUGH A FIFO BUFFER
5889973	30-Mar-16	METHOD AND APPARATUS FOR SELECTIVELY CONTROLLING INTERRUPT LATENCY IN A DATA PROCESSING SYSTEM
5890191	10-May-16	METHOD AND APPARATUS FOR PROVIDING ERASING AND PROGRAMMING PROTECTION FOR ELECTRICALLY ERASABLE PROGRAMMABLE READ ONLY MEMORY
5890196	28-Mar-16	METHOD AND APPARATUS FOR PERFORMING PAGE MODE ACCESSES
5890221	30-Mar-16	METHOD AND SYSTEM FOR OFFSET MISS SEQUENCE HANDLING IN A DATA CACHE ARRAY HAVING MULTIPLE CONTENT ADDRESSABLE FIELD PER CACHE LINE UTILIZING AN MRU BIT
5890799	18-May-15	METHOD FOR REDUCING POWER CONSUMPTION IN A PORTABLE ELECTRONIC DEVICE WITH A LIQUID CRYSTAL DISPLAY SCREEN
5891606	07-Oct-16	HIGH-DENSITY CIRCUIT STRUCTURE WITH INTERLAYER ELECTRICAL CONNECTIONS AND PROCESS THEREFOR
5891769	07-Apr-17	METHOD FOR FORMING A SEMICONDUCTOR DEVICE HAVING A HETEROEPI TAXIAL LAYER
5891795	18-Mar-16	HIGH DENSITY INTERCONNECT SUBSTRATE AND METHOD OF MANUFACTURING SAME
5892252	30-Sep-16	CHEMICAL SENSING TRENCH FIELD EFFECT TRANSISTOR
5892379	02-Jun-17	TRANSISTOR PROTECTION CIRCUIT AND METHOD
5892380	04-Aug-17	METHOD FOR SHAPING A PULSE WIDTH AND CIRCUIT THEREFOR
5892381	03-Jun-17	FAST START-UP CIRCUIT
5892661	31-Oct-16	SMARTCARD AND METHOD OF MAKING
5892682	17-Jun-16	METHOD AND APPARATUS FOR GENERATING A HIERARCHICAL INTERCONNECTION DESCRIPTION OF AN INTEGRATED CIRCUIT DESIGN AND USING THE DESCRIPTION TO EDIT THE INTEGRATED CIRCUIT DESIGN
5892709	09-May-17	SINGLE LEVEL GATE NONVOLATILE MEMORY DEVICE AND METHOD FOR ACCESSING THE SAME
5892755	13-Dec-16	TRANSFER LAYER OF THE ATM TYPE AND METHOD FOR OPERATING A TRANSFER SWITCH
5892777	05-May-17	APPARATUS AND METHOD FOR OBSERVING THE MODE OF A MEMORY DEVICE
5892826	30-Jan-16	DATA PROCESSOR WITH FLEXIBLE DATA ENCRYPTION
5893137	29-Nov-16	APPARATUS AND METHOD FOR IMPLEMENTING A CONTENT ADDRESSABLE MEMORY CIRCUIT WITH TWO STAGE MATCHING
5893142	14-Nov-16	DATA PROCESSING SYSTEM HAVING A CACHE AND METHOD THEREFOR

5893752	22-Dec-17	SEMICONDUCTOR DEVICE AND A PROCESS FOR FORMING THE DEVICE
5894163	02-Apr-16	DEVICE AND METHOD FOR MULTIPLYING CAPACITANCE
5894284	02-Dec-16	COMMON-MODE OUTPUT SENSING CIRCUIT
5894423	26-Dec-16	DATA PROCESSING SYSTEM HAVING AN AUTO-RANGING LOW VOLTAGE DETECTION CIRCUIT
5894562	28-Oct-16	METHOD AND APPARATUS FOR CONTROLLING BUS ARBITRATION IN A DATA PROCESSING SYSTEM
5895229	19-May-17	MICROELECTRONIC PACKAGE INCLUDING A POLYMER ENCAPSULATED DIE, AND METHOD FOR FORMING SAME
5895247	22-Dec-14	METHOD OF FORMING A HIGH PERFORMANCE, HIGH VOLTAGE NON-EPI BIPOLAR TRANSISTOR
5895260	29-Mar-16	METHOD OF FABRICATING SEMICONDUCTOR DEVICES AND THE DEVICES
5895929	22-Apr-16	LOW SUBTHRESHOLD LEAKAGE CURRENT HFET
5895976	31-May-16	MICROELECTRONIC ASSEMBLY INCLUDING POLYMERIC REINFORCEMENT DIE AND METHOD FOR FORMING SAME
5896045	05-May-17	A STATIC PULSED CROSS-COUPLED LEVEL SHIFTER AND METHOD THEREFOR
5896335	23-May-17	METHOD AND APPARATUS FOR REDUCING POWER DISSIPATION IN A PRECHARGE/DISCHARGE MEMORY SYSTEM
5897375	20-Oct-17	CHEMICAL MECHANICAL POLISHING (CMP) SLURRY FOR COPPER AND METHOD OF USE IN INTEGRATED CIRCUIT MANUFACTURE
5898122	02-Jul-16	SQUIB IGNITOR CIRCUIT AND METHOD THEREFOR
5898128	11-Sep-16	ELECTRONIC COMPONENT AND METHOD OF FABRICATING THE SAME
5898213	07-Jul-17	SEMICONDUCTOR PACKAGE BOND POST CONFIGURATION
5898215	13-Dec-16	MICROELECTRONIC ASSEMBLY WITH CONNECTION TO A BURIED ELECTRICAL ELEMENT, AND METHOD FOR FORMING SAME
5898217	05-Jan-18	SEMICONDUCTOR DEVICE INCLUDING A SUBSTRATE HAVING CLUSTERED INTERCONNECTS
5898617	21-May-17	SENSING CIRCUIT AND METHOD
5898619	27-Apr-16	MEMORY CELL HAVING A PLURAL TRANSISTOR TRANSMISSION GATE AND METHOD OF FORMATION
5898633	21-May-17	CIRCUIT AND METHOD OF LIMITING LEAKAGE CURRENT IN A MEMORY CIRCUIT
5898744	07-Oct-16	APPARATUS AND METHOD FOR CLOCK RECOVERY IN A COMMUNICATION SYSTEM
5899745	03-Jul-17	METHOD OF CHEMICAL MECHANICAL POLISHING (CMP) USING AN UNDER PAD WITH DIFFERENT COMPRESSION REGIONS AND POLISHING PAD THEREFOR
5900340	03-Mar-17	ONE-DIMENSIONAL LITHOGRAPHIC PROXIMITY CORRECTION USING DRC SHAPE FUNCTIONS
5900530	31-Jul-15	METHOD FOR TESTING PRESSURE SENSORS
5900763	04-May-16	CIRCUIT AND METHOD OF REDUCING CROSS-TALK IN AN INTEGRATED CIRCUIT
5900776	08-Jul-17	CURRENT SENSE CIRCUIT
5901065	07-Feb-16	APPARATUS AND METHOD FOR AUTOMATICALLY PLACING TIES AND CONNECTION ELEMENTS WITHIN AN INTEGRATED CIRCUIT

5901086	26-Dec-16	PIPELINED FAST-ACCESS FLOATING GATE MEMORY ARCHITECTURE AND METHOD OF OPERATION
5901103	07-Apr-17	INTEGRATED CIRCUIT HAVING STANDBY CONTROL FOR MEMORY AND METHOD THEREOF
5901307	22-Jul-16	PROCESSOR HAVING A SELECTIVELY CONFIGURABLE BRANCH PREDICTION UNIT THAT CAN ACCESS A BRANCH PREDICTION UTILIZING BITS DERIVED FROM A PLURALITY OF SOURCES
5902130	17-Jul-17	THERMAL PROCESSING OF OXIDE COMPOUND SEMICONDUCTOR STRUCTURE
5902690	25-Feb-17	STRAY MAGNETIC SHIELDING FOR A NON-VOLATILE MRAM
5903038	30-Jun-17	SEMICONDUCTOR SENSING DEVICE AND METHOD FOR FABRICATING THE SAME
5903051	03-Apr-18	ELECTRONIC COMPONENT AND METHOD OF MANUFACTURE
5903165	18-Jun-13	CONFIGURABLE LOGIC ARRAY
5903419	29-Sep-17	CIRCUIT FOR ELECTROSTATIC DISCHARGE (ESD) PROTECTION
5903471	03-Mar-17	METHOD FOR OPTIMIZING ELEMENT SIZES IN A SEMICONDUCTOR DEVICE
5903599	04-Nov-16	TRANSCIVER AND METHOD FOR REPORTING STATE TRANSITIONS IN A COMMUNICATION SYSTEM
5903748	18-Aug-17	METHOD AND APPARATUS FOR MANAGING FAILURE OF A SYSTEM CLOCK IN A DATA PROCESSING SYSTEM
5903919	07-Oct-17	METHOD AND APPARATUS FOR SELECTING A REGISTER BANK
5904547	26-Dec-16	APPARATUS FOR Dicing A SEMICONDUCTOR DEVICE SUBSTRATE AND A PROCESSOR THEREFOR
5904552	25-Feb-17	METHOD OF RESISTLESS PATTERNING OF A SUBSTRATE FOR IMPLANTATION
5904553	25-Aug-17	FABRICATION METHOD FOR A GATE QUALITY OXIDE-COMPOUND SEMICONDUCTOR STRUCTURE
5904800	03-Feb-17	SEMICONDUCTOR WAFER PROCESSING CHAMBER FOR REDUCING PARTICLES DEPOSITED ONTO THE SEMICONDUCTOR WAFER
5904955	05-Sep-17	ENCAPSULATION MEANS AND METHOD
5905393	06-Oct-17	UNBUFFERED LATCH RESISTANT TO BACKWRITING AND METHOD OF OPERATION THEREFOR
5905397	01-Jun-15	AMOS SWITCHING CIRCUIT
5905453	04-Aug-17	DITHERED SIGMA DELTA MODULATOR HAVING PROGRAMMABLE FULL SCALE RANGE ADJUSTMENT
5905757	04-Oct-16	A FILTER CO-PROCESSOR
5907589	10-Apr-17	GHZ RANGE FREQUENCY DIVIDER IN CMOS
5907698	24-Feb-17	METHOD AND APPARATUS FOR CHARACTERIZING STATIC AND DYNAMIC OPERATION OF AN ARCHITECTURAL SYSTEM
5907765	24-Jun-16	SEMICONDUCTOR SENSOR DEVICE AND METHOD FOR FORMING A SEMICONDUCTOR SENSOR DEVICE
5907792	25-Aug-17	METHOD OF FORMING A SILICON NITRIDE LAYER
5907865	28-Aug-15	METHOD AND DATA PROCESSING SYSTEM FOR DYNAMICALLY ACCESSING BOTH BIG-ENDIAN AND LITTLE-ENDIAN STORAGE SCHEMES
5908321	06-Jan-14	SEMICONDUCTOR STRUCTURE WITH STABLE PRE-REACTED PARTICLE AND METHOD FOR MAKING
5909463	04-Nov-16	SINGLE-CHIP SOFTWARE CONFIGURABLE TRANSCIVER FOR

		ASYMMETRIC COMMUNICATION SYSTEM
5909558	31-Jul-17	LOW POWER SERIAL ARBITRATION SYSTEM
5910680	08-Jun-16	GERMANIUM SILICATE SPIN ON GLASS SEMICONDUCTOR DEVICE AND METHODS OF SPIN ON GLASS SYNTHESIS AND USE
5910726	15-Aug-17	REFERENCE CIRCUIT AND METHOD
5910994	07-Aug-15	METHOD AND APPARATUS FOR SUPPRESSING ACOUSTIC FEEDBACK IN AN AUDIO SYSTEM
5911059	18-Dec-16	METHOD AND APPARATUS FOR TESTING SOFTWARE
5911151	10-Apr-16	OPTIMIZING BLOCK-SIZED OPERAND MOVEMENT UTILIZING STANDARD INSTRUCTIONS
5912510	29-May-16	BONDING STRUCTURE FOR AN ELECTRONIC DEVICE
5912562	04-Feb-17	QUIESCENT CURRENT MONITOR CIRCUIT FOR WAFER LEVEL INTEGRATED CIRCUIT TESTING
5912819	03-Dec-16	METHOD FOR DESIGNING AN ARCHITECTURAL SYSTEM
5913054	16-Dec-16	METHOD AND SYSTEM FOR EFFICIENTLY PROCESSING A MULTIPLE-REGISTER INSTRUCTION THAT PERMIT MULTIPLE DATA WORDS TO BE WRITTEN IN A SINGLE PROCESSOR CYCLE
5914521	30-Jul-17	SEMICONDUCTOR DEVICE AND METHOD FOR MAKING THE SAME
5915463	23-Mar-16	HEAT DISSIPATION APPARATUS AND METHOD
5916011	26-Dec-16	PROCESS FOR POLISHING A SEMICONDUCTOR DEVICE SUBSTRATE
5917203	29-Jul-16	LATERAL GATE VERTICAL DRIFT REGION TRANSISTOR
5917204	31-Mar-17	INSULATED GATE BIPOLAR TRANSISTOR WITH REDUCED ELECTRIC FIELDS
5917336	29-Sep-17	CIRCUIT FOR ELECTROSTATIC DISCHARGE (ESD) PROTECTION
5917358	09-Dec-17	METHOD AND OUTPUT BUFFER WITH PROGRAMMABLE BIAS TO ACCOMMODATE MULTIPLE SUPPLY VOLTAGES
5917363	21-Jun-16	MULTIPLEXED DRIVER SYSTEM REQUIRING A REDUCED NUMBER OF AMPLIFIER CIRCUITS
5917655	06-Apr-18	METHOD AND APPARATUS FOR GENERATING A STEREOSCOPIC IMAGE
5917749	23-May-17	MRAM CELL REQUIRING LOW SWITCHING FIELD
5917761	06-Nov-17	SYNCHRONOUS MEMORY INTERFACE
5918064	18-Feb-17	DATA PROCESSING SYSTEM HAVING AN INPUT/OUTPUT COPROCESSOR WITH A SEPARATE VISIBILITY BUS
5918112	24-Jul-17	SEMICONDUCTOR COMPONENT AND METHOD OF FABRICATION
5918147	29-Jun-16	PROCESS FOR FORMING A SEMICONDUCTOR DEVICE WITH AN ANTIREFLECTIVE LAYER
5918247	27-Oct-15	METHOD FOR CANCELING PARTIAL LINE FETCH FOR CACHE WHEN NEW DATA IS REQUESTED DURING CURRENT FETCH AND INVALIDATING PORTION OF PREVIOUSLY FETCHED DATA
5918363	20-May-16	METHOD FOR MARKING FUNCTIONAL INTEGRATED CIRCUIT CHIPS WITH UNDERFILL MATERIAL AND INTEGRATED CIRCUIT CHIP FORMED THEREBY
5920093	07-Apr-17	SOI FET HAVING GATE SUB-REGIONS CONFORMING TO T-SHAPE
5920095	30-Jul-17	SHOT CHANNEL FIELD EFFECT SEMICONDUCTOR DEVICE AND

		METHOD OF MAKING
5920102	30-May-17	SEMICONDUCTOR DEVICE HAVING A DECOUPLING CAPACITOR AND METHOD OF MAKING
5920113	25-Jul-17	LEADFRAME STRUCTURE
5920184	05-May-17	LOW RIPPLE VOLTAGE REFERENCE CIRCUIT
5920484	02-Dec-16	METHOD FOR GENERATING A REDUCED ORDER MODEL OF AN ELECTRONIC CIRCUIT
5920487	03-Mar-17	TWO DIMENSIONAL LITHOGRAPHIC PROXIMITY CORRECTION USING DRC SHAPE FUNCTIONS
5920500	23-Aug-16	A MAGNETIC RANDOM ACCESS MEMORY HAVING STACKED MEMORY CELLS AND FABRICATION METHOD THEREFOR
5920690	11-Aug-17	METHOD AND APPARATUS FOR PROVIDING ACCESS PROTECTION IN AN INTEGRATED CIRCUIT
5920790	29-Aug-17	METHOD OF FORMING A SEMICONDUCTOR DEVICE HAVING DUAL INLAID STRUCTURE
5920810	05-May-17	MULTIPLIER AND METHOD FOR MIXING SIGNALS
5920890	14-Nov-16	DISTRIBUTED TAG CACHE MEMORY SYSTEM AND METHOD FOR STORING DATA IN THE SAME
5922055	25-Feb-17	A METHOD FOR DETERMINING A TYPE OF A SERIAL EEPROM AND PLUG AND PLAY CONTROLLER
5923184	23-Dec-16	FERROELECTRIC TRANSISTOR LOGIC FUNCTIONS AND METHOD FOR PROGRAMMING
5923217	27-Jun-17	AMPLIFIER CIRCUIT AND METHOD FOR GENERATING A BIAS VOLTAGE
5923222	02-Apr-17	LOW POWER AMPLIFIER AND AN OSCILLATING CIRCUIT INCORPORATING THE AMPLIFIER
5923615	17-Apr-18	SYNCHRONOUS PIPELINED BURST MEMORY AND METHOD FOR OPERATING SAME
5923658	15-May-17	AN ATM LINE CARD AND METHOD FOR TRANSFERRING CONNECTION MEMORY DATA
5923893	05-Sep-17	METHOD AND APPARATUS FOR INTERFACING A PROCESSOR TO A COPROCESSOR
5924005	18-Feb-17	PROCESS FOR FORMING A SEMICONDUCTOR DEVICE WITH A LOW K DIELECTRIC LAYER AND DEVICE MADE THEREBY
5925908	30-Jul-17	SEMICONDUCTOR DEVICE AND METHOD OF MAKING
5926052	01-Jul-16	VARIABLE PHASE SHIFTER AND METHOD
5926734	05-Aug-17	SEMICONDUCTOR STRUCTURE HAVING A TITANIUM BARRIER LAYER
5927993	27-Jul-16	BACKSIDE PROCESSING METHOD
5928001	08-Sep-17	SURFACE MOUNTABLE FLEXIBLE INTERCONNECT
5928293	29-Apr-17	METHOD FOR GENERATING A CLOCK SIGNAL FOR USE IN A DATA RECEIVER, CLOCK GENERATORS, DATA RECEIVER AND REMOTE CONTROLLED ACCESS SYSTEM FOR VEHICLES
5928371	25-Jul-17	SYSTEM FOR PROGRAMMABLY INTERLEAVING AND DE-INTERLEAVING DATA AND METHOD THEREOF
5928595	01-May-15	METHOD OF MANUFACTURING A SEMICONDUCTOR COMPONENT
5928962	01-Jun-18	PROCESS FOR FORMING A SEMICONDUCTOR DEVICE
5929472	07-Apr-17	SEMICONDUCTOR FLOATING GATE SENSOR DEVICE
5929478	02-Jul-17	SINGLE LEVEL GATE NONVOLATILE MEMORY DEVICE AND METHOD FOR ACCESSING THE SAME

5929494	20-Oct-17	A READ ONLY MEMORY ARRAY AND A METHOD OF MANUFACTURING THE ARRAY
5929518	28-Jul-17	CIRCUIT BOARD AND METHOD
5929650	04-Feb-17	METHOD AND APPARATUS FOR PERFORMING OPERATIVE TESTING ON AN INTEGRATED CIRCUIT
5929659	12-Mar-17	CIRCUIT AND PROCESS FOR SENSING DATA
5929662	04-Nov-17	ANALOG COMPARATOR AND METHOD
5929935	02-Dec-16	METHOD FOR REDUCING FLICKER AND CIRCUIT THEREFOR
5930103	02-Mar-18	CONTROL CIRCUIT FOR AN ELECTROMECHANICAL DEVICE
5930162	30-Sep-18	QUANTUM RANDOM ADDRESS MEMORY WITH POLYMER MIXER AND/OR MEMORY
5930164	26-Feb-18	MAGNETIC MEMORY UNIT HAVING FOUR STATES AND OPERATING METHOD THEREOF
5930586	03-Jul-17	METHOD AND APPARATUS FOR IN-LINE MEASURING BACKSIDE WAFER LEVEL CONTAMINATION OF A SEMICONDUCTOR WAFER
5930598	29-Apr-16	MICROELECTRONIC ASSEMBLY INCLUDING A DECOMPOSABLE ENCAPSULATION, AND METHOD FOR FORMING AND REWORKING SAME
5932924	02-Feb-18	LEADFRAME HAVING CONTINUOUSLY REDUCING WIDTH AND SEMICONDUCTOR DEVICE INCLUDING SUCH A LEADFRAME
5933750	03-Apr-18	METHOD OF FABRICATING A SEMICONDUCTOR DEVICE WITH A THINNED SUBSTRATE
5933758	12-May-17	A METHOD FOR PREVENTING ELECTROPLATING OF COPPER ON AN EXPOSED SURFACE AT THE EDGE EXCLUSION OF A SEMICONDUCTOR WAFER
5935321	01-Aug-17	SINGLE CRYSTAL INGOT AND METHOD AND APPARATUS FOR GROWING THE SAME
5935871	22-Aug-17	PROCESS FOR FORMING A SEMICONDUCTOR DEVICE
5936294	28-May-16	OPTICAL SEMICONDUCTOR COMPONENT AND METHOD OF FABRICATION
5936390	20-Jan-18	CONTROL CIRCUIT
5936454	10-Aug-16	LATERAL BIPOLAR TRANSISTOR OPERATING WITH INDEPENDENT BASE AND GATE BIASING
5936469	04-Aug-17	AMPLIFIER WITH INPUT REFERRED COMMON-MODE ADJUSTMENT
5936837	11-Aug-17	SEMICONDUCTOR COMPONENT HAVING LEADFRAME WITH OFFSET GROUND PLANE
5936871	28-Aug-15	METHOD AND SYSTEM FOR PERFORMING AN L2 NORM OPERATION
5936882	31-Mar-18	MAGNETIC RESISTIVE RANDOM ACCESS MEMORY DEVICE AND METHOD OF MANUFACTURE
5937285	23-May-17	METHOD OF FABRICATING SUBMICRON FETS AND DEVICE
5937324	26-Aug-16	METHOD FOR FORMING A LINE-ON-LINE MULTI-LEVEL METAL INTERCONNECT STRUCTURE FOR USE IN INTEGRATED CIRCUITS
5939753	02-Apr-17	MONOLITHIC INTEGRATED CIRCUIT AND PROCESS FOR FABRICATING THE SAME
5939906	08-Sep-17	CIRCUIT COMPENSATING NONLINEARITIES
5940319	31-Aug-18	MAGNETIC RANDOM ACCESS MEMORY AND FABRICATING METHOD THEREOF
5940683	18-Jan-16	LED DISPLAY PACKAGING WITH SUBSTRATE REMOVAL AND

		METHOD OF FABRICATION
5940779	05-Mar-17	ARCHITECTURAL POWER ESTIMATION METHOD AND APPARATUS
5941939	31-Jan-15	LOGARITHM/INVERSE-LOGARITHM CONVERTER AND METHOD OF USING SAME
5941974	29-Nov-16	SERIAL INTERFACE WITH REGISTER SELECTION WHICH USES CLOCK COUNTING, CHIP SELECT PULSING, AND NO ADDRESS BITS
5942760	03-Nov-17	METHOD OF FORMING A SEMICONDUCTOR DEVICE UTILIZING SCALPEL MASK, AND MASK THEREFOR
5942939	01-Jun-18	AMPLIFIER AND METHOD OF CANCELING DISTORTION BY COMBINING HYPERBOLIC TANGENT AND HYPERBOLIC SINE TRANSFER FUNCTIONS
5943274	02-Feb-18	METHOD AND APPARATUS FOR AMPLIFYING A SIGNAL TO PRODUCE A LATENCYED DIGITAL SIGNAL
5943378	01-Aug-16	DIGITAL SIGNAL CLOCK RECOVERY
5943485	15-Oct-16	METHOD FOR GENERATING A MAPPING FOR AN ELECTRONIC DEVICE
5943574	23-Feb-18	3D MULTILAYER SEMICONDUCTOR CIRCUITS
5943597	15-Jun-18	BUMPED SEMICONDUCTOR DEVICE HAVING A TRENCH FOR STRESS RELIEF
5945346	03-Nov-17	CHEMICAL MECHANICAL PLANARIZATION SYSTEM AND METHOD THEREFOR
5945354	03-Feb-17	METHOD FOR REDUCING PARTICLES DEPOSITED ONTO A SEMICONDUCTOR WAFER DURING PLASMA PROCESSING
5945694	31-Jan-17	METHOD OF FORMING A COMPOUND SEMICONDUCTOR DEVICE HAVING REDUCED TEMPERATURE VIABILITY
5945718	12-Feb-18	SELF-ALIGNED METAL-OXIDE-COMPOUND SEMICONDUCTOR DEVICE AND METHOD OF FABRICATION
5945878	17-Feb-18	SINGLE-ENDED TO DIFFERENTIAL CONVERTER
5945958	14-Jul-17	LOOP ANTENNA
5946177	17-Aug-18	CIRCUIT FOR ELECTROSTATIC DISCHARGE PROTECTION
5946227	20-Jul-18	LOW SWITCHING FIELD MAGNETIC TUNNELING JUNCTION FOR HIGH DENSITY ARRAYS
5946633	13-Dec-16	METHOD AND TERMINAL FOR HIGH BANDWIDTH CELLULAR DATA TRANSMISSION
5948052	31-Jan-15	APPARATUS USING A LOGARITHM BASED PROCESSOR AND AN AUDIO AMPLIFIER PERFORMING THIS IS CIP OF 341N
5948361	27-Aug-16	CHEMICAL SENSOR AND METHOD OF MAKING
5949125	06-Apr-15	SEMICONDUCTOR DEVICE HAVING FIELD ISOLATION WITH A MESA
5949304	16-Oct-17	COUPLING MEANS FOR MULTILAYER CERAMIC RESONATORS
5949706	01-Mar-13	STATIC RANDOM ACCESS MEMORY CELL HAVING A THIN FILM TRANSISTOR (TFT) PASS GATE CONNECTION TO A BIT LINE
5949709	30-Aug-16	ELECTRICALLY PROGRAMMABLE MEMORY METHOD OF PROGRAMMING AND METHOD OF READING
5951628	28-Sep-15	METHOD AND SYSTEM FOR PERFORMING A CONVOLUTION OPERATION
5951678	25-Jul-17	METHOD AND APPARATUS FOR CONTROLLING CONDITIONAL BRANCH EXECUTION IN A DATA PROCESSOR
5951688	05-Feb-16	A LOW POWER DATA PROCESSING SYSTEM FOR INTERFACING WITH AN EXTERNAL DEVICE AND METHOD

		THE REFERENCE
5951893	05-Dec-15	INTEGRATED CIRCUIT PAD STRUCTURE WITH HIGH TEMPERATURE HEATING ELEMENT AND METHOD THEREOF
5952870	29-Dec-17	CIRCUIT WITH HYSTERESIS AND METHOD USING SAME
5952875	09-Sep-17	CIRCUIT WITH HOT ELECTRON PROTECTION AND METHOD
5953248	20-Jul-18	LOW SWITCHING FIELD MAGNETIC TUNNELING JUNCTION FOR HIGH DENSITY ARRAYS
5953251	18-Dec-18	PROGRAMMING METHOD FOR NONVOLATILE MEMORIES
5954813	10-Nov-14	DATA PROCESSOR WITH TRANSPARENT OPERATION DURING A BACKGROUND MODE AND METHOD THEREFOR
5955980	03-Oct-17	CIRCUIT AND METHOD FOR CALIBRATING A DIGITAL TO ANALOG CONVERTER
5956336	27-Sep-16	APPARATUS AND METHOD FOR CONCURRENT SEARCH CONTENT ADDRESSABLE MEMORY CIRCUIT
5956494	21-Mar-16	METHOD, APPARATUS, AND COMPUTER INSTRUCTION FOR ENABLING GAIN CONTROL IN A DIGITAL SIGNAL PROCESSOR
5956568	01-Mar-16	METHOD OF FABRICATING AND CONTROLLING ULTRA-SMALL SEMICONDUCTOR DEVICES
5956578	23-Apr-17	VERTICAL FET WITH SCHOTTKY DIODE AND METHOD OF FABRICATION
5956606	31-Oct-17	METHOD FOR BUMPING AND PACKAGING SEMICONDUCTOR DIE
5957988	03-Feb-17	CONTROL CIRCUIT FOR CONTROLLING POWER SUPPLIED TO AN IGNITER ELEMENT AND IGNITER SYSTEM
5958001	28-Sep-16	OUTPUT PROCESSING CIRCUIT FOR A NEURAL NETWORK AND METHOD OF USING SAME
5958029	20-Oct-17	METHOD AND SYSTEM FOR EFFICIENT MESSAGE VALIDATION
5958508	31-Mar-17	PROCESS FOR FORMING A SEMICONDUCTOR DEVICE
5958635	20-Oct-17	LITHOGRAPHIC PROXIMITY CORRECTION THROUGH SUBSET FEATURE MODIFICATION
5959460	27-Jun-14	HIGH FREQUENCY STRIPLINE BLADE PROBE DEVICE AND METHOD OF PROBING
5959462	03-Sep-16	A TEST STRUCTURE FOR ENABLING BURN-IN TESTING ON AN ENTIRE SEMICONDUCTOR WAFER
5959522	03-Feb-18	INTEGRATED ELECTROMAGNETIC DEVICE AND METHOD
5959880	18-Dec-17	LOW ASPECT RATIO MAGNETORESISTIVE TUNNELING JUNCTION
5960036	04-Nov-16	APPARATUS AND METHOD FOR AUTO-CONFIGURING A COMMUNICATION SYSTEM
5960042	01-Jul-17	METHOD IN A SELECTIVE CALL RECEIVER FOR SYNCHRONIZING TO A MULTI-LEVEL RADIO SIGNAL
5960171	06-Dec-16	DYNAMIC SIGNAL LOOP RESOLUTION IN A COMPILED CYCLE BASED CIRCUIT SIMULATOR
5960270	11-Aug-17	METHOD OF FORMING AN MOS TRANSISTOR HAVING A METALLIC GATE ELECTRODE THAT IS FORMED AFTER THE FORMATION OF SELF-ALIGNED SOURCE AND DRAIN REGIONS
5960289	22-Jun-18	A METHOD FOR MAKING A DUAL-THICKNESS GATE OXIDE LAYER USING A NITRIDE/OXIDE COMPOSITE REGION
5960306	15-Dec-15	PROCESS FOR FORMING A SEMICONDUCTOR DEVICE
5961373	16-Jun-17	PROCESS FOR FORMING A SEMICONDUCTOR DEVICE
5961579	17-Apr-17	APPARATUS USING A LOGARITHM BASED PROCESSOR

5961622	23-Oct-17	SYSTEM AND METHOD FOR RECOVERING A MICROPROCESSOR FROM A LOCKED BUS STATE
5961791	26-Feb-17	PROCESS FOR FABRICATING A SEMICONDUCTOR DEVICE
5962926	30-Sep-17	SEMICONDUCTOR DEVICE HAVING MULTIPLE OVERLAPPING ROWS OF BOND PADS WITH CONDUCTIVE INTERCONNECTS AND METHOD OF PAD PLACE
5963068	28-Jul-17	FAST START-UP PROCESSOR CLOCK GENERATION METHOD AND SYSTEM
5963076	14-Apr-17	CIRCUIT WITH HOT-ELECTRON PROTECTION AND METHOD
5963315	18-Aug-17	METHOD AND APPARATUS FOR PROCESSING A SEMICONDUCTOR WAFER ON A ROBOTIC TRACK HAVING ACCESS TO IN SITU WAFER BACKSIDE PARTICLE DETECTION
5963588	21-Mar-17	APPARATUS FOR MODULATING DEMODULATING SIGNALS
5963782	01-Aug-17	SEMICONDUCTOR COMPONENT AND METHOD OF MANUFACTURE
5963818	17-Sep-17	A COMBINED TRENCH ISOLATION AND INLAID PROCESS FOR INTEGRATED CIRCUIT FORMATION
5964863	15-Apr-16	METHOD AND APPARATUS FOR PROVIDING PIPE FULLNESS INFORMATION EXTERNAL TO A DATA PROCESSING SYSTEM
5964893	30-Aug-15	DATA PROCESSING SYSTEM FOR PERFORMING A TRACE FUNCTION AND METHOD THEREFOR
5965912	03-Sep-17	VARIABLE CAPACITOR AND METHOD FOR FABRICATING THE SAME
5965930	04-Nov-17	HIGH FREQUENCY BIPOLAR TRANSISTOR AND METHOD OF FORMING THE SAME
5966004	17-Feb-18	ELECTRONIC SYSTEM WITH REGULATOR AND METHOD
5966029	15-Jul-17	MULTI-BIT EXCLUSIVE OR
5966038	15-Dec-17	CIRCUIT WITH OVERVOLTAGE PROTECTION AND METHOD
5966047	27-Mar-17	PROGRAMMABLE ANALOG ARRAY AND METHOD
5966054	29-Jan-18	METHOD AND APPARATUS FOR PROVIDING A CLOCKING SIGNAL
5966087	26-Feb-18	APPARATUS PROVIDING A SUBSTANTIALLY EQUAL TRANSCONDUCTANCE RATIO AND METHOD
5966143	14-Oct-17	DATA ALLOCATION INTO MULTIPLE MEMORIES FOR CONCURRENT ACCESS
5966306	07-Jul-17	METHOD FOR VERIFYING PROTOCOL CONFORMANCE OF AN ELECTRICAL INTERFACE
5966323	18-Dec-17	LOW SWITCHING FIELD MAGNETORESISTIVE TUNNELING JUNCTION FOR HIGH DENSITY ARRAYS
5966517	01-Nov-16	SEMICONDUCTOR DEVICE USING DIE PLATE-HOLDERS AND METHOD OF MANUFACTURE THEREOF
5966619	23-Dec-16	PROCESS FOR FORMING A SEMICONDUCTOR DEVICE HAVING A CONDUCTIVE MEMBER THAT PROTECTS FIELD ISOLATION DURING ETCHING
5966635	31-Jan-17	METHOD FOR REDUCING PARTICLES ON A SUBSTRATE USING CHUCK CLEANING
5968849	26-Jun-15	METHOD FOR PRE-SHAPING A SEMICONDUCTOR SUBSTRATE FOR POLISHING AND STRUCTURE
5969383	16-Jun-17	SPLIT GATE MEMORY DEVICE AND METHOD FOR ACCESSING THE SAME
5969698	19-Oct-16	MANUALLY CONTROLLABLE CURSOR AND CONTROL PANEL IN A VIRTUAL IMAGE

5969972	01-Jul-17	METHOD FOR MANUFACTURING A SEMICONDUCTOR COMPONENT AND AUTOMATIC MACHINE PROGRAM GENERATOR THEREFOR
5970246	11-Sep-17	DATA PROCESSING SYSTEM HAVING A TRACE MECHANISM AND METHOD THEREFOR
5972804	05-Aug-17	PROCESS FOR FORMING A SEMICONDUCTOR DEVICE
5973337	25-Aug-17	SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURE
5973379	07-Nov-16	FERROELECTRIC SEMICONDUCTOR DEVICE
5973388	04-Jan-19	LEADFRAME, METHOD OF MANUFACTURING A LEADFRAME AND METHOD OF PACKAGING AN ELECTRONIC COMPONENT UTILIZING THE LEADFRAME
5973528	16-Apr-18	CONTROL CIRCUIT AND METHOD FOR A TEMPERATURE SENSITIVE DEVICE
5973568	01-Jun-18	POWER AMPLIFIER OUTPUT MODULE FOR DUAL-MODE DIGITAL SYSTEMS
5973955	02-Feb-18	COMPARISON CIRCUIT UTILIZING A DIFFERENTIAL AMPLIFIER
5974255	26-Oct-16	METHOD FOR OBJECT ORIENTED TESTING
5974505	02-Sep-17	METHOD AND SYSTEM FOR REDUCING POWER CONSUMPTION OF A NON-BLOCKING CACHE WITHIN A DATA PROCESSING SYSTEM
5974535	09-May-17	METHOD AND SYSTEM IN A DATA PROCESSING SYSTEM OF PERMITTING CONCURRENT PROCESSING OF INSTRUCTIONS OF A PARTICULAR TYPE
5975757	20-Nov-15	PROBE FOR PROVIDING SURFACE IMAGES AND METHOD FOR MAKING
5977632	02-Feb-18	FLIP CHIP BUMP STRUCTURE AND METHOD OF MAKING
5977892	16-May-16	OFFSET CANCELLATION CIRCUIT
5977950	02-Nov-16	MANUALLY CONTROLLABLE CURSOR IN A VIRTUAL IMAGE
5978249	17-Dec-17	HIGH IMPEDANCE SIGNAL CONVERSION CIRCUIT AND METHOD
5978257	10-Sep-16	MULTI-LAYER MAGNETIC TUNNELING JUNCTION MEMORY CELLS
5978262	20-Jan-18	CIRCUIT AND METHOD OF LATCHING A BIT LINE IN A NON-VOLATILE MEMORY
5978286	01-Mar-19	TIMING CONTROL OF AMPLIFIERS IN A MEMORY
5980106	22-Apr-18	TEMPERATURE DETECTION CIRCUIT
5981340	29-Sep-17	METHOD OF BUILDING AN EPROM CELL WITHOUT DRAIN DISTURB AND REDUCED SELECT GATE RESISTANCE
5982166	27-Jan-17	METHOD FOR MEASURING A CHARACTERISTIC OF A SEMICONDUCTOR WAFER USING CYLINDRICAL CONTROL
5982493	02-Jun-18	METHOD AND APPARATUS FOR VISUALLY INSPECTING AN OBJECT
5983338	05-Sep-17	METHOD AND APPARATUS FOR INTERFACING A PROCESSOR TO A COPROCESSOR FOR COMMUNICATING REGISTER WRITE INFORMATION
5984510	01-Nov-16	AUTOMATIC SYNTHESIS OF STANDARD CELL LAYOUTS
5985045	24-Oct-14	PROCESS FOR POLISHING A SEMICONDUCTOR SUBSTRATE
5985682	25-Aug-17	METHOD FOR TESTING A BUMPED SEMICONDUCTOR DIE
5985694	29-Sep-17	SEMICONDUCTOR DIE BUMPING METHOD UTILIZING VACUUM STENCIL
5985731	17-Aug-18	METHOD FOR FORMING A SEMICONDUCTOR DEVICE HAVING

		A CAPACITOR STRUCTURE
5985736	28-Jun-14	PROCESS FOR FORMING FIELD ISOLATION
5985748	01-Dec-17	METHOD OF MAKING A SEMICONDUCTOR DEVIVCE USING CHEMICAL-MECHANICAL POLISHING HAVING A COMBINATION- STEP PROCESS
5986598	19-Dec-17	SIGMA DELTA DATA CONVERTER WIT H FEED-FORWARD PATH TO STABILI ZE INTEGRATOR SIGNAL SWING
5986925	07-Apr-18	MAGNETORESISTIVE RANDOM ACCESS MEMORY DEVICE AND OPERATING M ETHOD THEREOF
5987086	01-Nov-16	AUTOMATIC LAYOUT STANDARD CELL ROUTING
5987486	06-Oct-17	APPARATUS AND METHOD FOR DATA PROCESSING
5989760	18-Mar-18	METHOD OF PROCESSING A SUBSTRATE
5990547	02-Mar-18	SEMICONDUCTOR DEVICE HAVING PLATED CONTACTS AND METHOD THEREOF
5990554	23-Nov-16	SEMICONDUCTOR PACKAGE HAVING I SOLATED HEATSINK BONDING PADS
5991190	30-Sep-18	QUANTUM RANDOM ADDRESS MEMORY WITH PIEZO READOUT
5991201	27-Apr-18	NON-VOLATILE MEMORY WITH OVER- PROGRAM PROTECTION AND METHOD THEREFOR
5994161	03-Sep-17	TEMPERATURE COEFFICIENT OF OFF SET ADJUSTED SEMICONDUCTOR DEV ICE AND METHOD THEREOF
5994961	08-Dec-17	TEMPERATURE COMPENSATED DECIBE L LINEAR VARIABLE GAIN AMPLIFI ER
5995568	28-Oct-16	METHOD AND APPARATUS FOR PERFO RMING FRAME SYNCHRONIZATION IN AN ASYMMETRICAL DIGITAL SUBSC RIBER LINE (ADSL) SYSTEM
5995731	29-Dec-17	MULTIPLE BIST CONTROLLERS FOR TESTING MULTIPLE EMBEDDED MEMO RY ARRAYS
5996057	17-Apr-18	DATA PROCESSING SYSTEM AND METHOD OF PERMUTATION WITH REPLICATION WITHIN A VECTOR REGISTER FILE
5998258	22-Apr-18	METHOD OF FORMING A SEMICONDUCTOR DEVICE HAVING A STACKED CAPACITOR STRUCTURE
5999017	03-Jul-17	CMOS IMPLEMENTED OUTPUT BUFFER CIRCUIT FOR PROVIDING ECL LEVEL SIGNALS
5999717	31-Dec-17	METHOD FOR PERFORMING MODEL CHECKING IN INTEGRATED CIRCUIT DESIGN
6000029	03-Nov-17	METHOD AND APPARATUS FOR AFFECTING SUBSEQUENT INSTRUCTION PROCESSING IN A DATA PROCESSOR
6000415	12-May-17	METHOD AND APPARATUS FOR POSITIONING A RESTRICTOR SHIELD OF A PUMP IN RESPONSE TO AN ELECTRIC SIGNAL
6000835	28-Aug-15	METHOD AND SYSTEM FOR PERFORMI NG AN L1 NORM OPERATION
6001493	31-May-16	SUBSTRATE FOR TRANSFERRING BUM PS AND METHOD OF USE
6001513	16-Jun-18	METHOD FOR FORMING A LITHOGRAPHIC MASK USED FOR PATTERNING SEMICONDUCTOR DIE
6001661	06-Apr-18	INTEGRATED CIRCUIT INTERCONNEC T METHOD AND APPARATUS
6001722	20-Jun-17	METALLIZATION/DEPOSITION FOR S EMICONDUCTOR DEVICES
6001726	24-Mar-17	METHOD FOR USING A CONDUCTIVE TUNGSTEN NITRIDE ETCH STOP LAYER TO FORM CONDUCTIVE INTERCONNECTS AND TUNGSTEN NITRIDE CONTACT STRUCTURE

6001730	20-Oct-17	A CHEMICAL MECHANICAL POLISHING (CMP) SLURRY FOR POLISHING COPPER INTERCONNECTS WHICH USE TANTALUM-BASED BARRIER LAYERS
6002148	30-Jun-15	SILICON CARBIDE TRANSISTOR AND METHOD
6002273	05-Oct-18	LINEAR LOW NOISE PHASE-FREQUENCY DETECTOR
6002992	16-Jun-17	TEST SYSTEM FOR VERIFYING ANGLE/TIME BASED SYSTEMS AND METHOD THEREFOR
6003133	17-Nov-17	DATA PROCESSOR WITH A PRIVILEGED STATE FIREWALL AND METHOD THEREFORE
6003753	14-Jul-17	AIR-BLOW SOLDER BALL LOADING SYSTEM FOR MICRO BALL GRID ARRAYS
6004027	21-Dec-16	METHOD AND APPARATUS FOR CONSTRUCTING TEST SUBSEQUENCE GRAPHS UTILIZING UNIQUE INPUT/OUTPUT SEQUENCE (UIO) SETS
6004850	23-Feb-18	A TANTALUM OXIDE ANTI-REFLECTIVE COATING (ARC) INTEGRATED WITH A METALLIC TRANSISTOR GATE ELECTRODE AND METHOD OF FORMATION
6005634	08-Jul-17	METHOD AND APPARATUS FOR CONTROLLING THE DISPLAY OF A VIDEO IMAGE
6006024	01-Nov-16	METHOD OF ROUTING AN INTEGRATED CIRCUIT
6006288	06-Jun-16	A METHOD AND APPARATUS FOR ADAPTABLE BURST CHIP SELECT IN A DATA PROCESSING SYSTEM
6007948	28-May-16	METHOD OF FABRICATING X-RAY MASKS WITH REDUCED ERRORS
6008129	28-Aug-17	PROCESS FOR FORMING A SEMICONDUCTOR DEVICE
6008134	05-Feb-17	PROCESS FOR FORMING A SEMICONDUCTOR DEVICE USING BEVELED CLAMP FINGERS IN AN ETCHING SYSTEM
6008677	02-Apr-18	VOLTAGE RECOVERY CIRCUIT AND METHOD THEREFOR
6009012	03-Jun-18	MICROCONTROLLER HAVING A NON-VOLATILE MEMORY AND A METHOD FOR SELECTING AN OPERATIONAL MODE
6009187	02-Dec-16	WAFER PROBER HAVING AN EMISSIVE DISPLAY INSPECTION SYSTEM AND METHOD
6010927	01-Mar-16	A METHOD FOR MAKING A FERROELECTRIC DEVICE HAVING A TANTALUM NITRIDE BARRIER LAYER
6011719	26-Dec-16	DIGITAL SIGNAL PROCESSOR HAVING AN ON-CHIP PIPELINED EEPROM DATA MEMORY AND AN ON-CHIP PIPELINED EEPROM PROGRAM MEMORY
6011734	12-Mar-18	A FUSELESS MEMORY REPAIR SYSTEM AND METHOD OF OPERATION
6011749	27-Mar-18	INTEGRATED CIRCUIT HAVING OUTPUT TIMING CONTROL CIRCUIT AND METHOD THEREOF
6012076	29-Dec-17	ARITHMETIC LOGIC UNIT HAVING PRESHIFT AND PREROUND CIRCUITS
6012970	15-Jan-17	PROCESS FOR FORMING A SEMICONDUCTOR DEVICE
6013571	16-Jun-17	MICROELECTRONIC ASSEMBLY INCLUDING COLUMNAR INTERCONNECTIONS AND METHOD FOR FORMING SAME
6013933	30-May-17	SEMICONDUCTOR STRUCTURE HAVING A MONOCRYSTALLINE MEMBER OVERLYING A CAVITY IN A SEMICONDUCTOR SUBSTRATE AND PROCESS THEREFOR
6014120	24-Jun-16	LED DISPLAY CONTROLLER AND METHOD OF OPERATION
6014722	04-Apr-14	DATA COMMUNICATION SYSTEM FOR CONTROLLING PRIORITIZATION AND TRANSFER OF DATA AND METHOD THEREFOR

6014761	06-Oct-17	CONVOLUTIONAL INTERLEAVING/DE-INTERLEAVING METHOD USING POINTER INCREMENTING ACROSS PREDETERMINED DISTANCES AND APPARATUS FOR DATA TRANSMISSION
6016017	12-Dec-17	SYSTEM FOR PROVIDING UNINTERRUPTED SUPPLY VOLTAGE AND METHOD
6016269	30-Sep-18	QUANTUM RANDOM ADDRESS MEMORY WITH MAGNETIC READOUT AND/OR NON-VOLATILE MEMORY ELEMENTS
6017792	06-Sep-14	PROCESS FOR FABRICATING A SEMICONDUCTOR DEVICE INCLUDING A NONVOLATILE MEMORY CELL
6017798	07-Jul-15	FET WITH STABLE THRESHOLD VOLTAGE AND METHOD OF MANUFACTURING THE SAME
6018192	30-Jul-18	ELECTRONIC DEVICE WITH A THERMAL CONTROL CAPABILITY
6018998	30-Apr-16	ACCELERATION SENSING DEVICE AND METHOD OF OPERATION
6019508	29-Apr-18	INTEGRATED TEMPERATURE SENSOR
6020024	04-Aug-17	METHOD FOR FORMING HIGH DIELECTRIC CONSTANT METAL OXIDES
6020213	07-Nov-16	FERROELECTRIC SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURE
6020261	01-Jun-19	PROCESS FOR FORMING HIGH ASPECT RATIO CIRCUIT FEATURES
6020611	10-Jun-18	SEMICONDUCTOR COMPONENT AND METHOD OF MANUFACTURE
6020787	07-Jun-15	METHOD AND APPARATUS FOR AMPLIFYING A SIGNAL
6021072	27-Jul-18	METHOD AND APPARATUS FOR PRECHARGING BITLINES IN A NONVOLATILE MEMORY
6021078	27-Jan-18	FUSE CIRCUIT AND METHOD THEREFOR WHICH MINIMIZES FUSE GROW BACK EFFECT
6021271	16-Nov-15	METHODS OF SIMULATING AN ELECTRONIC CIRCUIT DESIGN
6022410	01-Sep-18	METHOD OF FABRICATING ALKALINE-EARTH METAL SILICIDE ON SILICON
6022754	22-Jul-18	ELECTRONIC DEVICE AND METHOD FOR FORMING A MEMBRANE FOR AN ELECTRONIC DEVICE
6022761	28-May-16	METHOD FOR COUPLING SUBSTRATES AND STRUCTURE
6022784	06-Apr-18	METHOD FOR MANUFACTURING A SEMICONDUCTOR DEVICE
6023081	14-Nov-17	SEMICONDUCTOR IMAGE SENSOR AND METHOD THEREFOR
6023086	02-Sep-17	SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURE
6023091	30-Nov-15	SEMICONDUCTOR HEATER AND METHOD FOR MAKING
6023133	20-Mar-18	PARABOLIC SIGNAL GENERATOR
6023136	28-Nov-17	ADAPTIVE MOTOR CONTROL CIRCUIT AND METHOD
6023141	13-May-19	METHOD AND APPARATUS FOR ELECTRONICALLY COMMUTATING AN ELECTRIC MOTOR
6023189	06-Sep-14	CMOS CIRCUIT FOR PROVIDING A BANDGAP REFERENCE VOLTAGE
6023199	16-Jan-18	PULSE WIDTH MODULATION CIRCUIT AND METHOD
6023256	13-May-17	LIQUID CRYSTAL DISPLAY DRIVER SYSTEM AND METHOD THEREFOR
6023737	24-Apr-18	MULTI-STAGE PIPELINED DATA COALESCING FOR IMPROVED FREQUENCY OPERATION
6024885	08-Dec-17	PROCESS OF PATTERNING MAGNETIC FILMS

6025281	18-Dec-17	PASSIVATION OF OXIDE-COMPOUND SEMICONDUCTOR INTERFACES
6025735	23-Dec-16	PROGRAMMABLE SWITCH MATRIX AND METHOD OF PROGRAMMING
6026003	18-Dec-18	CHARGE PUMP CIRCUIT AND METHOD
6026013	30-Sep-18	QUANTUM RANDOM ADDRESS MEMORY
6026501	30-Aug-15	DATA PROCESSING SYSTEM FOR CONTROLLING EXECUTION OF A DEBUG FUNCTION AND METHOD THEREFOR
6027961	30-Jun-18	CMOS SEMICONDUCTOR DEVICES AND METHOD OF FORMATION
6027997	22-Feb-17	METHOD FOR CHEMICAL MECHANICAL POLISHING A SEMICONDUCTOR DEVICE USING SLURRY
6028003	03-Jul-17	METHOD OF FORMING AN INTERCONNECT STRUCTURE WITH A GRADED COMPOSITION USING A NITRIDED TARGET
6028544	02-Jan-18	DIGITAL-TO-ANALOG CONVERTER WITH NOISESHAPING MODULATOR, COMMUTATOR AND PLURALITY OF UNIT CONVERTERS, AND METHOD
6029006	23-Dec-16	DATA PROCESSOR WITH CIRCUIT FOR REGULATING INSTRUCTION THROUGHPUT WHILE POWERED AND METHOD OF OPERATION
6029073	24-Feb-18	DISPLAY CARRIER WITH BINOCULAR VIEWING FOR A PORTABLE ELECTRONIC DEVICE
6030453	04-Mar-17	III-V EPITAXIAL WAFER PRODUCTION
6031251	28-Feb-17	LINEAR INTEGRATED OPTO-COUPLER AND METHOD FOR FORMING THE SAME
6031775	01-Mar-19	DYNAMIC SENSE AMPLIFIER IN A MEMORY CAPABLE OF LIMITING THE VOLTAGE SWING ON HIGH-CAPACITANCE GLOBAL DATA LINES
6033231	29-Feb-16	SEMICONDUCTOR DEVICE HAVING A PEDESTAL AND METHOD OF FORMING
6034333	29-Dec-15	ASSEMBLY HAVING A FRAME EMBEDDED IN A POLYMERIC ENCAPSULANT AND METHOD FOR FORMING SAME
6034562	07-Mar-17	MIXED SIGNAL PROCESSING SYSTEM AND METHOD FOR POWERING SAME
6034735	08-Apr-17	CLOCK GENERATOR FOR DIGITAL VIDEO SIGNAL PROCESSING APPARATUS
6034736	08-Dec-17	DIGITAL HORIZONTAL FLYBACK CONTROL CIRCUIT
6035372	20-Dec-16	MICROPROCESSOR AND SYSTEM
6035422	30-Aug-15	DATA PROCESSING SYSTEM FOR CONTROLLING EXECUTION OF A DEBUG FUNCTION AND METHOD THEREFOR
6037202	28-Sep-15	A METHOD FOR GROWING AN EPITAXIAL LAYER OF MATERIAL USING A HIGH TEMPERATURE BUCK GROWTH PHASE (AS AMENDED)
6037246	17-Sep-16	METHOD OF MAKING A CONTACT STRUCTURE
6037668	13-Nov-18	INTEGRATED CIRCUIT HAVING A SUPPORT STRUCTURE
6037674	26-Jun-18	CIRCUIT AND METHOD OF CURRENT LIMITING A HALF-BRIDGE DRIVER
6037789	14-Mar-17	WIPING CONTACTS
6039765	15-Dec-17	COMPUTER INSTRUCTION WHICH GENERATES MULTIPLE RESULTS OF DIFFERENT DATA TYPES TO IMPROVE SOFTWARE EMULATION
6039835	15-Sep-17	ETCHING APPARATUS AND METHOD OF ETCHING A

		SUBSTRATE
6039849	28-Oct-17	APPARATUS FOR THE MANUFACTURE OF ELECTRONIC COMPONENTS AND METHOD FOR ITS USE
6040604	21-Jul-17	SEMICONDUCTOR COMPONENT COMPRISING AN ELECTROSTATIC-DISCHARGE PROTECTION DEVICE
6040624	02-Oct-17	SEMICONDUCTOR DEVICE PACKAGE AND METHOD
6040729	25-Aug-17	DIGITAL OUTPUT BUFFER FOR MULTIPLE VOLTAGE SYSTEM
6041221	21-May-17	CIRCUIT AND METHOD FOR VERIFYING DATA OF A WIRELESS COMMUNICATIONS DEVICE
6043143	04-May-18	OHMIC CONTACT AND METHOD OF MANUFACTURE
6043146	27-Jul-18	PROCESS FOR FORMING A SEMICONDUCTOR DEVICE
6043524	03-Feb-17	TRANSDUCER AND INTERFACE CIRCUIT AND METHOD
6044036	13-May-18	BUFFER CIRCUIT MEMORY DEVICE, AND INTEGRATED CIRCUIT FOR RECEIVING DIGITAL SIGNALS
6044220	25-Feb-17	METHOD AND APPARATUS FOR OPERATING A DATA PROCESSOR TO EXECUTE SOFTWARE WRITTEN USING A FOREIGN INSTRUCTION SET
6044392	04-Aug-17	METHOD AND APPARATUS FOR PERFORMING ROUNDING IN A DATA PROCESSOR
6045435	04-Aug-17	LOW SELECTIVITY CHEMICAL MECHANICAL POLISHING (CMP) PROCESS FOR USE ON INTEGRATED CIRCUIT METAL INTERCONNECTS
6046642	08-Sep-18	AMPLIFIER WITH ACTIVE BIAS COMPENSATION AND METHOD FOR ADJUSTING QUIESCENT CURRENT
6046894	02-Mar-18	SEMICONDUCTOR PROTECTION CIRCUIT AND METHOD
6046897	29-Sep-17	SEGMENTED BUS ARCHITECTURE (SBA) FOR ELECTROSTATIC DISCHARGE (ESD) PROTECTION
6046901	04-May-18	SUPPORT STRUCTURE ELECTRONIC ASSEMBLY AND METHOD OF MANUFACTURE
6046910	18-Mar-18	MICROELECTRONIC ASSEMBLY HAVING SLIDABLE
6047025	13-Feb-18	METHOD AND APPARATUS FOR EQUALIZATION IN AN ASYMMETRIC DIGITAL SUBSCRIBER LINE (ADSL)
6047390	26-Aug-16	MULTIPLE CONTEXT SOFTWARE ANALYSIS
6047480	13-Apr-18	METHOD OF PROCESSING A SEMICONDUCTOR DEVICE
6049114	20-Jul-18	METHOD FOR FORMING A SEMICONDUCTOR DEVICE AND A SEMICONDUCTOR DEVICE FORMED THEREBY
6049119	01-May-18	PROTECTION CIRCUIT FOR A SEMICONDUCTOR DEVICE
6049233	17-Mar-18	PHASE DETECTION APPARATUS
6049501	14-Dec-18	MEMORY DATA BUS ARCHITECTURE AND METHOD OF CONFIGURING MULTI-WIDE WORD MEMORIES
6049703	28-Nov-17	AMPLIFIER CIRCUIT AND METHOD FOR INCREASING LINEARITY OF THE AMPLIFIER CIRCUIT
6049865	18-Dec-17	METHOD AND APPARATUS FOR IMPLEMENTING FLOATING POINT PROJECTION INSTRUCTIONS
6049876	09-Feb-18	DATA PROCESSING SYSTEM AND METHOD WHICH DETECT UNAUTHORIZED MEMORY ACCESSES
6051456	21-Dec-18	SEMICONDUCTOR COMPONENT AND METHOD OF MANUFACTURE
6051997	12-May-18	CIRCUIT FOR TRACKING RAPID CHANGES IN MID-POINT VOLTAGE OF A DATA SIGNAL

6052302	27-Sep-19	BIT-WISE CONDITIONAL WRITE METHOD AND SYSTEM FOR AN MRAM
6052746	14-Apr-18	INTEGRATED CIRCUIT HAVING SINGLE PROGRAMMABLE PULL DEVICE CONFIGURED TO ENABLE/DISABLE FIRST FUNCTION IN FAVOR OF SECOND FUNCTION ACCORDING TO PREDETERMINED SCHEME BEFORE/AFTER RESET
6053049	30-May-17	ELECTRICAL DEVICE HAVING ATMOSPHERIC ISOLATION
6054340	06-Jun-17	METHOD FOR FORMING A CAVITY CAPABLE OF ACCESSING DEEP FUSE STRUCTURES AND DEVICE CONTAINING THE SAME
6054377	19-May-17	METHOD FOR FORMING AN INLAID VIA IN A SEMICONDUCTOR DEVICE
6054825	20-Jan-18	METHOD AND APPARATUS FOR HIGH VOLTAGE GENERATION
6054901	02-Oct-17	LOW-NOISE PREAMPLIFIER
6055178	18-Dec-18	MAGNETIC RANDOM ACCESS MEMORY WITH A REFERENCE MEMORY ARRAY
6055436	20-Jan-18	METHOD AND APPARATUS FOR DETECTING SIMULCAST CHANNEL CONDITIONS
6055557	08-Jan-17	AN ADDER CIRCUIT AND METHOD THEREFOR
6056888	19-Apr-19	ELECTRONIC COMPONENT AND METHOD OF MANUFACTURE
6057219	02-May-17	METHOD OF FORMING AN OHMIC CONTACT TO A III-A SEMICONDUCTOR MATERIAL
6057566	29-Apr-18	SEMICONDUCTOR DEVICE
6057713	12-Mar-18	METHOD AND APPARATUS FOR PERFORMING VOLTAGE SAMPLING
6058405	06-Nov-17	SIMD COMPUTATION OF RANK BASED FILTERS FOR M X N GRIDS
6058449	31-Jul-17	FAULT TOLERANT SERIAL ARBITRATION SYSTEM
6058463	20-Jan-18	PAGED MEMORY DATA PROCESSING SYSTEM WITH OVERLAID MEMORY CONTROL REGISTERS
6059840	17-Mar-17	AUTOMATIC SCHEDULING OF INSTRUCTIONS TO REDUCE CODE SIZE
6061099	14-Oct-17	VIDEO OVERLAY CIRCUIT AND METHOD
6061137	04-May-18	INSITU ENDPOINT DETECTION FOR MEMBRANE FORMATION
6061218	03-Oct-17	OVERVOLTAGE PROTECTION DEVICE AND METHOD FOR INCREASING SHUNT CURRENT
6063698	30-Jun-17	A METHOD FOR MANUFACTURING A HIGH DIELECTRIC CONSTANT GATE OXIDE FOR USE IN SEMICONDUCTOR INTEGRATED CIRCUITS
6064114	01-Dec-17	SEMICONDUCTOR DEVICE HAVING A SUB-CHIP-SCALE PACKAGE STRUCTURE AND METHOD FOR FORMING SAME
6064780	16-May-17	INTERCONNECT SUBSTRATE WITH A SINGLE CONTACT ACCESSIBLE IN AN UPPER AND AN END SURFACE
6065031	28-Jul-15	LOG CONVERTER UTILIZING OFFSET AND METHOD OF USE THEREOF
6065140	30-Apr-17	OPTIMIZED COMPUTATION OF FIRST AND SECOND DIVIDER VALUES FOR A PHASE LOCKED LOOP SYSTEM
6066971	02-Oct-17	INTEGRATED CIRCUIT HAVING BUFFERING CIRCUITRY WITH SLEW RATE CONTROL
6067610	29-Jul-16	METHOD AND DATA PROCESSOR FOR SYNCHRONIZING MULTIPLE MASTERS USING MULTI-BIT SYNCHRONIZATION INDICATORS

6067633	31-Mar-18	A DESIGN AND METHODOLOGY FOR MANUFACTURING DATA PROCESSING SYSTEMS HAVING MULTIPLE PROCESSORS
6068668	31-Mar-17	PROCESS FOR FORMING A SEMICONDUCTOR DEVICE
6069493	28-Nov-17	INPUT CIRCUIT AND METHOD FOR PROTECTING THE INPUT CIRCUIT
6069593	24-Feb-18	DISPLAY CARRIER AND ELECTRONIC DISPLAY CONTROL FOR MULTIPLE DISPLAYS IN A PORTABLE ELECTRONIC DEVICE
6070263	20-Apr-18	CIRCUIT FOR USE IN A VITERBI DECODER
6070464	05-Sep-17	SENSING STRUCTURE COMPRISING A MOVABLE MASS AND A SELF-TEST STRUCTURE
6070600	01-Jul-17	POINT OF USE DILUTION TOOL AND METHOD
6071816	29-Aug-17	METHOD OF CHEMICAL MECHANICAL PLANARIZATION USING A WATER RINSE TO PREVENT PARTICLE CONTAMINATION
6072211	03-Aug-18	SEMICONDUCTOR PACKAGE
6072238	07-Apr-19	SEMICONDUCTOR COMPONENT
6073002	04-May-18	MIXER CIRCUIT AND COMMUNICATION DEVICE USING THE SAME
6073215	03-Aug-18	DATA PROCESSING SYSTEM HAVING A DATA PREFETCH MECHANISM AND METHOD THEREFOR
6073252	25-Sep-17	DATA PROCESSING SYSTEM WITH MEMORY PATCHING AND METHOD THEREOF
6074429	03-Mar-17	OPTIMIZING COMBINATIONAL CIRCUIT LAYOUT THROUGH ITERATIVE RESTRUCTURING
6075271	03-Mar-18	SEMICONDUCTOR DEVICE INHIBITING PARASITIC EFFECTS DURING ELECTROSTATIC DISCHARGE
6075409	01-Apr-19	DEMODULATION METHOD AND ARRANGEMENT
6075727	29-Jul-18	PROCESS FOR FORMING A SEMICONDUCTOR DEVICE
6075934	01-May-17	METHOD FOR OPTIMIZING CONTACT PIN PLACEMENT IN AN INTEGRATED CIRCUIT
6076096	13-Jan-18	BINARY RATE MULTIPLIER
6076149	20-Jan-18	DATA PROCESSING CIRCUIT
6076170	16-Sep-16	METHOD AND APPARATUS FOR SELECTIVELY PROGRAMMING ACCESS TIME IN A DATA PROCESSOR
6076177	23-Sep-17	METHOD AND APPARATUS FOR TESTING A CIRCUIT MODULE CONCURRENTLY WITH A NON-VOLATILE MEMORY OPERATION IN A MULT-MODULE DATA PROCESSING SYSTEM
6076585	02-Mar-18	METHOD OF MANUFACTURING A SEMICONDUCTOR DEVICE AND APPARATUS THEREFOR
6077726	30-Jul-18	METHOD AND APPARATUS FOR STRESS RELIEF IN SOLDER BUMP FORMATION ON A SEMICONDUCTOR DEVICE
6077768	19-Jul-16	PROCESS FOR FABRICATING A MULTILEVEL INTERCONNECT
6077791	16-Dec-16	METHOD OF FORMING PASSIVATION LAYERS USING DEUTERIUM CONTAINING REACTION GASES
6078277	02-Jul-18	ARRANGEMENT AND METHOD FOR PRODUCING A PLURALITY OF PULSE WIDTH MODULATED SIGNALS
6078527	29-Jul-17	PIPELINED DUAL PORT INTEGRATED CIRCUIT MEMORY
6078938	29-May-16	METHOD AND SYSTEM FOR SOLVING LINEAR SYSTEMS
6079001	12-Dec-14	METHOD FOR SYNCHRONOUSLY ACCESSING MEMORY USING OVERLAPPING ACCESSES AND EARLY SYNCHRONOUS DATA TRANSFER CONTROL

6079015	20-Apr-18	DATA PROCESSING SYSTEM HAVING SELECTABLE EXCEPTIONAL TABLE RELOCATION AND METHOD THEREFOR
6081037	22-Jun-18	SEMICONDUCTOR DEVICE AND METHOD FOR PACKAGING A SEMICONDUCTOR CHIP
6081091	08-Mar-19	MOTOR CONTROLLER, INTEGRATED CIRCUIT, AND METHOD OF CONTROLLING A MOTOR
6081216	11-Jun-18	LOW-POWER DECIMATOR FOR AN OVERSAMPLED ANALOG-TO-DIGITAL CONVERTER AND METHOD THEREFOR
6081445	27-Jul-18	METHOD TO WRITE/READ MRAM ARRAYS
6081915	30-Mar-18	METHOD AND APPARATUS FOR REDUCING THE TIME REQUIRED TO TEST AN INTEGRATED CIRCUIT USING SLEW RATE CONTROL
6083764	20-Jul-18	METHOD OF FABRICATING AN MTJ WITH LOW AREAL RESISTANCE
6083806	06-Jul-18	METHOD OF FORMING AN ALIGNMENT MARK
6083819	22-Nov-15	METHOD AND ASSEMBLY FOR PROVIDING IMPROVED UNDERCHIP ENCAPSULATION
6084241	01-Jun-18	METHOD OF MANUFACTURING SEMICONDUCTOR DEVICES AND APPARATUS THEREFOR
6084269	21-Dec-18	SEMICONDUCTOR DEVICE AND METHOD OF MAKING
6084279	31-Mar-17	SEMICONDUCTOR DEVICE AND A PROCESS FOR FORMING THE DEVICE
6084566	29-Dec-17	PATTERN DISPLAY CIRCUIT
6085209	28-Aug-15	METHOD AND SYSTEM FOR PERFORMING AN IIR FILTERING OPERATION
6085261	29-Jul-16	METHOD AND APPARATUS FOR BURST PROTOCOL IN A DATA PROCESSING SYSTEM
6085275	04-Jul-17	A DATA PROCESSING SYSTEM AND METHOD THEREOF
6085282	24-Sep-17	METHOD AND APPARATUS FOR DISTINGUISHING REGISTER READS FROM MEMORY READS IN A FLASH MEMORY
6085334	17-Apr-18	METHOD AND APPARATUS FOR TESTING AN INTEGRATED MEMORY DEVICE
6087267	11-Jul-17	PROCESS FOR FORMING AN INTEGRATED CIRCUIT
6087701	23-Dec-17	SEMICONDUCTOR DEVICE HAVING A CAVITY AND METHOD OF MAKING
6087873	21-Jul-18	PRECISION HYSTERESIS CIRCUIT
6087969	27-Apr-18	SIGMA-DELTA MODULATOR AND METHOD FOR DIGITIZING A SIGNAL
6088215	03-Nov-17	CAPACITOR AND METHOD OF MANUFACTURE
6088782	10-Jul-17	METHOD AND APPARATUS FOR MOVING DATA IN A PARALLEL PROCESSOR USING SOURCE AND DESTINATION VECTOR REGISTERS
6091287	23-Jan-18	VOLTAGE REGULATOR WITH AUTOMATIC ACCELERATED AGING CIRCUIT
6091391	20-Mar-18	CIRCUIT FOR PRODUCING A CONTRAST VOLTAGE SIGNAL FOR A LIQUID CRYSTAL DISPLAY
6091786	25-Mar-18	METHOD AND APPARATUS IN A COMMUNICATION RECEIVER FOR ESTIMATING SYMBOL TIMING AND CARRIER FREQUENCY
6093966	20-Mar-18	A SEMICONDUCTOR DEVICE WITH A COPPER BARRIER LAYER AND FORMATION THEREOF
6093972	19-May-17	MICROELECTRONIC PACKAGE INCLUDING A POLYMER ENCAPSULATED DIE, AND METHOD FOR FORMING SAME

6094295	12-Feb-18	ULTRAVIOLET TRANSMITTING OXIDE WITH METALLIC OXIDE PHASE AND METHOD OF FABRICATION
6096575	26-Aug-19	OPTIMUM CONDITION DETECTING METHOD FOR FLIP-CHIP
6096606	04-May-18	SEMICONDUCTOR DEVICE AND METHOD OF MAKING
6096652	03-Nov-17	METHOD OF CHEMICAL MECHANICAL PLANARIZATION USING COPPER COORDINATING LIGANDS
6097060	30-Sep-16	INSULATED GATE SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURE
6097075	02-Aug-19	SEMICONDUCTOR STRUCTURE FOR DRIVER CIRCUITS WITH LEVEL SHIFTING
6097627	30-Sep-18	QUANTUM RANDOM ADDRESS MEMORY WITH NANO-DIODE MIXER
6097674	30-Oct-15	METHOD FOR MEASURING TIME AND STRUCTURE THEREFOR
6097889	23-Jun-17	SIGNAL PROCESSING APPARATUS AND METHOD FOR PROCESSING SIGNALS
6100549	12-Aug-18	HIGH BREAKDOWN VOLTAGE RESURF HFET
6100556	14-Nov-17	METHOD OF FORMING A SEMICONDUCTOR IMAGE SENSOR AND STRUCTURE
6100568	06-Nov-17	SEMICONDUCTOR DEVICE INCLUDING A MEMORY CELL AND PERIPHERAL PORTION AND METHOD FOR FORMING SAME
6100717	16-Oct-18	LINE DRIVER CIRCUIT WITH REDUCED POWER CONSUMPTION
6100721	01-Feb-19	CIRCUIT AND METHOD OF EXTENDING THE LINEAR RANGE OF A PHASE FREQUENCY DETECTOR
6100763	29-Mar-19	CIRCUIT FOR RF BUFFER AND METHOD OF OPERATION
6100787	28-May-17	MULTILAYER CERAMIC PACKAGE WITH LOW-VARIANCE EMBEDDED RESISTORS AND METHOD OF MAKING SAME
6101125	20-May-18	ELECTRICALLY PROGRAMMABLE MEMORY AND METHOD OF PROGRAMMING
6101130	29-Jun-19	SEMICONDUCTOR DEVICE MEMORY CELL AND METHOD FOR SELECTIVELY ERASING THE SAME
6101145	21-Dec-18	SENSING CIRCUIT AND METHOD
6103548	17-Sep-17	SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURE
6104227	29-Mar-19	RF MIXER CIRCUIT AND METHOD OF OPERATION
6105428	10-Dec-18	SENSOR AND METHOD OF USE
6106567	28-Apr-18	CIRCUIT DESIGN VERIFICATION TOOL AND METHOD THEREFOR
6106571	29-Jan-18	RELOCATABLE INSTRUMENTATION TAGS FOR TESTING AND DEBUGGING A COMPUTER PROGRAM
6107136	17-Aug-18	METHOD FOR FORMING A CAPACITOR STRUCTURE
6107180	30-Jan-18	METHOD FOR FORMING INTERCONNECT BUMPS ON A SEMICONDUCTOR DIE
6107187	17-Jun-19	METHOD FOR FORMING A SEMICONDUCTOR DEVICE
6107203	03-Nov-17	A CHEMICAL MECHANICAL POLISHING SYSTEM AND METHOD THEREFOR
6108181	23-Apr-16	ELECTROSTATIC DISCHARGE (ESD) CIRCUIT
6108263	12-Aug-19	MEMORY SYSTEM, METHOD FOR VERIFYING DATA STORED IN A MEMORY SYSTEM AFTER A WRITE CYCLE AND METHOD FOR WRITING TO A MEMORY SYSTEM
6108266	28-Oct-19	MEMORY UTILIZING A PROGRAMMABLE DELAY TO CONTROL ADDRESS BUFFERS

6110840	17-Feb-18	METHOD OF PASSIVATING THE SURFACE OF A SI SUBSTRATE
6111316	29-Aug-17	ELECTRONIC COMPONENT AND METHOD FOR MAKING
6111419	19-May-18	METHOD OF PROBING A SUBSTRATE INCLUDING MEASURING FOR PLANARITY AND PROBING THE SUBSTRATE
6111761	23-Aug-19	AN ELECTRONIC ASSEMBLY
6111781	03-Aug-18	MAGNETIC RANDOM ACCESS MEMORY ARRAY DIVIDED INTO A PLURALITY OF MEMORY BANKS
6111796	01-Mar-19	PROGRAMMABLE DELAY CONTROL IN A MEMORY
6111855	03-Aug-18	METHOD AND APPARATUS FOR BAUD DETECTION IN A COMMUNICATION DEVICE
6113690	08-Jun-18	METHOD OF PREPARING CRYSTALLINE ALKALINE EARTH METAL OXIDES ON A SI SUBSTRATE
6113721	05-Sep-17	METHOD OF BONDING A SEMICONDUCTOR WAFER
6115618	24-Feb-18	PORTABLE ELECTRONIC DEVICE WITH REMOVABLE DISPLAY
6117759	03-Jan-17	METHOD FOR MULTIPLEXED JOINING OF SOLDER BUMPS TO VARIOUS SUBSTRATES DURING ASSEMBLY OF AN INTEGRATED CIRCUIT PACKAGE
6118171	21-Dec-18	SEMICONDUCTOR DEVICE AND METHOD OF MAKING
6118175	21-Apr-18	WIRE BONDING SUPPORT STRUCTURE AND METHOD FOR COUPLING SEMICONDUCTOR CHIPS TO LEAD FRAMES
6119203	03-Aug-18	MECHANISM FOR SHARING DATA CACHE RESOURCES BETWEEN DATA PREFETCH OPERATIONS AND NORMAL LOAD/STORE OPERATIONS IN A DATA PROCESSING SYSTEM
6119204	30-Jun-18	DATA PROCESSING SYSTEM AND METHOD FOR MAINTAINING TRANSLATION LOOK-ASIDE BUFFER (TLB) COHERENCY WITHOUT ENFORCING COMPLE
6119240	05-Feb-16	A LOW POWER DATA PROCESSING SYSTEM FOR INTERFACING WITH AN EXTERNAL DEVICE AND METHOD THEREFOR
6121784	30-Nov-15	PROBE TIP, A PROBE CARD, AND A PROCESS FOR TESTING A SEMICONDUCTOR DEVICE
6121845	15-May-18	PHASED-LOCKED LOOP SYSTEM AND METHOD FOR MODIFYING AN OUTPUT TRANSITION TIME
6121849	24-Jul-18	OSCILLATOR AMPLIFIER WITH FREQUENCY BASED DIGITAL MULTI-DISCRETE-LEVEL GAIN CONTROL AND METHOD OF OPERATION
6122247	24-Nov-17	METHOD FOR REALLOCATING DATA IN A DISCRETE MULTITONE COMMUNICATION SYSTEM
6122963	22-Jan-19	ELECTRONIC COMPONENT
6124063	30-Jul-18	METHOD OF FORMING A SEMICONDUCTOR DEVICE UTILIZING LITHOGRAPHIC MASK AND MASK THEREFOR
6125147	31-Jul-18	METHOD AND APPARATUS FOR REDUCING BREATHING ARTIFACTS IN COMPRESSED VIDEO
6125404	17-Apr-18	COMMUNICATIONS SYSTEM HAVING A PROTOCOL TIMER
6125413	29-Dec-17	COMPUTER SYSTEM WITH TRIGGER CONTROLLED INTERFACE AND METHOD
6127230	29-Mar-16	VERTICAL SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING THE SAME
6127257	03-Oct-17	IMPROVED CONTACT STRUCTURE AND PROCESS
6127258	25-Jun-18	METHOD FOR FORMING A SEMICONDUCTOR DEVICE
6127272	26-Jan-18	METHOD OF ELECTRON BEAM LITHOGRAPHY ON VERY HIGH RESISTIVITY SUBSTRATES

6127831	21-Apr-17	METHOD OF TESTING A SEMICONDUCTOR DEVICE BY AUTOMATICALLY MEASURING PROBE TIP PARAMETERS
6127875	13-Aug-18	COMPLIMENTARY DOUBLE PUMPING VOLTAGE BOOST CONVERTER
6128224	29-Jul-18	METHOD AND APPARATUS FOR WRITING AN ERASABLE NON-VOLATILE MEMORY
6128317	22-Dec-17	TRANSMITTER AND RECEIVER SUPPORTING DIFFERING SPEED CODECS OVER SINGLE LINKS
6128404	03-Oct-17	METHOD OF DETECTING DEFECTS IN SEMICONDUCTOR PACKAGE LEADS
6128672	10-Mar-18	DATA TRANSFER USING SOFTWARE INTERRUPT SERVICE ROUTINE BETWEEN HOST PROCESSOR AND EXTERNAL DEVICE WITH QUEUE ON HOST PROCESSOR AND HARDWARE QUEUE POINTERS ON EXTERNAL DEVICE
6128716	23-Jan-18	MEMORY CONTROLLER WITH CONTINUOUS PAGE MODE AND METHOD THEREFOR
6130102	03-Nov-17	METHOD FOR FORMING A SEMICONDUCTOR DEVICE INCLUDING A DUAL INLAID STRUCTURE
6130548	09-Jul-19	SIGNAL CONVERTING RECEIVER HAVING CONSTANT HYSTERESIS, AND METHOD THEREFOR
6130635	03-Aug-18	METHOD OF CONVERTING AN ANALOG SIGNAL IN AN A/D CONVERTER UTILIZING RDAC PRECHARGING
6130821	03-Dec-18	MULTI-CHIP ASSEMBLY HAVING A HEAT SINK AND METHOD THEREOF
6130882	25-Sep-17	METHOD AND APPARATUS FOR CONFIGURING A COMMUNICATION SYSTEM
6130920	13-May-18	METHOD AND APPARATUS FOR ACCURATE SYNCHRONIZATION USING SYMBOL DECISION FEEDBACK
6130921	23-Dec-17	FREQUENCY SHIFT MODULATION AUTOMATIC FREQUENCY CORRECTION CIRCUIT AND METHOD
6131017	27-Mar-18	DUAL SYSTEM PORTABLE ELECTRONIC COMMUNICATOR
6131080	17-Aug-18	METHOD OF MONITORING A COMPUTER SIMULATION OF AN ELECTRICAL CIRCUIT
6133054	02-Aug-19	METHOD FOR TESTING AN INTEGRATED CIRCUIT
6133093	30-Jan-18	METHOD FOR FORMING AN INTEGRATED CIRCUIT
6133100	29-Nov-16	METHOD FOR MANUFACTURING A READ ONLY MEMORY ARRAY
6133764	27-Jan-19	COMPARATOR CIRCUIT AND METHOD
6133774	05-Mar-19	CLOCK GENERATOR AND METHOD THEREFOR
6133793	27-Jul-18	CIRCUIT AND METHOD OF AMPLIFYING A SIGNAL FOR A RECEIVER
6133797	30-Jul-19	SELF CALIBRATING VCO CORRECTION CIRCUIT AND METHOD OF OPERATION
6134620	31-Mar-18	IMPROVED TRI-STATE BUS CONTENTION CIRCUIT
6134675	14-Jan-18	METHOD OF TESTING MULTI-CORE PROCESSORS AND MULTI-CORE PROCESSOR TESTING DEVICE
6134689	12-Feb-18	METHOD OF MAKING AND TESTING LOGIC DEVICES
6134941	23-Mar-18	METHOD OF TESTING SENSORS AND APPARATUS THEREFOR
6135855	30-Jul-18	TRANSLATION MECHANISM FOR A CHEMICAL MECHANICAL PLANARIZATION SYSTEM AND METHOD THEREFOR
6136678	02-Mar-18	METHOD OF PROCESSING A CONDUCTIVE LAYER AND FORMING A SEMICONDUCTOR DEVICE

6136682	20-Oct-17	METHOD FOR FORMING A CONDUCTIVE STRUCTURE HAVING A COMPOSITE OR AMORPHOUS BARRIER LAYER
6136718	10-Jul-18	METHOD OF MANUFACTURING A SEMICONDUCTOR WAFER USING A THREADLESS CORROSION-PREVENTING GAS RING
6137062	11-May-18	BALL GRID ARRAY WITH RECESSED SOLDER BALLS
6137154	02-Feb-18	BIPOLAR TRANSISTOR WITH INCREASED EARLY VOLTAGE
6137272	03-Nov-19	AC-DC-CONVERTER, AND METHOD
6137347	04-Nov-18	MID SUPPLY REFERENCE GENERATOR
6137429	08-Mar-19	CIRCUIT AND METHOD FOR ATTENUATING NOISE IN A DATA CONVERTER
6137466	03-Nov-17	LCD DRIVER MODULE AND METHOD THEREOF
6137690	18-Aug-17	ELECTRONIC ASSEMBLY UTILIZING A NICKEL-GOLD FINISH
6137852	23-Dec-17	PHASE DETECTOR CIRCUIT AND METHOD OF PHASE DETECTING
6137995	08-Dec-18	CIRCUIT AND METHOD OF GENERATING A PHASE LOCKED LOOP SIGNAL HAVING AN OFFSET REFERENCE
6137997	04-Aug-18	CIRCUIT FOR RECEIVING AND TRANSMITTING SIGNALS AND METHOD
6137999	24-Dec-17	IMAGE REJECTION TRANSCEIVER AND METHOD OF REJECTING AN IMAGE
6138204	17-Dec-17	MULTI BUS ACCESS MEMORY
6138229	29-May-18	METHOD AND ARCHITECTURE FOR CUSTOMIZABLE INSTRUCTION SET PROCESSOR
6139079	20-Oct-17	UNIVERSAL TRANSPORT APPARATUS AND METHOD
6139696	25-Oct-19	METHOD AND APPARATUS FOR FORMING A LAYER ON A SUBSTRATE
6140184	01-Jun-18	FIELD EFFECT TRANSISTOR AND METHOD OF MAKING
6140212	01-Jun-18	SEMICONDUCTOR DEVICE AND METHOD THEREFOR
6140703	05-Aug-16	METHOD OF FORMING A SEMICONDUCTOR METALLIZATION SYSTEM AND STRUCTURE THEREFOR
6140854	25-Jan-19	SYSTEM WITH DLL
6141200	20-Apr-18	STACKED PFET OFF-CHIP DRIVER WITH A LATCH BIAS GENERATOR FOR OVERVOLTAGE PROTECTION
6143646	03-Jun-17	DUAL IN-LINE INTEGRATED CIRCUIT STRUCTURE WITH SELECTIVELY POSITIONED LOW-K DIELECTRIC ISOLATION AND METHOD OF FORMATION
6143648	18-Feb-17	METHOD FOR FORMING AN INTEGRATED CIRCUIT
6144569	29-Feb-20	SYSTEM AND METHOD FOR RECOVERING FROM A POWER SUPPLY INTERRUPTION
6144611	07-Sep-19	METHOD FOR CLEARING MEMORY CONTENTS AND MEMORY ARRAY CAPABLE OF PERFORMING THE SAME
6144845	31-Dec-17	METHOD AND CIRCUIT FOR IMAGE REJECTION
6144846	31-Dec-17	FREQUENCY TRANSLATION CIRCUIT AND METHOD OF TRANSLATING
6145070	15-Aug-17	METHOD FOR DIGITAL SIGNAL PROCESSING, DSP, MOBILE COMMUNICATION AND AUDIO-DEVICE
6145097	05-Sep-17	METHOD AND APPARATUS FOR PROVIDING OPERAND FEED FORWARD SUPPORT IN A DATA PROCESSING SYSTEM
6145104	12-Feb-18	DATA PROCESSING SYSTEM EXTERNAL PIN CONNECTIVITY TO COMPLEX FUNCTIONS

6145122	27-Apr-18	DEVELOPMENT INTERFACE FOR A DATA PROCESSOR
6146250	15-Jan-17	PROCESS FOR FORMING A SEMICONDUCTOR DEVICE
6146541	02-May-17	METHOD OF MANUFACTURING A SEMI CONDUCTOR DEVICE
6146926	15-Aug-17	LATERAL GATE VERTICAL DRIFT REGION TRANSISTOR
6146948	03-Jun-17	METHOD FOR MANUFACTURING A THIN OXIDE FOR USE IN SEMICONDUCTOR INTEGRATED CIRCUITS
6146970	26-May-18	CAPPED SHALLOW TRENCH ISOLATION AND METHOD OF FORMATION
6147410	02-Mar-18	ELECTRONIC COMPONENT AND METHOD OF MANUFACTURE
6147510	13-Jul-18	INTEGRATED CIRCUIT FOR HANDLING BUFFER CONTENTION AND METHOD THEREOF
6147540	31-Aug-18	HIGH VOLTAGE INPUT BUFFER MADE BY A LOW VOLTAGE PROCESS AND HAVING A SELF-ADJUSTING TRIGGER POINT
6147551	05-Jan-18	SWITCHED CAPACITOR CIRCUIT AND METHOD FOR REDUCING SAMPLING NOISE
6147741	25-Feb-17	DIGITAL SCANNER AND METHOD OF FABRICATION
6148673	21-Nov-17	DIFFERENTIAL PRESSURE SENSOR AND METHOD THEREOF
6149508	03-Nov-17	CHEMICAL MECHANICAL PLANARIZATION SYSTEM AND METHOD THEREFOR
6150190	27-May-19	METHOD OF FORMATION OF BURIED MIRROR SEMICONDUCTIVE DEVICE
6150200	03-Apr-18	SEMICONDUCTOR DEVICE AND METHOD OF MAKING
6150240	27-Jul-18	METHOD AND APPARATUS FOR SINGULATING SEMICONDUCTOR DEVICES
6150724	02-Mar-18	MULTI-CHIP SEMICONDUCTOR DEVICE AND METHOD FOR MAKING THE DEVICE BY USING MULTIPLE FLIP CHIP INTERFACES
6150881	25-May-19	AMPLIFIER CIRCUIT WITH AMPLITUDE AND PHASE CORRECTION AND METHOD OF OPERATION
6150889	03-Aug-18	CIRCUIT AND METHOD FOR MINIMIZING RECOVERY TIME
6150917	21-Nov-17	PIEZORESISTIVE SENSOR AND METHOD
6151594	21-Nov-17	ARTIFICIAL NEURON AND METHOD OF USING SAME
6153141	28-Nov-17	SEMICONDUCTOR PACKAGING METHOD AND APPARATUS
6153443	21-Dec-18	METHOD OF FABRICATING A MAGNETIC RANDOM ACCESS MEMORY
6153519	31-Mar-17	METHOD FOR DEPOSITING A DIFFUSION BARRIER
6153905	21-Dec-18	SEMICONDUCTOR COMPONENT AND METHOD OF MANUFACTURE
6154369	23-Mar-18	ELECTRONIC ASSEMBLY AND METHOD OF MANUFACTURE
6154413	05-Apr-19	METHOD FOR DESIGNING A MEMORY TILE FOR USE IN A TILED MEMORY
6156579	20-Apr-12	CIRCUIT IDENTIFIER FOR USE WITH FOCUSED ION BEAM EQUIPMENT
6156585	02-Feb-18	SEMICONDUCTOR COMPONENT AND METHOD OF MANUFACTURE
6156611	20-Jul-18	METHOD OF FABRICATING VERTICAL FET WITH SIDEWALL GATE ELECTRODE
6157247	06-Aug-17	METHODS AND CIRCUITS FOR DYNAMICALLY ADJUSTING A SUPPLY VOLTAGE AND/OR A FREQUENCY OF A CLOCK SIGNAL IN A DIGITAL CIRCUIT

6157583	02-Mar-19	INTEGRATED CIRCUIT MEMORY HAVING A FUSE DETECT CIRCUIT AND METHOD THEREFOR
6157968	26-Jan-18	INTERFACE WITH SELECTOR RECEIVING CONTROL WORDS COMPRISING DEVICE IDENTIFIERS FOR DETERMINING CORRESPONDING COMMUNICATIONS PARAMETER SET FOR INTERFACE PORT TRANSFER OF DATA WORDS TO PERIPHERAL DEVICES
6157989	03-Jun-18	DYNAMIC BUS ARBITRATION PRIORITY AND TASK SWITCHING BASED ON SHARED MEMORY FULLNESS IN A MULTI-PROCESSOR SYSTEM
6157998	03-Apr-18	METHOD FOR PERFORMING BRANCH PREDICTION AND RESOLUTION OF TWO OR MORE BRANCH INSTRUCTIONS WITHIN TWO OR MORE BRANCH PREDICTION BUFFERS
6157999	03-Jun-17	DATA PROCESSING SYSTEM HAVING A SYNCHRONIZING LINK STACK AND METHOD THEREOF
6159834	12-Feb-18	METHOD OF FORMING A GATE QUALITY OXIDE COMPOUND SEMICONDUCTOR STRUCTURE
6160280	04-Mar-16	DOWN CONVERTER AND METHOD FOR GENERATING AN INTERMEDIATE FREQUENCY SIGNAL
6160305	23-Dec-16	BETA DEPENDENT TEMPERATURE SENSOR FOR AN INTEGRATED CIRCUIT
6160490	02-Feb-18	APPARATUS FOR IMPROVING THE BATTERY LIFE OF A SELECTIVE CALL RECEIVER
6160842	17-Feb-18	DEVICE AND METHOD FOR SERIALY COMMUNICATING
6160861	22-Oct-19	METHOD AND APPARATUS FOR A FREQUENCY MODULATION PHASE LOCKED LOOP
6160906	01-Jun-18	METHOD AND APPARATUS FOR VISUALLY INSPECTING AN OBJECT
6161200	11-Sep-15	METHOD AND APPARATUS FOR ANALYZING SOFTWARE EXECUTED IN EMBEDDED SYSTEMS
6163063	01-Jun-18	SEMICONDUCTOR DEVICE AND METHOD THEREFOR
6163835	06-Jul-18	METHOD AND APPARATUS FOR TRANSFERRING DATA OVER A PROCESSOR INTERFACE BUS
6165567	12-Apr-19	PROCESS OF FORMING A SEMICONDUCTOR DEVICE
6165803	31-Aug-18	MAGNETIC RANDOM ACCESS MEMORY AND FABRICATING METHOD THEREOF
6165888	02-Oct-17	TWO STEP WIRE BOND PROCESS
6166451	14-Jan-19	RECTIFIER CIRCUIT HAVING FIRST AND SECOND INPUTS INTERCHANGEABLY CONNECTED TO FIRST AND SEMICONDUCTORS
6166552	10-Jun-16	METHOD AND APPARATUS FOR TESTING A SEMICONDUCTOR WAFER
6166556	28-May-18	METHOD FOR TESTING A SEMICONDUCTOR DEVICE AND SEMICONDUCTOR DEVICE TESTED THEREBY
6166578	31-Aug-18	CIRCUIT ARRANGEMENT TO COMPENSATE NON-LINEARITIES IN A RESISTOR, AND METHOD
6166586	23-Dec-16	INTEGRATED CIRCUIT AND METHOD THEREFOR
6166653	13-Aug-18	SYSTEM FOR ADDRESS INITIALIZATION OF GENERIC NODES IN A DISTRIBUTED COMMAND AND CONTROL SYSTEM AND METHOD THEREFOR
6166766	03-Sep-17	IMAGE CAPTURING CIRCUIT
6167059	26-Jan-18	APPARATUS AND METHOD FOR TRANSMITTING DATA
6167081	03-Sep-19	DUAL MODE RECEIVER

6167484	12-May-18	METHOD AND APPARATUS FOR LEVERAGING HISTORY BITS TO OPTIMIZE MEMORY REFRESH PERFORMANCE
6169408	30-Sep-16	METHOD AND APPARATUS FOR TESTING AN INTEGRATED CIRCUIT WITH A PULSED RADIATION BEAM
6169420	10-Aug-18	OUTPUT BUFFER AND METHOD THEREFOR
6169689	08-Dec-19	MTJ STACKED CELL MEMORY SENSING METHOD AND APPARATUS
6169800	02-Jul-17	INTEGRATED CIRCUIT AMPLIFIER AND METHOD FOR ADAPTIVE OFFSET
6170736	04-May-18	SEMICONDUCTOR DIE ATTACH METHOD AND APPARATUS THEREFOR
6171114	12-Mar-19	LOW INSERTION FORCE ARRAY CONNECTOR FOR PROVIDING A REMOVABLE HIGH DENSITY ELECTRICAL INTERCONNECT TO A FLEXIBLE CIRCUIT
6171407	12-Oct-19	VENTILATION FIXTURE AND METHOD OF USING SAME
6171910	21-Jul-19	METHOD FOR FORMING A SEMICONDUCTOR DEVICE
6171959	20-Jan-18	METHOD FOR MAKING A SEMICONDUCTOR DEVICE
6172420	04-May-18	SILICON DELTA-DOPED GALLIUM ARSENIDE/INDIUM ARSENIDE HETEROJUNCTION OHMIC CONTACT
6172529	28-Sep-18	COMPOUND DOMINO LOGIC CIRCUIT HAVING OUTPUT NOISE ELIMINATION
6172905	01-Feb-20	METHOD OF OPERATING A SEMICONDUCTOR DEVICE
6174425	14-May-17	PROCESS FOR DEPOSITING A LAYER OF MATERIAL ON A SUBSTRATE AND A PLATING SYSTEM
6174737	31-Aug-18	MAGNETIC RANDOM ACCESS MEMORY AND FABRICATING METHOD THEREOF
6174810	06-Apr-18	COPPER INTERCONNECT STRUCTURE AND METHOD OF FORMATION
6175224	29-Jun-18	REGULATOR CIRCUIT HAVING A BANDGAP GENERATOR COUPLED TO A VOLTAGE SENSOR AND METHOD
6175346	24-Oct-16	DISPLAY DRIVER AND METHOD THEREOF
6176373	17-Feb-18	EMBOSSED CARRIER TAPE
6177204	06-Nov-15	FERROMAGNETIC GMR MATERIAL
6177298	03-Sep-16	ELECTROSTATIC DISCHARGE PROTECTION CIRCUIT FOR AN INTEGRATED CIRCUIT AND METHOD OF MANUFACTURE
6177354	15-Sep-17	METHOD OF ETCHING A SUBSTRATE
6177727	01-May-18	SADDLE BRACKET FOR SOLID STATE PRESSURE GAUGE
6177832	06-Jul-18	HIGH FREQUENCY DIFFERENTIAL TO SINGLE-ENDED CONVERTER
6178332	08-Mar-19	RADIO WITH HALTING APPARATUS AND METHOD
6178491	22-Mar-19	METHOD FOR STORING DATA STRUCTURES IN MEMORY USING ADDRESS POINTERS, AND APPARATUS
6178496	17-Feb-19	SYSTEM FOR CONVERTING INSTRUCTIONS, AND METHOD THEREFOR
6180495	03-Apr-18	SILICON CARBIDE TRANSISTOR AND METHOD THEREFOR
6181168	24-Sep-19	HIGH SPEED PHASE DETECTOR AND A METHOD FOR DETECTING PHASE DIFFERENCE
6181170	09-Jul-19	DRIVER HAVING SUBSTANTIALLY CONSTANT AND LINEAR OUTPUT RESISTANCE
6181757	27-Apr-18	RETIMING METHOD AND MEANS

6182104	22-Jul-18	CIRCUIT AND METHOD OF MODULO M MULTIPLICATION
6182181	29-Dec-17	COMPUTER SYSTEM WITH INTERFACE AND METHOD
6183859	20-Jul-18	LOW RESISTANCE MTJ
6184072	17-May-20	PROCESS FOR FORMING A HIGH-K GATE DIELECTRIC
6184073	23-Dec-17	PROCESS FOR FORMING A SEMICONDUCTOR DEVICE HAVING AN INTERCONNECT OR CONDUCTIVE FILM ELECTRICALLY INSULATED FROM A CONDUCTIVE MEMBER OR REGION
6185139	12-Jan-20	CIRCUIT AND METHOD FOR ENABLING SEMICONDUCTOR DEVICE BURN-IN
6185411	09-Nov-13	APPARATUS AND METHOD FOR ENABLING ELEMENTS OF A PHASE LOCKED LOOP
6185657	20-Apr-18	MULTI-WAY CACHE APPARATUS AND METHOD
6187216	27-Aug-17	METHOD FOR ETCHING A DIELECTRIC LAYER OVER A SEMICONDUCTOR SUBSTRATE
6187682	26-May-18	INERT PLASMA GAS SURFACE CLEANING PROCESS PERFORMED INSITU WITH PHYSICAL VAPOR DEPOSITION (PVD) OF A LAYER OF MATERIAL
6189061	01-Feb-19	MULTI-MASTER BUS SYSTEM PERFORMING ATOMIC TRANSACTIONS AND METHOD OF OPERATING SAME
6189133	14-May-18	A COUPLING NOISE REDUCTION TECHNIQUE USING RESET TIMING
6191973	27-Sep-19	MRAM CAM
6192089	07-Aug-18	ELECTRONIC CIRCUIT AND METHOD FOR AUTOMATIC FREQUENCY CONTROL
6192449	12-Apr-16	APPARATUS AND METHOD FOR OPTIMIZING PERFORMANCE OF A CACHE MEMORY IN A DATA PROCESSING SYSTEM
6194246	25-Aug-19	PROCESS FOR FABRICATING ELECTRONIC DEVICES HAVING A THERMALLY CONDUCTIVE SUBSTRATE
6194250	22-Mar-16	LOW-PROFILE MICROELECTRIC PACKAGE, AND METHOD FOR FORMING SAME
6195536	19-Feb-17	IMPEDANCE MATCHING FOR A DUAL BAND POWER AMPLIFIER
6197688	12-Feb-18	INTERCONNECT STRUCTURE IN A SEMICONDUCTOR DEVICE AND METHOD OF FORMATION
6198314	25-Jan-19	SAMPLE AND HOLD CIRCUIT AND METHOD THEREFOR
6200829	13-Dec-16	MICROELECTRONIC ASSEMBLY WITH CONNECTION TO A BURIED ELECTRICAL ELEMENT, AND METHOD FOR FORMING SAME
6201186	29-Jun-18	ELECTRONIC COMPONENT ASSEMBLY AND METHOD OF MAKING THE SAME
6201192	22-Nov-15	METHOD AND ASSEMBLY FOR PROVIDING IMPROVED UNDERCHIP ENCAPSULATION
6202077	24-Feb-18	SIMD DATA PROCESSING EXTENDED PRECISION ARITHMETIC OPERAND FORMAT
6202130	17-Apr-18	DATA PROCESSING SYSTEM FOR PROCESSING VECTOR DATA AND METHOD THEREFOR
6202141	16-Jun-18	METHOD AND APPARATUS FOR PERFORMING VECTOR MULTIPLICATION BY SPLITTING MULTIPLICATION OPERATION AMONG ODD AND EVEN DATA ELEMENTS
6202144	22-Mar-19	COMPUTER SYSTEM HAVING A SINGLE POINTER BRANCH INSTRUCTION AND METHOD
6204169	24-Mar-17	PROCESS FOR POLISHING DISSIMILAR CONDUCTIVE LAYERS IN A SEMICONDUCTOR DEVICE

6204513	04-May-19	HETEROSTRUCTURE INTERBAND TUNNELING DIODE
6204783	22-Mar-19	DIGITAL TO ANALOG CONVERTER HAVING A DC OFFSET CANCELLING DEVICE AND A METHOD THEREFOR
6205052	21-Oct-19	MAGNETIC ELEMENT WITH IMPROVED FIELD RESPONSE AND FABRICATING METHOD THEREOF
6205073	31-Mar-20	CURRENT CONVEYOR AND METHOD FOR READOUT OF MTJ MEMORIES
6205546	22-Mar-19	COMPUTER SYSTEM HAVING A MULTI-POINTER BRANCH INSTRUCTION AND METHOD
6208205	12-Jul-19	AMPLIFIER CIRCUIT AND METHOD FOR REDUCING NOISE THEREIN
6208211	24-Sep-19	LOW JITTER PHASE LOCKED LOOP HAVING A SIGMA DELTA MODULATOR AND A METHOD THEREOF
6209073	27-Apr-18	SYSTEM AND METHOD FOR INTERLOCKING BARRIER OPERATIONS IN LOAD AND STORE QUEUES
6209123	01-Nov-16	METHODS OF PLACING TRANSISTORS IN A CIRCUIT LAYOUT AND SEMICONDUCTOR DEVICE WITH AUTOMATICALLY PLACED TRANSISTORS
6211090	21-Mar-20	METHOD OF FABRICATING FLUX CONCENTRATING LAYER FOR USE WITH MAGNETORESISTIVE RANDOM ACCESS MEMORIES
6211530	12-Jun-18	SPARSE-CARRIER DEVICES AND METHOD OF FABRICATION
6211559	27-Feb-18	SYMMETRIC MAGNETIC TUNNEL DEVICE
6214122	17-Mar-17	RAPID THERMAL PROCESSING SUSCEPTOR
6214634	28-May-18	SENSOR DEVICE AND METHOD OF FORMING A SENSOR DEVICE
6215359	19-Feb-17	IMPEDANCE MATCHING FOR A DUAL BAND POWER AMPLIFIER
6215423	26-Aug-18	METHOD AND SYSTEM FOR A SYNCHRONOUS SAMPLE RATE CONVERSION USING A NOISE-SHAPED NUMERICALLY CONTROL OSCILLATOR
6215645	01-May-18	DIFFERENTIAL CAPACITOR STRUCTURE
6215707	10-Apr-20	CHARGE CONSERVING WRITE METHOD AND SYSTEM FOR AN MRAM
6215834	04-Aug-17	DUAL BANDWIDTH PHASE LOCKED LOOP FREQUENCY LOCK DETECTION SYSTEM AND METHOD
6216213	07-Jun-16	METHOD AND APPARATUS FOR COMPRESSION, DECOMPRESSION, AND EXECUTION OF PROGRAM CODE
6216251	30-Apr-19	ON-CHIP ERROR DETECTION AND CORRECTION SYSTEM FOR AN EMBEDDED NON-VOLATILE MEMORY ARRAY AND METHOD OF OPERATION
6217660	03-Apr-20	METHOD OF CLEANING A THROTTLE VALVE AND APPARATUS
6218200	14-Jul-20	MULTI-LAYER REGISTRATION CONTROL FOR PHOTOLITHOGRAPHY PROCESSES
6218302	21-Jul-18	METHOD FOR FORMING A SEMICONDUCTOR DEVICE
6218733	17-Apr-18	SEMICONDUCTOR DEVICE HAVING A TITANIUM-ALUMINUM COMPOUND
6221537	19-Dec-17	SEMICONDUCTOR DEVICE AND METHOD OF FORMATION
6221538	02-Mar-18	METHOD FOR AUTOMATING MANUFACTURE OF A PHOTOMASK
6221686	14-Nov-17	METHOD OF MAKING A SEMICONDUCTOR IMAGE SENSOR
6222236	30-Apr-19	PROTECTION CIRCUIT AND METHOD FOR PROTECTING A SEMICONDUCTOR DEVICE

6222420	03-Aug-18	MINIMIZING RECOVERY TIME
6222686	22-Oct-19	GRADIENT INDEX MAGNIFYING LENS
6222944	06-Apr-19	DOWN-SAMPLING MPEG IMAGE DECODER
6224669	22-Mar-19	METHOD FOR FABRICATING A SEMICONDUCTOR STRUCTURE HAVING A CRYSTALLINE ALKALINE EARTH METAL OXIDE INTERFACE WITH SILICON
6225144	19-Jan-20	A METHOD AND MACHINE FOR UNDER FILLING AN ASSEMBLY TO FORM A SEMICONDUCTOR PACKAGE
6225674	02-Apr-19	SEMICONDUCTOR STRUCTURE AND METHOD OF MANUFACTURE
6225933	29-Sep-20	DIGITAL TO ANALOG CONVERTER USING MAGNETORESISTIVE MEMORY TECHNOLOGY
6226200	17-Nov-19	IN-CIRCUIT MEMORY ARRAY BIT CELL THRESHOLD VOLTAGE DISTRIBUTION MEASUREMENT
6226556	09-Jul-18	APPARATUS WITH FAILURE RECOVERY AND METHOD THEREFORE
6226724	03-Sep-17	MEMORY CONTROLLER AND METHOD FOR GENERATING COMMANDS TO A MEMORY
6228275	10-Dec-18	METHOD OF MANUFACTURING A SENSOR
6228743	04-May-18	SEMICONDUCTOR DEVICE AND ALIGNMENT METHOD
6229097	08-Mar-16	SUBSTRATE HAVING TRIM WINDOW IN A C5 ARRAY
6229400	22-Oct-19	METHOD AND APPARATUS FOR A CALIBRATED FREQUENCY MODULATION PHASE LOCKED LOOP
6230238	02-Mar-19	METHOD AND APPARATUS FOR ACCESSING MISALIGNED DATA FROM MEMORY IN AN EFFICIENT MANNER
6231743	03-Jan-20	METHOD FOR FORMING A SEMICONDUCTOR DEVICE
6232134	24-Jan-20	METHOD AND APPARATUS FOR MONITORING WAFER CHARACTERISTICS AND/OR SEMICONDUCTOR PROCESSING CONSISTENCY USING WAFER CHARGE DISTRIBUTION MEASUREMENTS
6232235	03-Jun-18	METHOD OF FORMING A SEMICONDUCTOR DEVICE
6232634	29-Jul-18	NONVOLATILE MEMORY CELL AND METHOD FOR MANUFACTURING SAME
6232903	07-Dec-15	SEQUENCING SCHEME FOR REDUCING LOW FREQUENCY TONE GENERATION IN AN ANALOGUE OUTPUT SIGNAL
6233172	17-Dec-19	MAGNETIC ELEMENT WITH DUAL MAGNETIC STATES AND FABRICATING METHOD THEREOF
6233287	04-Apr-17	METHOD AND APPARATUS FOR MIXING SIGNALS
6235603	12-Jul-19	METHOD FOR FORMING A SEMICONDUCTOR DEVICE USING AN ETCH STOP LAYER
6236611	20-Dec-19	PEAK PROGRAM CURRENT APPARATUS AND METHOD
6237089	03-Nov-17	METHOD AND APPARATUS FOR AFFECTING SUBSEQUENT INSTRUCTION PROCESSING IN A DATA PROCESSOR
6238967	12-Apr-19	METHOD OF FORMING EMBEDDED DRAM STRUCTURE
6239636	12-May-20	DIGITAL WAVEFORM GENERATOR APPARATUS AND METHOD THEREFOR
6240024	10-Apr-20	METHOD AND APPARATUS FOR GENERATING AN ECHO CLOCK IN A MEMORY
6240479	31-Jul-18	METHOD AND APPARATUS FOR TRANSFERRING DATA ON A SPLIT BUS IN A DATA PROCESSING SYSTEM
6240490	20-Jul-18	COMPREHENSIVE MULTI-LEVEL CACHE PRELOADING

		MECHANISM IN A MULTIPROCESSING SIMULATION ENVIRONMENT
6240493	17-Apr-18	METHOD AND APPARATUS FOR PERFORMING ACCESS CENSORSHIP IN A DATA PROCESSING SYSTEM
6241821	22-Mar-19	METHOD FOR FABRICATING A SEMICONDUCTOR STRUCTURE HAVING A CRYSTALLINE ALKALINE EARTH METAL OXIDE INTERFACE WITH SILICON
6242802	17-Jul-15	MOISURE ENHANCED BALL GRID ARRAY PACKAGE
6242892	30-Apr-19	PORTABLE ELECTRONIC DEVICE AND METHOD
6242956	22-Oct-19	PHASE LOCKED LOOP
6243410	29-Mar-16	DEMULATOR HAVING AN INFINITE-DURATION IMPULSE RESPONSE FILTER WITH DYNAMIC COEFFICIENT SCALING
6243566	19-Feb-17	IMPEDANCE MATCHING FOR A DUAL BAND POWER AMPLIFIER
6243802	05-Mar-19	APPARATUS AND METHOD FOR ENCRYPTED INSTRUCTIONS
6245686	05-Jun-20	PROCESS FOR FORMING A SEMICONDUCTOR DEVICE AND A PROCESS FOR OPERATING AN APPARATUS
6246088	12-Jun-18	NON-VOLATILE MEMORY CIRCUIT AND STRUCTURE
6246123	04-May-18	TRANSPARENT COMPOUND AND APPLICATIONS FOR ITS USE
6246973	12-Aug-19	MODELING METHOD OF MOSFET
6248459	22-Mar-19	SEMICONDUCTOR STRUCTURE HAVING A CRYSTALLINE ALKALINE EARTH METAL OXIDE INTERFACE WITH SILICON
6249170	14-Apr-20	LOGARITHMIC GAIN CONTROL CIRCUIT AND METHOD
6249475	05-Apr-19	METHOD FOR DESIGNING A TILED MEMORY
6249857	20-Oct-17	APPARATUS USING A MULTIPLE INSTRUCTION REGISTER LOGARITHM BASED PROCESSOR
6251734	01-Jul-18	METHOD FOR FABRICATING TRENCH ISOLATION AND TRENCH SUBSTRATE CONTACT
6252471	29-Sep-20	PROGRAMMABLE OSCILLATOR USING MAGNETORESISTIVE MEMORY TECHNOLOGY
6252795	29-Sep-20	PROGRAMMABLE RESISTIVE CIRCUIT USING MAGNETORESISTIVE MEMORY TECHNOLOGY
6252814	29-Apr-19	DUMMY WORDLINE CIRCUITRY
6254685	03-Jul-18	CHEMICAL VAPOR DEPOSITION TRAP WITH TAPERED INLET
6254815	03-Jul-18	MOLDED PACKAGING METHOD FOR A SENSING DIE HAVING A PRESSURE SENSING DIAPHRAGM
6255204	21-May-19	METHOD FOR FORMING A SEMICONDUCTOR DEVICE
6255710	04-May-18	3-D SMART POWER IC
6256713	29-Apr-19	BUS UTILIZATION OPTIMIZATION WITH READ/WRITE COHERENCE
6257756	16-Jul-17	APPARATUS AND METHOD FOR IMPLEMENTING VITERBI BUTTERFLIES
6259279	28-Jan-20	HIGH FREQUENCY DETECTION CIRCUIT AND METHOD
6259318	28-May-19	METHOD FOR EXTENDING THE LINEAR RANGE OF AN AMPLIFIER
6259746	14-Jan-18	METHOD FOR ALLOCATING DATA AND POWER IN A DISCRETE MULTI-TONE COMMUNICATION SYSTEM
6259902	29-Oct-18	DUAL CHANNEL SUPERHETERODYNE RECEIVER
6259904	06-Oct-17	EAST SQUELCH CIRCUIT AND METHOD
6260086	22-Dec-18	CONTROLLER CIRCUIT FOR TRANSFERRING A SET OF

		PERIPHERAL DATA WORDS
6260157	16-Feb-19	PATCHING OF A READ ONLY MEMORY
6261646	20-Jul-18	METHOD OF MAKING A LOW RESISTANCE MTJ
6261868	02-Apr-19	SEMICONDUCTOR COMPONENT AND METHOD FOR MANUFACTURING THE SEMICONDUCTOR COMPONENT
6261978	22-Feb-19	PROCESS FOR FORMING A SEMICONDUCTOR DEVICE WITH THICK AND THIN FILMS
6262451	13-Mar-17	ELECTRODE STRUCTURE FOR TRANSISTORS NON-VOLATILE MEMORIES AND THE LIKE
6262461	22-Jun-18	METHOD AND APPARATUS FOR CREATING A VOLTAGE THRESHOLD IN A FET
6262462	22-Jun-18	ENHANCED DIELECTRIC CONSTANT GATE INSULATOR
6263199	14-Dec-18	CIRCUIT AND METHOD OF SIGNAL FREQUENCY CONVERSION
6263605	21-Dec-18	PAD CONDITIONER COUPLING AND END EFFECTOR FOR A CHEMICAL MECHANICAL PLANARIZATION SYSTEM AND METHOD THEREFOR
6265329	09-Mar-18	QUANTUM DEPOSITION DISTRIBUTION CONTROL
6265917	22-Oct-19	CIRCUIT AND METHOD FOR ALTERING THE FREQUENCY OF A SIGNAL
6266741	15-Jun-18	METHOD AND APPARATUS TO REDUCE SYSTEM BUS LATENCY
6266807	09-Sep-17	METHOD AND SYSTEM FOR EXECUTING INSTRUCTIONS IN A MICROPROCESSOR
6267641	19-May-20	METHOD OF MANUFACTURING A SEMICONDUCTOR COMPONENT AND CHEMICAL-MECHANICAL POLISHING SYSTEM THEREFOR
6268289	18-May-18	METHOD FOR PROTECTING THE EDGE EXCLUSION OF A SEMICONDUCTOR WAFER FROM COPPER PLATING THROUGH USE OF AN EDGE EXCLUSION MASKING LAYER
6269016	27-Sep-19	MRAM CAM
6269360	24-Apr-18	OPTIMIZATION OF ORDERED STORES ON A PIPELINED BUS VIA SELF-INITIATED RETRY
6269427	18-Mar-19	MULTIPLE LOAD MISS HANDLING IN A CACHE MEMORY SYSTEM
6270568	15-Jul-19	METHOD FOR FABRICATING A SEMICONDUCTOR STRUCTURE WITH REDUCED LEAKAGE CURRENT DENSITY
6271106	29-Oct-19	METHOD OF MANUFACTURING A SEMICONDUCTOR COMPONENT
6271143	06-May-19	METHOD FOR PREVENTING TRENCH FILL EROSION
6271699	02-Apr-19	DRIVER CIRCUIT AND METHOD FOR CONTROLLING TRANSITION TIME OF A SIGNAL
6272040	29-Sep-20	SYSTEM AND METHOD FOR PROGRAMING A MAGNETORESISTIVE MEMORY DEVICE
6272041	28-Aug-20	MTJ MRAM PARALLEL-PARALLEL ARCHITECTURE
6272588	30-May-17	METHOD AND APPARATUS FOR VERIFYING AND CHARACTERIZING DATA RETENTION TIME IN A DRAM USING BUILT-IN TEST CIRCUITRY
6272604	20-May-19	CONTINGENT RESPONSE APPARATUS AND METHOD FOR MAINTAINING CACHE COHERENCY
6272669	15-Dec-17	METHOD FOR CONFIGURING A PROGRAMMABLE SEMICONDUCTOR DEVICE
6274424	03-Nov-17	SEMICONDUCTOR DEVICE INCLUDING A DUAL INLAID STRUCTURE

6274478	13-Jul-19	A METHOD FOR FORMING A COPPER INTERCONNECT USING A MULTI-PLATEN CHEMICAL MECHANICAL POLISHING (CMP) PROCESS
6274515	14-Aug-18	SPIN-ON DIELECTRIC FOR USE IN MANUFACTURING SEMICONDUCTORS
6274899	17-Aug-18	SEMICONDUCTOR DEVICE CAPACITOR ELECTRODE
6275070	21-Sep-19	INTEGRATED CIRCUIT HAVING A HIGH SPEED CLOCK INPUT BUFFER
6275178	27-Jan-20	VARIABLE CAPACITANCE VOLTAGE SHIFTER AND AMPLIFIER AND A METHOD FOR AMPLIFYING AND SHIFTING VOLTAGE
6275522	14-Jan-18	METHOD FOR ALLOCATING DATA AND POWER IN A DISCRETE MULTI-TONE COMMUNICATION SYSTEM
6275540	01-Oct-17	SELECTIVE CALL RECEIVER HAVING AN APPARATUS FOR MODIFYING AN ANALOG SIGNAL TO A DIGITAL SIGNAL AND METHOD THEREFOR
6275835	16-Feb-19	FINITE IMPULSE RESPONSE FILTER AND METHOD
6278158	29-Dec-19	VOLTAGE VARIABLE CAPACITOR WITH IMPROVED C-V LINEARITY
6278394	21-Sep-19	A SIGNAL PROCESSING CIRCUIT AND METHOD OF OPERATION
6278631	03-Aug-18	MAGNETIC RANDOM ACCESS MEMORY ARRAY DIVIDED INTO A PLURALITY OF MEMORY BANKS
6279083	24-Mar-19	CIRCUIT AND METHOD OF CONTROLLING CACHE MEMORY
6281538	22-Mar-20	MULTI-LAYER TUNNELING DEVICE WITH A GRADED STOICHIOMETRY INSULATING LAYER
6281889	02-Mar-18	MOIRE CANCELLATION CIRCUIT
6282623	15-Aug-17	DIGITAL SIGNAL PROCESSOR AND METHOD FOR PERFORMING A DIGITAL SIGNAL PROCESSING OPERATION
6282628	24-Feb-19	METHOD AND SYSTEM FOR A RESULT CODE FOR A SINGLE-INSTRUCTION MULTIPLE-DATA PREDICATE COMPARISON OPERATION
6282681	28-Aug-18	METHOD AND APPARATUS FOR TESTING FINITE STATE MACHINE (FSM) CONFORMANCE UTILIZING UNIQUE INPUT/OUTPUT SEQUENCE (UIOS) SETS
6284616	30-Nov-18	CIRCUIT AND METHOD FOR REDUCING PARASITIC BIPOLAR EFFECTS DURING ELECTROSTATIC DISCHARGES
6284633	24-Nov-17	METHOD FOR FORMING A TENSILE PLASMA ENHANCED NITRIDE CAPPING LAYER OVER A GATE ELECTRODE
6285066	06-Apr-15	SEMICONDUCTOR DEVICE HAVING FIELD ISOLATION
6285073	04-Sep-18	CONTACT STRUCTURE AND METHOD OF FORMATION
6285214	31-Jan-20	OUTPUT BUFFER STAGE FOR USE WITH A CURRENT CONTROLLED OSCILLATOR
6285581	13-Dec-19	MRAM HAVING SEMICONDUCTOR DEVICE INTEGRATED THEREIN
6287951	07-Dec-18	PROCESS FOR FORMING A COMBINATION HARDMASK AND ANTIREFLECTIVE LAYER
6287960	08-May-20	SELF ALIGNED INLAID PATTERNING AND ETCHING
6288599	31-Aug-18	DATA PROCESSING SYSTEM HAVING AN INPUT BUFFER WHICH INTERFACES UNDER ADDRESS CONTROL WITH CIRCUITRY OPERATING WITH A DIFFERENT SUPPLY VOLTAGE VALUE
6289060	12-May-20	METHOD AND APPARATUS FOR PERFORMING A GENERALIZED VITERBI SEARCH TO DEMODULATE A SEQUENCE OF SYMBOLS
6289204	09-Jul-18	INTEGRATION OF A RECEIVER FRONT-END IN MULTILAYER

		CERAMIC IN INTEGRATED CIRCUIT TECHNOLOGY
6290491	29-Jun-20	METHOD FOR HEATING A SEMICONDUCTOR WAFER IN A PROCESS CHAMBER BY A SHOWER HEAD, AND PROCESS CHAMBER
6291319	17-Dec-19	METHOD FOR FABRICATING A SEMICONDUCTOR STRUCTURE HAVING A STABLE CRYSTALLINE INTERFACE WITH SILICON
6291888	17-Sep-16	METHOD OF MAKING A CONTACT STRUCTURE
6292034	24-Feb-20	LOW NOISE TRANSCONDUCTANCE DEVICE
6292389	19-Jul-19	MAGNETIC ELEMENT WITH IMPROVED FIELD RESPONSE AND FABRICATING METHOD THEREOF
6294405	01-Dec-17	METHOD OF FORMING SEMICONDUCTOR DEVICE HAVING A SUB-CHIP-SCALE PACKAGE STRUCTURE
6294458	31-Jan-20	SEMICONDUCTOR DEVICE ADHESIVE LAYER STRUCTURE AND PROCESS FOR FORMING STRUCTURE
6294820	23-Feb-18	A METAL OXIDE GATE ELECTRODE STACK HAVING A METALLIC GATE DIELECTRIC, METALLIC GATE ELECTRODE AND A METALLIC ARC LAYER
6294933	01-Feb-20	METHOD AND APPARATUS FOR LOW POWER DIFFERENTIAL SIGNALING TO REDUCE POWER
6294938	25-Jan-19	SYSTEM WITH DLL
6295229	29-Sep-17	SEMICONDUCTOR DEVICE AND METHODS OF FORMING AND OPERATING IT
6297095	16-Jun-20	MEMORY DEVICE THAT INCLUDES PASSIVATED NANOCLUSTERS AND METHOD FOR MANUFACTURE
6297155	03-May-19	METHOD OF FORMING A COPPER LAYER OVER A SEMICONDUCTOR LAYER
6297169	27-Jul-18	METHOD OF FORMING A SEMICONDUCTOR DEVICE USING A MASK HAVING A SELF-ASSEMBLED MONOLAYER
6297173	05-Aug-17	PROCESS FOR FORMING A SEMICONDUCTOR DEVICE
6297757	11-Feb-19	METHOD AND CIRCUIT FOR TESTING AN ANALOG-TO-DIGITAL CONVERTER
6298320	17-Feb-18	SYSTEM AND METHOD FOR TESTING AN EMBEDDED MICROPROCESSOR SYSTEM CONTAINING PHYSICAL AND/OR SIMULATED HARDWARE
6298365	24-Feb-19	METHOD AND SYSTEM FOR BOUNDS COMPARATOR
6298464	02-Dec-18	METHOD AND APPARATUS FOR MAXIMUM LIKELIHOOD SEQUENCE DETECTION
6300202	18-May-20	SELECTIVE REMOVAL OF A METAL OXIDE DIELECTRIC
6300234	26-Jun-20	PROCESS FOR FORMING AN ELECTRICAL DEVICE
6300884	08-Aug-20	METHOD FOR DECODING A QUADRATURE ENCODED SIGNAL
6301168	23-Aug-20	CMOS CELL AND CIRCUIT DESIGN FOR IMPROVED IDDQ TESTING
6301306	26-May-20	METHOD AND APPARATUS FOR GENERATING A SHORT-RANGE WIRELESS DATA COMMUNICATION LINK
6302775	22-Mar-20	APPARATUS AND METHOD FOR COLD CROSS-SECTIONING OF SOFT MATERIALS
6303978	27-Jul-20	OPTICAL SEMICONDUCTOR COMPONENT AND METHOD OF MANUFACTURE
6304140	12-Jun-20	DIGITAL PREDISTORTION FOR AMPLIFIERS
6304477	31-Jan-21	CONTENT ADDRESSABLE MAGNETIC RANDOM ACCESS MEMORY

6304843	05-Jan-19	METHOD AND APPARATUS FOR RECONSTRUCTING A LINEAR PREDICTION
6304921	07-Dec-18	SYSTEM FOR SERIAL PERIPHERAL INTERFACE WITH EMBEDDED ADDRESSING CIRCUIT FOR PROVIDING PORTION OF AN ADDRESS FOR PERIPHERAL DEVICES
6305708	29-Jun-18	AIRBAG DEPLOYMENT SYSTEM AND METHOD FOR MONITORING SAME
6307169	01-Feb-20	MICRO-ELECTROMECHANICAL SWITCH
6307298	20-Mar-20	ACTUATOR AND METHOD OF MANUFACTURE
6307452	16-Sep-19	FOLDED SPRING BASED MICRO ELECTROMECHANICAL RF SWITCH
6307782	03-Apr-20	PROCESS FOR OPERATING A SEMICONDUCTOR DEVICE
6307904	14-Jan-19	CLOCK RECOVERY CIRCUIT
6308050	23-Feb-19	DUAL BAND MOBILE PHONE HANDSET
6308308	01-Nov-16	SEMICONDUCTOR DEVICE USING DIODE PLACE-HOLDERS AND METHOD OF MANUFACTURE THEREOF
6308577	30-Sep-16	CIRCUIT AND METHOD OF COMPENSATING FOR MEMBRANE STRESS IN A SENSOR
6309908	21-Dec-19	A PACKAGE FOR AN ELECTRONIC COMPONENT AND METHOD OF MAKING IT
6309912	20-Jun-20	METHOD OF INTERCONNECTING AN EMBEDDED INTEGRATED CIRCUIT
6309918	21-Sep-18	MANUFACTURABLE GAAS VFET PROCESS
6309966	15-Aug-20	APPARATUS AND METHOD OF A LOW PRESSURE, TWO-STEP NUCLEATION TUNGSTEN DEPOSITION
6310403	31-Aug-20	METHOD OF MANUFACTURING COMPONENTS AND COMPONENT THEREOF
6310856	07-Aug-18	CDMA COMMUNICATIONS SYSTEM HAVING A SEARCHER RECEIVER AND METHOD THEREFOR
6311254	18-Mar-19	MULTIPLE STORE MISS HANDLING IN A CACHE MEMORY SYSTEM
6311327	11-Sep-15	METHOD AND APPARATUS FOR ANALYZING SOFTWARE IN A LANGUAGE-INDEPENDENT MANNER
6313024	13-Nov-18	METHOD FOR FORMING AN INTEGRATED CIRCUIT
6313567	10-Apr-20	LITHOGRAPHY CHUCK HAVING PIEZOELECTRIC ELEMENTS AND METHOD
6313664	20-Mar-20	LOAD CAPACITANCE COMPENSATED BUFFER AND METHOD THEREOF
6313774	19-May-20	DELTA-SIGMA ANALOG-TO-DIGITAL CONVERTER, AND METHOD
6314020	29-Sep-20	AN ANALOG FUNCTIONAL MODULE USING MAGNETORESISTIVE MEMORY TECHNOLOGY
6314023	15-Jun-20	NON-VOLATILE PROGRAMMING ELEMENTS FOR REDUNDANCY AND IDENTIFICATION IN AN INTEGRATED CIRCUIT
6316164	16-Mar-20	PROXIMITY EFFECT CORRECTION METHOD THROUGH UNIFORM REMOVAL OF FRACTION OF INTERIOR PIXELS
6316359	12-Feb-18	INTERCONNECT STRUCTURE IN A SEMICONDUCTOR DEVICE AND METHOD OF FORMATION
6316968	19-Oct-19	SENSE AMPLIFIER CIRCUIT
6317064	29-Feb-20	DC OFFSET CORRECTION ADAPTABLE TO MULTIPLE REQUIREMENTS
6317474	06-Aug-18	METHOD AND APPARATUS FOR ESTIMATING TIME-OF-ARRIVAL

		OF A SYNCHRONIZATION SIGNAL SENT SIMULTANEOUSLY FROM AT LEAST TWO NON-COLLOCATED TRANSMITTERS
6317806	20-May-19	APPARATUS AND METHOD FOR QUEUING INFORMATION IN A PROCESSOR
6318174	10-Dec-18	SENSOR AND METHOD OF USE
6319730	15-Jul-19	METHOD FOR FABRICATING A SEMICONDUCTOR STRUCTURE INCLUDING A METAL OXIDE INTERFACE
6320425	12-Jul-20	DUAL FET DIFFERENTIAL VOLTAGE CONTROLLED ATTENUATOR
6320784	14-Mar-20	MEMORY CELL AND METHOD FOR PROGRAMMING THEREOF
6321186	03-May-19	METHOD AND APPARATUS FOR INTEGRATED CIRCUIT DESIGN VERIFICATION
6321303	18-Mar-19	DYNAMICALLY MODIFYING QUEUED TRANSACTIONS IN A CACHE MEMORY SYSTEM
6323704	08-Aug-20	MULTIPLE VOLTAGE COMPATIBLE I/O BUFFER
6324638	31-Mar-19	PROCESSOR HAVING VECTOR PROCESSING CAPABILITY AND METHOD FOR EXECUTING A VECTOR INSTRUCTION IN A PROCESSOR
6326228	25-Mar-16	SENSOR AND METHOD OF FABRICATI ON
6326301	13-Jul-19	A METHOD FOR FORMING A DUAL INLAID COPPER INTERCONNECT STRUCTURE
6326554	23-Mar-20	SURFACE MOUNT FLEXIBLE INTERCONNECT AND COMPONENT CARRIER
6326811	10-Aug-18	OUTPUT BUFFER AND METHOD THEREFOR
6327126	28-Jan-20	ELECTROSTATIC DISCHARGE CIRCUIT
6327182	29-Jun-19	SEMICONDUCTOR DEVICE AND A METHOD OF OPERATING THE SAME
6327313	29-Dec-19	METHOD AND APPARATUS FOR DC OFFSET CORRECTION
6327319	06-Nov-18	PHASE DETECTOR WITH FREQUENCY STEERING
6327647	05-Sep-17	METHOD AND APPARATUS FOR INTERFACING A PROCESSOR TO A COPROCESSOR
6327651	08-Sep-18	WIDE SHIFTING IN THE VECTOR PERMUTE UNIT
6329692	30-Nov-18	CIRCUIT AND METHOD FOR REDUCING PARASITIC BIPOLAR EFFECTS DURING ELECTROSTATIC DISCHARGES
6330184	01-Feb-20	METHOD OF OPERATING A SEMICONDUCTOR DEVICE
6330234	27-Apr-20	METHOD AND APPARATUS FOR REDUCING CURRENT CONSUMPTION
6331943	28-Aug-20	MTJ MRAM SERIES-PARALLEL ARCHITECTURE
6332179	19-Aug-19	ALLOCATION FOR BACK-TO-BACK MISSES IN A DIRECTORY BASED CACHE
6334176	17-Apr-18	METHOD AND APPARATUS FOR GENERATING ELEMENTS OF A SECOND VECTOR FROM A FIRST VECTOR BY GENERATING VECTOR ELEMENTS OF THE SECOND VECTOR FROM A TABLE WITH PRESELECTED OFFSET
6334929	01-Jan-19	PLASMA PROCESSING METHOD
6342411	03-Sep-19	ELECTRONIC COMPONENT AND METHOD FOR MANUFACTURE
6343354	07-Jun-16	METHOD AND APPARATUS FOR COMPRESSION, DECOMPRESSION, AND EXECUTION OF PROGRAM CODE
6344403	16-Jun-20	MEMORY DEVICE AND METHOD FOR MANUFACTURE
6344413	22-Dec-17	METHOD OF FORMING A SEMICONDUCTOR DEVICE

6345589	29-Mar-16	METHOD AND APPARATUS FOR FORMING A BOROPHOSPHOSILICATE FILM
6346469	03-Jan-20	SEMICONDUCTOR DEVICE AND A PROCESS FOR FORMING THE SEMICONDUCTOR DEVICE
6346829	31-Aug-18	HIGH VOLTAGE INPUT BUFFER MADE BY A LOW VOLTAGE PROCESS AND HAVING A SELF-ADJUSTING TRIGGER POINT
6346832	22-May-20	MULTI-CHANNEL SIGNALING
6346880	20-Dec-19	CIRCUIT AND METHOD FOR CONTROLLING AN ALARM
6346901	22-Dec-19	DIGITAL-TO-ANALOG CONVERTING CIRCUIT
6346908	03-Aug-18	APPARATUS FOR CONVERTING AN ANALOG SIGNAL UTILIZING RESISTOR D/A CONVERTER PRECHARGING
6346968	02-Sep-17	IMAGING CIRCUIT AND METHOD FOR COLOR BALANCING PIXEL SIGNALS
6347056	16-May-21	RECORDING OF RESULT INFORMATION IN A BUILT-IN SELF-TEST CIRCUIT AND METHOD THEREFOR
6348386	16-Apr-21	METHOD FOR MAKING A HAFNIUM-BASED INSULATING FILM
6348806	18-Mar-19	METHOD AND APPARATUS FOR MEASURING GATE LEAKAGE CURRENT IN A N INTEGRATED CIRCUIT
6348820	17-Jul-20	HIGH-SIDE, LOW-SIDE CONFIGURABLE DRIVER
6349196	24-Mar-19	COMMUNICATIONS SYSTEM, MOBILE DEVICE AND METHOD
6350690	09-Apr-19	PROCESS FOR ACHIEVING FULL GLOBAL PLANARIZATION DURING CMP OF DAMASCENE SEMICONDUCTOR STRUCTURES
6350954	24-Jan-20	ELECTRONIC DEVICE PACKAGE, AND METHOD
6351020	12-Nov-19	LINEAR CAPACITOR STRUCTURE IN A CMOS PROCESS
6351246	03-May-20	PLANAR ULTRA WIDE BAND ANTENNA WITH INTEGRATED ELECTRONICS
6351293	13-Jul-18	DECISION DIRECTED PHASE DETECTOR
6351409	04-Jan-21	MRAM WRITE APPARATUS AND METHOD
6352192	29-Feb-20	SYSTEM AND METHOD TO CONTROL SOLDER REFLOW FURNACE WITH WAFER SURFACE CHARACTERIZATION
6352874	24-May-19	METHOD OF MANUFACTURING A SENSOR
6353296	15-Oct-19	ELECTRONIC DRIVER CIRCUIT WITH MULTIPLEXER FOR ALTERNATIVELY DRIVING A LOAD FOR A BUSLINE, AND METHOD
6355384	06-Mar-20	A MASK, ITS METHOD OF FORMATION, AND A SEMICONDUCTOR DEVICE MADE THEREBY
6355541	21-Apr-19	METHOD FOR TRANSFER OF THIN-FILM OF SILICON CARBIDE VIA IMPLANTATION AND WAFER BONDING
6355550	19-May-20	ULTRA-LATE PROGRAMMING ROM AND METHOD OF MANUFACTURE
6355994	05-Apr-20	PRECISION STAGE
6356142	20-Sep-20	DIGITAL FILTER TUNE LOOP
6356217	29-Feb-20	ENHANCED DC OFFSET CORRECTION THROUGH BANDWIDTH AND CLOCK SPEED SELECTION
6356594	02-Nov-19	DATA CONVERTER
6356636	22-Jul-18	CIRCUIT AND METHOD FOR FAST MODULAR MULTIPLICATION
6358430	28-Jul-19	TECHNIQUE FOR ETCHING OXIDES AND/OR INSULATORS
6358816	05-Sep-20	METHOD FOR UNIFORM POLISH IN MICROELECTRONIC DEVICE
6359294	04-Mar-17	INSULATOR COMPOUND SEMICONDUCTOR INTERFACE

		STRUCTURE AND METHODS OF FABRICATION
6359458	24-Jun-18	APPARATUS FOR DETECTING A DIAPHRAGM FAILURE
6360243	10-Mar-18	METHOD, DEVICE AND ARTICLE OF MANUFACTURE FOR IMPLEMENTING A REAL-TIME TASK SCHEDULING ACCELERATOR
6361675	01-Dec-19	METHOD OF MANUFACTURING A SEMICONDUCTOR COMPONENT AND PLATING TOOL THEREFOR
6362018	02-Feb-20	MEMS VARIABLE CAPACITOR WITH STABILIZED ELECTROSTATIC DRIVE AND METHOD THEREFOR
6362057	26-Oct-19	METHOD FOR FORMING A SEMICONDUCTOR DEVICE
6362071	05-Apr-20	METHOD FOR FORMING A SEMICONDUCTOR DEVICE WITH AN OPENING IN A DIELECTRIC LAYER
6362089	19-Apr-19	METHOD FOR PROCESSING A SEMICONDUCTOR SUBSTRATE HAVING A COPPER SURFACE DISPOSED THEREON AND STRUCTURE FORMED
6362098	28-Feb-21	PLASMA-ENHANCED CHEMICAL VAPOR DEPOSITION (CVD) METHOD TO FILL A TRENCH IN A SEMICONDUCTOR SUBSTRATE
6362516	03-Apr-17	ELECTRONIC APPARATUS
6362770	12-Sep-20	DUAL INPUT SWITCHED CAPACITOR GAIN STAGE
6365419	28-Aug-20	HIGH DENSITY MRAM CELL ARRAY
6365474	12-Jul-20	SEMICONDUCTOR DEVICE AND METHOD
6365947	27-Apr-20	SEMICONDUCTOR DEVICE AND METHOD OF INCREASING CHANNEL LENGTH TO ELIMINATE SHORT CHANNEL EFFECTS OF CORNER DEVICES
6366157	14-Oct-16	METHODS AND CIRCUITS FOR SYNTHETICALLY ADJUSTING A SUPPLY VOLTAGE AND/OR A FREQUENCY OF A CLOCK SIGNAL IN A DIGITAL CIRCUIT
6366768	09-Nov-18	CIRCUIT AND METHOD OF FREQUENCY SYNTHESIZER CONTROL WITH A SERIAL PERIPHERAL INTERFACE
6366786	08-Mar-19	RADIO WITH SYNCHRONIZATION APPARATUS AND METHOD THEREFOR
6366865	03-Nov-19	APPARATUS AND METHOD FOR ESTIMATING THE COIL RESISTANCE AND ELECTRIC MOTOR
6368752	29-Oct-16	LOW STRESS HARD MASK FORMATION METHOD DURING REFRACTORY RADIATION MASK FABRICATION
6368924	31-Oct-20	AMORPHOUS CARBON LAYER FOR IMPROVED ADHESION OF PHOTORESIST AND METHOD OF FABRICATION
6368929	17-Aug-20	METHOD OF MANUFACTURING A SEMICONDUCTOR COMPONENT AND SEMICONDUCTOR COMPONENT THEREOF
6369345	18-Aug-20	METHOD AND APPARATUS FOR REFLOWING SOLDER PASTE USING A LIGHT SOURCE
6369430	02-Apr-21	METHOD OF PREVENTING TWO NEIGHBORING CONTACTS FROM A SHORT-CIRCUIT CAUSED BY A VOID BETWEEN THEM AND DEVICE HAVING THE SAME
6369647	21-Dec-20	DEMODULATOR CIRCUIT AND METHOD OF TUNING
6369742	01-Nov-20	SELECTIVE OVER-RANGING IN FOLDING AND AVERAGING INTEGRATED CIRCUITS
6370211	05-Feb-19	METHOD AND APPARATUS FOR FACILITATING RECEPTION OF A SIGNAL ON ONE OF A PLURALITY OF CONTIGUOUS CHANNELS
6372414	12-Mar-19	LIFT-OFF PROCESS FOR PATTERNING FINE METAL LINES
6372622	26-Oct-19	FINE PITCH BUMPING WITH IMPROVED DEVICE STANDOFF AND

		BUMP VOLUME
6372638	18-Feb-17	A METHOD FOR FORMING A CONDUCTIVE PLUG BETWEEN CONDUCTIVE LAYERS OF AN INTEGRATED CIRCUIT
6372665	25-Jun-18	METHOD FOR FORMING A SEMICONDUCTOR DEVICE
6373104	18-Feb-17	CIRCUIT AND METHOD FOR REDUCING PARASITIC BIPOLAR EFFECTS DURING ELECTROSTATIC DISCHARGES
6373139	06-Oct-19	LAYOUT FOR A BALL GRID ARRAY
6373271	29-Dec-19	SEMICONDUCTOR FRONT SIDE PRESSURE TESTING SYSTEM AND METHOD THEREFOR
6373299	04-Dec-20	ELECTRIC DRIVER CIRCUIT AND METHOD
6374203	22-Jun-18	METHOD OF MODELING CIRCUIT CELLS WITH DISTRIBUTED SERIAL LOADS
6374280	15-Sep-18	COMPUTATIONALLY EFFICIENT INVERSE DISCRETE COSINE TRANSFORM METHOD AND APPARATUS
6375549	17-Mar-20	POLISHING HEAD FOR WAFER, AND METHOD FOR POLISHING
6376260	19-Jul-19	MAGNETIC ELEMENT WITH IMPROVED FIELD RESPONSE AND FABRICATING METHOD THEREOF
6376349	19-Jan-20	PROCESS FOR FORMING A SEMICONDUCTOR DEVICE AND A CONDUCTIVE STRUCTURE
6376371	31-Mar-17	METHOD OF FORMING A SEMICONDUCTOR DEVICE
6377125	15-Mar-21	DISTRIBUTED AMPLIFIER HAVING SEPARATELY BIASED SECTIONS
6378022	17-Jun-19	METHOD AND APPARATUS FOR PROCESSING INTERRUPTIBLE MULTI-CYCLE INSTRUCTIONS
6378112	14-Jun-19	VERIFICATION OF DESIGN BLOCKS AND METHOD OF EQUIVALENCE CHECKING OF MULTIPLE DESIGN VIEWS
6379744	05-Feb-16	METHOD FOR COATING AN INTEGRATED CIRCUIT SUBSTRATE
6380760	13-Jul-18	INTEGRATED CIRCUIT FOR HANDLING BUFFER CONTENTION AND METHOD THEREOF
6380811	16-Feb-21	SIGNAL GENERATOR AND METHOD
6381224	31-Mar-19	METHOD AND APPARATUS FOR CONTROLLING A FULL-DUPLEX COMMUNICATION SYSTEM
6381656	10-Mar-19	METHOD AND APPARATUS FOR MONITORING INPUT/OUTPUT ("I/O") PERFORMANCE IN I/O PROCESSORS
6381739	15-May-16	METHOD AND APPARATUS FOR HIERARCHICAL RESTRUCTURING OF COMPUTER CODE
6383873	18-May-20	PROCESS FOR FORMING A STRUCTURE
6383885	27-Oct-19	BIPOLAR TRANSISTOR WITH IMPROVED REVERSE BREAKDOWN CHARACTERISTICS
6384353	01-Feb-20	MICRO-ELECTROMECHANICAL SYSTEM DEVICE
6384467	06-Jun-17	METHOD FOR FORMING A CAVITY CAPABLE OF ACCESSING DEEP FUSE STRUCTURES AND DEVICE CONTAINING THE SAME
6384641	04-Jun-21	SIGNAL SAMPLING CIRCUIT WITH HIGH FREQUENCY NOISE IMMUNITY AND METHOD THEREFOR
6385021	10-Apr-20	ELECTROSTATIC DISCHARGE (ESD) PROTECTION CIRCUIT
6385101	01-Mar-19	PROGRAMMABLE DELAY CONTROL FOR SENSE AMPLIFIERS IN A MEMORY
6385109	30-Jan-21	REFERENCE VOLTAGE GENERATOR FOR MRAM AND METHOD
6387787	02-Mar-21	LITHOGRAPHIC TEMPLATE AND METHOD OF FORMATION AND

		USE
6389489	17-Mar-19	DATA PROCESSING SYSTEM HAVING A FIFO BUFFER WITH VARIABLE THRESHOLD VALUE BASED ON INPUT AND OUTPUT DATA RATES AND DATA BLOCK SIZE
6389516	20-May-19	INTERVENTION ORDERING IN A MULTIPROCESSOR SYSTEM
6389706	17-Aug-20	WAFER CONTAINER HAVING ELECTRICALLY CONDUCTIVE KINEMATIC COUPLING GROOVE, SUPPORT SURFACE WITH ELECTRICALLY CONDUCTIVE KINEMATIC COUPLING PIN, TRANSPORTATION SYSTEM, AND METHOD
6389707	17-Aug-20	WAFER CONTAINER HAVING ELECTRICALLY CONDUCTIVE KINEMATIC COUPLING GROOVE TO DETECT THE PRESENCE OF THE WAFER CONTAINER ON A SUPPORT SURFACE, THE SUPPORT SURFACE, AND METHOD
6390755	06-Apr-20	EXHAUST DEVICE FOR USE IN A CLEANROOM, CLEANROOM AND METHOD
6391762	12-Nov-19	METHOD OF FORMING A MICROELECTRIC ASSEMBLY WITH A PARTICULATE FREE UNDERFILL MATERIAL AND A MICROELECTRONIC ASSEMBLY INCORPORATING THE SAME
6391800	12-Nov-19	METHOD FOR PATTERNING A SUBSTRATE WITH PHOTORESIST
6392257	10-Feb-20	SEMICONDUCTOR STRUCTURE, SEMICONDUCTOR DEVICE, COMMUNICATING DEVICE, INTEGRATED CIRCUIT, AND PROCESS FOR FABRICATING THE SAME
6392558	13-Aug-18	SYSTEM FOR ADDRESS INITIALIZATION OF GENERIC NODES IN A DISTRIBUTED COMMAND AND CONTROL SYSTEM AND METHOD THEREFOR
6392923	27-Feb-21	MAGNETORESISTIVE MIDPOINT GENERATOR AND METHOD
6393071	15-Jan-19	CIRCUIT AND METHOD OF IDENTIFYING A BURST FREQUENCY
6395053	28-May-19	METHOD OF FORMING METAL COLLOIDS, METAL COLLOIDS AND METHOD OF FORMING A METAL OXIDE SENSITIVE LAYER FOR A CHEMICAL SENSOR DEVICE
6395595	22-Mar-20	MULTI-LAYER TUNNELING DEVICE WITH A GRADED STOICHIOMETRY INSULATING LAYER
6396158	29-Jun-19	SEMICONDUCTOR DEVICE AND A PROCESS FOR DESIGNING A MASK
6397241	18-Dec-18	MULTIPLIER CELL AND METHOD OF COMPUTING
6400218	29-Feb-20	VARIABLE GAIN CONTROL CIRCUIT AND METHOD
6400610	05-Jul-20	MEMORY DEVICE INCLUDING ISOLATED STORAGE ELEMENTS THAT UTILIZE HOLE CONDUCTION AND METHOD THEREFOR
6400821	29-May-18	DIGITAL TONE GENERATOR
6401196	19-Jun-18	DATA PROCESSING SYSTEM HAVING BRANCH CONTROL AND METHOD THEREOF
6401536	11-Feb-20	ACCELERATION SENSOR AND METHOD OF MANUFACTURE
6401545	25-Jan-20	MICRO ELECTRO-MECHANICAL SYSTEM SENSOR WITH SELECTIVE ENCAPSULATION AND METHOD THEREFOR
6404283	18-Aug-20	METHOD AND APPARATUS FOR AMPLIFYING A RADIO FREQUENCY SIGNAL
6404912	01-Jun-18	METHOD AND APPARATUS FOR VISUALLY INSPECTING AN OBJECT
6405160	03-Aug-18	MEMORY COMPILER INTERFACE AND METHODOLOGY
6406555	01-Jul-17	POINT OF USE DILUTION TOOL AND METHOD
6406791	07-Sep-20	MULTIPHASE DIALECTRIC COMPOSITION AND MULTILAYERED DEVICE INCORPORATING THE SAME

6406926	25-Aug-21	METHOD OF FORMING A VACUUM MICRO-ELECTRONIC DEVICE
6406976	18-Sep-20	SEMICONDUCTOR DEVICE AND PROCESS FOR FORMING THE SAME
6407615	14-Apr-20	TEMPERATURE COMPENSATION CIRCUIT AND METHOD OF COMPENSATING
6407629	30-Jun-20	SAMPLE AND HOLD DEMODULATOR CIRCUIT
6407634	16-Jun-20	LINEAR ENVELOPE TRACKING RF POWER AMPLIFIER WITH ADAPTIVE ANALOG SIGNAL PROCESSING
6408023	17-Jun-19	METHOD AND APPARATUS FOR PERFORMING EQUALISATION IN A RADIO RECEIVER
6408361	02-Sep-19	AUTONOMOUS, WAY SPECIFIC TAX UPDATE
6410861	03-Dec-19	LOW PROFILE INTERCONNECT STRUCTURE
6410941	30-Jun-20	RECONFIGURABLE SYSTEMS USING HYBRID INTEGRATED CIRCUITS WITH OPTICAL PORTS
6411116	31-Aug-14	A METHOD FOR TESTING A PRODUCT INTEGRATED CIRCUIT WAFER USING A STIMULUS INTEGRATED CIRCUIT WAFER
6411226	16-Jan-21	HUFFMAN DECODER WITH REDUCED MEMORY SIZE
6411232	30-Sep-19	METHOD AND SYSTEM FOR DETERMINING AN ELEMENT CONVERSION CHARACTERISTIC CONTEMPORANEOUS WITH CONVERTING AN INPUT SIGNAL IN A SIGNAL CONVERTER
6411758	31-Jan-20	METHOD AND APPARATUS FOR ALIGNING A WAVEGUIDE TO A DEVICE
6412103	22-Jul-19	ROUTING METHOD REMOVING CYCLES IN VERTICAL CONSTRAINT GRAPH
6413806	23-Feb-20	SEMICONDUCTOR DEVICE AND METHOD FOR PROTECTING SUCH DEVICE FROM A REVERSED DRAIN VOLTAGE
6413819	16-Jun-20	MEMORY DEVICE AND METHOD FOR USING PREFABRICATED ISOLATED STORAGE ELEMENTS
6413878	30-Jul-18	METHOD OF MANUFACTURING ELECTRONIC COMPONENTS
6414562	27-May-17	CIRCUIT AND METHOD FOR IMPEDANCE MATCHING
6414613	05-Jan-20	APPARATUS FOR NOISESHAPING A PULSE WIDTH MODULATION (PWM) SIGNAL AND METHOD THEREFOR
6414719	26-May-20	MOTION ADAPTIVE MEDIAN FILTER FOR INTERFACE TO PROGRESSIVE SCAN CONVERSION
6415362	29-Apr-19	METHOD AND SYSTEM FOR WRITE-THROUGH STORES OF VARYING SIZES
6417077	07-Feb-20	EDGE GROWTH HETEROEPITAXY PROCESSES WITH REDUCED LATTICE MISMATCH STRAIN BETWEEN A DEPOSITED SEMICONDUCTOR MATERIAL AND A SEMICONDUCTOR SUBSTRATE
6418016	18-Jan-21	SYSTEM AND METHOD FOR COOLING USING AN OSCILLATORY IMPINGING JET
6418029	28-Feb-20	INTERCONNECT SYSTEM HAVING VERTICALLY MOUNTED PASSIVE COMPONENTS ON AN UNDERSIDE OF A SUBSTRATE
6418046	30-Jan-21	MRAM ARCHITECTURE AND SYSTEM
6418489	25-Oct-19	DIRECT MEMORY ACCESS CONTROLLER AND METHOD THEREFOR
6418527	13-Oct-18	DATA PROCESSOR INSTRUCTION SYSTEM FOR GROUPING INSTRUCTION WITH OR WITHOUT A COMMON PREFIX AND DATA PROCESSING SYSTEM THAT USES TWO OR MORE INSTRUCTION GROUPING METHODS
6419567	14-Aug-20	RETAINING RING FOR CHEMICAL-MECHANICAL POLISHING

		(CMP) HEAD, POLISHING APPARATUS, SLURRY CYCLE SYSTEM, AND METHOD
6420098	12-Jul-20	METHOD AND SYSTEM FOR MANUFACTURING SEMICONDUCTOR DEVICES ON A WAFER
6420208	14-Sep-20	AN ALTERNATIVE GROUND CONTACT FOR A SEMICONDUCTOR DIE
6420243	04-Dec-20	A METHOD FOR PRODUCING SOI WAFERS BY DELAMINATION
6420247	10-Apr-20	METHOD OF FORMING STRUCTURES ON A SEMICONDUCTOR
6420923	12-Jul-20	LOW SUPPLY CURRENT CONTROLLED FET P1 ATTENUATOR
6421077	15-Jul-18	METHOD AND APPARATUS FOR DETECTING CO-CHANNEL INTERFERENCE AND SELECTIVELY FILTERING THE INTERFERENCE WHEN DETECTED
6421104	22-Oct-19	FRONT ILLUMINATOR FOR A LIQUID CRYSTAL DISPLAY AND METHOD OF MAKING SAME
6421744	25-Oct-19	DIRECT MEMORY ACCESS CONTROLLER AND METHOD THEREFOR
6423619	30-Nov-21	TRANSISTOR METAL GATE STRUCTURE THAT MINIMIZES NON-PLANARITY EFFECTS AND METHOD OF FORMATION
6423632	21-Jul-20	SEMICONDUCTOR DEVICE AND A PROCESS FOR FORMING
6423638	28-Sep-19	FILTER APPARATUS AND METHOD THEREFOR
6423961	24-Jan-20	PIXEL READOUT SWITCHED CAPACITOR BUFFER CIRCUIT AND METHOD THEREFOR
6423991	01-Jun-18	FIELD EFFECT TRANSISTOR AND METHOD OF MAKING
6424238	08-Jan-21	ACOUSTIC WAVE FILTER AND METHOD OF FORMING THE SAME
6424825	16-Nov-19	FEEDFORWARD AND FEEDBACK CONTROL IN A RADIO
6426239	02-Feb-18	SEMICONDUCTOR COMPONENT AND METHOD OF MANUFACTURE
6426683	09-Nov-19	INTEGRATED FILTER WITH IMPROVED I/O MATCHING AND METHOD OF FABRICATION
6426698	19-May-20	LOT SIGNALLING DEVICE
6427066	30-Jun-20	APPARATUS AND METHOD FOR EFFECTING COMMUNICATIONS AMONG A PLURALITY OF REMOTE STATIONS
6429030	09-Dec-17	METHOD AND APPARATUS FOR TESTING A SEMICONDUCTOR DIE
6429046	13-Jul-20	FLIP CHIP DEVICE AND METHOD OF MANUFACTURE
6429103	13-Apr-20	MOCVD-GROWN EMODE HIGFET BUFFER
6429531	18-Apr-20	METHOD AND APPARATUS FOR MANUFACTURING AN INTERCONNECT STRUCTURE
6430084	27-Aug-21	MAGNETO-ELECTRONIC COMPONENT
6430085	27-Aug-21	MAGNETO-ELECTRONIC COMPONENT AND METHOD OF MANUFACTURE
6430234	30-Jul-18	METHOD AND APPARATUS FOR PERFORMING PHASE DETECTION AND TIMING RECOVERY FOR A VESTIGIAL SIDEBAND RECEIVER
6430243	31-Aug-18	SYMBOL SIGN DIRECTED PHASE DETECTOR
6430593	10-Mar-18	METHOD, DEVICE AND ARTICLE OF MANUFACTURE FOR EFFICIENT TASK SCHEDULING IN A MULTI-TASKING PREEMPTIVE PRIORITY-BASED REAL-TIME OPERATING SYSTEM
6430666	24-Aug-18	LINKED LIST MEMORY AND METHOD THEREFOR

6432546	24-Jul-20	MICROELECTRONIC PIEZOELECTRIC STRUCTURE AND METHOD OF FORMING THE SAME
6432779	18-May-20	SELECTIVE REMOVAL OF A METAL OXIDE DIELECTRIC
6433382	13-Aug-19	SPLIT-GATE VERTICALLY ORIENTATED EEPROM DEVICE AND PROCESS
6433568	08-Jun-20	MASSIVE PARALLEL SEMICONDUCTOR MANUFACTURING TEST PROCESS
6433571	30-Nov-15	PROCESS FOR TESTING A SEMICONDUCTOR DEVICE
6433626	16-Jan-21	CURRENT MODE FILTER WITH COMPLEX ZEROS
6433640	25-May-21	METHODS AND APPARATUS FOR AMPLIFYING A TELECOMMUNICATION SIGNAL
6434698	22-Dec-19	MICROPROCESSOR MODULE AND METHOD THEREFOR
6434707	07-Jun-19	LOW JITTER CLOCK GENERATION WITHOUT SINE LOOK-UP TABLES
6434721	03-Apr-20	METHOD AND APPARATUS FOR CONSTRAINT GRAPH BASED LAYOUT COMPACTION FOR INTEGRATED CIRCUITS
6436300	30-Jul-18	METHOD OF MANUFACTURING ELECTRONIC COMPONENTS
6436730	17-Sep-17	MICROELECTRONIC PACKAGE COMPRISING TIN-COPPER BUMP INTERCONNECTIONS, AND METHOD FOR FORMING SAME
6438030	15-Aug-20	NON-VOLATILE MEMORY, METHOD OF MANUFACTURE, AND METHOD OF PROGRAMMING
6440805	29-Feb-20	METHOD OF FORMING A SEMICONDUCTOR DEVICE WITH ISOLATION AND WELL REGIONS
6441449	02-Feb-20	MEMS VARIABLE CAPACITOR WITH STABILIZED ELECTROSTATIC DRIVE AND METHOD THEREFOR
6441594	27-Apr-21	LOW POWER VOLTAGE REGULATOR WITH IMPROVED ON-CHIP NOISE ISOLATION
6441688	27-Oct-20	SINGLE-TO-DIFFERENTIAL BUFFER AMPLIFIER
6444398	30-Nov-18	METHOD FOR MANUFACTURING A SEMICONDUCTOR WAFER USING A MASK THAT HAS SEVERAL REGIONS WITH DIFFERENT SCATTERING ABILITY
6444512	08-Sep-20	DUAL METAL GATE TRANSISTORS FOR CMOS PROCESS
6444545	19-Dec-20	DEVICE STRUCTURE FOR STORING CHARGE AND METHOD THEREFORE
6444563	22-Feb-19	METHOD AND APPARATUS FOR EXTENDING FATIGUE LIFE OF SOLDER JOINTS IN A SEMICONDUCTOR DEVICE
6444569	13-Jul-19	A METHOD FOR FORMING A COPPER INTERCONNECT USING A MULTI-PLATEN CHEMICAL MECHANICAL POLISHING (CMP) PROCESS
6445612	27-Aug-21	MRAM WITH MIDPOINT GENERATOR REFERENCE AND METHOD FOR READOUT
6445790	29-May-18	DIGITAL TONE GENERATOR
6448192	16-Apr-21	METHOD FOR FORMING A HIGH DIELECTRIC CONSTANT MATERIAL
6448736	16-Apr-21	METHOD FOR CONTROLLING SWITCHED RELUCTANCE MOTOR, AND CONTROLLER
6449195	28-Sep-20	METHOD AND APPARATUS FOR COUPLING NOISE REDUCTION IN A SEMICONDUCTOR DEVICE
6449394	09-Jul-19	PACKING VARIABLE-LENGTH CODE BITS AT FIXED POSITIONS
6449675	29-Jun-19	MULTIFIELD REGISTER HAVING A SELECTION FIELD FOR SELECTING A SOURCE OF AN INFORMATION FIELD

6449738	03-Dec-18	AN APPARATUS FOR BUS FREQUENCY INDEPENDENT WRAP I/O TESTING AND METHOD THEREFOR
6451127	01-Jun-19	CONDUCTIVE PASTE AND SEMICONDUCTOR COMPONENT HAVING CONDUCTIVE BUMPS MADE FROM THE CONDUCTIVE PASTE
6451181	02-Mar-19	METHOD FOR FORMING A SEMICONDUCTOR DEVICE BARRIER LAYER
6451627	07-Sep-19	SEMICONDUCTOR DEVICE AND PROCESS FOR MANUFACTURING AND PACKAGING A SEMICONDUCTOR DEVICE
6451681	04-Oct-19	METHOD OF FORMING COPPER INTERCONNECTION UTILIZING ALUMINUM CAPPING FILM
6452205	28-Jun-19	SPARSE-CARRIER DEVICES AND METHOD OF FABRICATION
6452284	22-Jun-20	SEMICONDUCTOR DEVICE SUBSTRATE AND A PROCESS FOR ALTERING A SEMICONDUCTOR DEVICE
6452445	15-Jun-20	VOLTAGE CONTROLLED VARIABLE GAIN ELEMENT
6452823	01-Apr-21	NON-VOLATILE MAGNETIC CACHE MEMORY AND METHOD OF USE
6452907	15-Oct-18	METHOD FOR MONITORING UNUSED BINS IN A DISCRETE MULTI-TONE COMMUNICATION SYSTEM
6453749	28-Oct-19	PHYSICAL SENSOR COMPONENT
6456746	26-Jan-19	METHOD OF MEMORY UTILIZATION IN A PREDICTIVE VIDEO DECODER
6458622	06-Jul-19	STRESS COMPENSATION COMPOSITION AND SEMICONDUCTOR COMPONENT FORMED USING THE STRESS COMPENSATION COMPOSITION
6459156	22-Dec-19	SEMICONDUCTOR DEVICE, A PROCESS FOR A SEMICONDUCTOR DEVICE, AND A PROCESS FOR MAKING A MASKING DATABASE
6459325	11-Apr-21	OUTPUT BUFFER HAVING A PRE-DRIVER TRANSITION CONTROLLER
6459344	19-Mar-21	SWITCH ASSEMBLY AND METHOD OF FORMING THE SAME
6461898	02-Oct-17	TWO STEP WIRE BOND PROCESS
6461914	29-Aug-21	PROCESS FOR MAKING A MIM CAPACITOR
6461925	30-Mar-20	METHOD OF MANUFACTURING A HETEROJUNCTION BICMOS INTEGRATED CIRCUIT
6462360	06-Aug-21	INTEGRATED GALLIUM ARSENIDE COMMUNICATION SYSTEMS
6462789	26-Mar-19	CIRCUIT AND METHOD FOR GENERATING CHROMINANCE LOCK
6463453	12-Jan-18	LOW POWER PIPELINED MULTIPLY/ACCUMULATOR WITH MODIFIED BOOTH'S RECORDER
6463549	04-Jan-21	A DEVICE AND METHOD FOR PATCHING CODE RESIDING ON A READ ONLY MEMORY MODULE
6465281	30-Sep-20	METHOD OF MANUFACTURING A SEMICONDUCTOR WAFER LEVEL PACKAGE
6465297	08-Mar-21	METHOD OF MANUFACTURING A SEMICONDUCTOR COMPONENT AND SEMICONDUCTOR COMPONENT THEREOF
6465320	03-Jul-20	ELECTRONIC COMPONENT AND METHOD OF MANUFACTURING
6465743	15-Oct-19	MULTI-STRAND SUBSTRATE FOR BALL-GRID ARRAY ASSEMBLIES AND METHOD
6465853	08-May-21	METHOD FOR MAKING A SEMICONDUCTOR DEVICE
6468017	21-Nov-20	VEHICLE, SYSTEM AND METHOD FOR LOADING AND

		UNLOADING
6469536	17-Nov-20	A METHOD AND DEVICE FOR TESTING AN INTEGRATED CIRCUIT
6469926	22-Mar-20	MAGNETIC ELEMENT WITH AN IMPROVED MAGNETORESISTANCE RATIO AND FABRICATING METHOD THEREOF
6471435	05-Apr-20	FLEXURAL JOINT
6472237	26-Oct-21	METHOD AND SYSTEM FOR DETERMINING A THICKNESS OF A LAYER
6472243	09-Jan-21	METHOD OF FORMING AN INTEGRATED CMOS CAPACITIVE PRESSURE SENSOR
6472276	20-Jul-21	USING SILICATE LAYERS FOR COMPOSITE SEMICONDUCTOR STRUCTURES
6472278	02-Sep-18	METHOD AND APPARATUS FOR CREATING A VOLTAGE THRESHOLD IN A FET
6472291	27-Jan-20	PLANARIZATION PROCESS TO ACHIEVE IMPROVED UNIFORMITY ACROSS SEMICONDUCTOR WAFERS
6472694	23-Jul-21	MICROPROCESSOR STRUCTURE
6473349	29-Nov-21	CASCADE SENSE AMP AND COLUMN SELECT CIRCUIT AND METHOD OF OPERATION
6473457	07-May-19	METHOD AND APPARATUS FOR PRODUCING A PULSE WIDTH MODULATED SIGNAL
6473605	16-Nov-19	NOISE REDUCTION AND RANGE CONTROL FOR AN AM/FM DUAL RADIO SYSTEM
6473606	16-Nov-19	COMMON INTERMEDIATE FREQUENCY BROADCAST RADIO FRONT END
6473732	18-Oct-15	SIGNAL ANALYZER AND METHOD THEREOF
6473808	02-Apr-19	HIGH PERFORMANCE COMMUNICATION CONTROLLER
6475841	02-Jun-20	TRANSISTOR WITH SHAPED GATE ELECTRODE AND METHOD THEREFOR
6475925	10-Apr-20	REDUCED WATER ADSORPTION FOR INTERLAYER DIELECTRIC
6475930	31-Jan-20	UV CURE PROCESS AND TOOL FOR LOW K FILM FORMATION
6476426	06-Jul-19	ELECTRONIC COMPONENT AND METHOD FOR IMPROVING PIXEL CHARGE TRANSFER IN THE ELECTRONIC COMPONENT
6476506	28-Sep-21	PACKAGED SEMICONDUCTOR WITH MULTIPLE ROWS OF BOND PADS AND METHOD THEREFOR
6476623	06-Nov-20	PERCENT BACKSPUTTERING AS A CONTROL PARAMETER FOR METALLIZATION
6476753	29-Nov-20	AN ANALOG TO DIGITAL CONVERTER USING MAGNETORESISTIVE MEMORY TECHNOLOGY
6477104	05-Apr-19	TILED MEMORY AND MEMORY TILE FOR USE THEREIN
6477285	30-Jun-20	INTEGRATED CIRCUITS WITH OPTICAL SIGNAL PROPAGATION
6477477	10-Feb-21	EXTENDED BASE BAND MULTICARRIER SYSTEM
6477608	26-Apr-20	INTERFACE CIRCUIT
6477640	03-Apr-18	APPARATUS FOR PERFORMING BRANCH PREDICTION AND RESOLUTION OF TWO OR MORE BRANCH INSTRUCTIONS WITHIN TWO OR MORE BRANCH PREDICTION BUFFERS
6477679	07-Feb-20	METHODS FOR DECODING DATA IN DIGITAL COMMUNICATION SYSTEMS
6477681	07-Feb-20	METHODS FOR DECODING DATA IN DIGITAL COMMUNICATION SYSTEMS

6477692	22-Jul-19	METHOD AND APPARATUS FOR CHANNEL-ROUTING OF AN ELECTRONIC DEVICE
6477693	17-Feb-18	METHOD FOR MANUFACTURING AND DESIGNING A WIRING OF A CHANNEL OF AN ELECTRONIC DEVICE AND ELECTRONIC APPARATUS
6477898	01-May-21	MEMBRANE MASK STRESS MEASUREMENT APPARATUS AND METHOD THEREFOR
6479173	17-Dec-19	SEMICONDUCTOR STRUCTURE HAVING A CRYSTALLINE ALKALINE EARTH METAL SILICON NITRIDE/OXIDE INTERFACE WITH SILICON
6479310	03-Jan-20	METHOD AND APPARATUS FOR TESTING A SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE
6479843	27-Apr-20	SINGLE SUPPLY HFET WITH TEMPERATURE COMPENSATION
6479892	31-Oct-20	ENHANCED PROBE FOR GATHERING DATA FROM SEMICONDUCTOR DEVICES
6480800	08-Feb-19	METHOD AND SYSTEM FOR GENERATING SELF-TESTING AND RANDOM INPUT STIMULI FOR TESTING DIGITAL SYSTEMS
6480874	09-Nov-19	POWER SAVING METHOD FOR PERFORMING ADDITIONS AND SUBTRACTIONS AND A DEVICE THEREOF
6480998	18-Apr-20	ITERATIVE, NOISE SENSITIVE METHOD OF ROUTING SEMICONDUCTOR NETS
6482073	30-Jul-18	TRANSLATION MECHANISM FOR A CHEMICAL MECHANICAL PLANARIZATION SYSTEM AND METHOD THEREFOR
6482289	06-Oct-15	NONCONDUCTIVE LAMINANT FOR COU PLING SUBSTRATES AND METHOD TH EREON
6482538	24-Jul-20	MICROELECTRONIC PIEZOELECTRIC STRUCTURE AND METHOD OF FORMING THE SAME
6483885	03-Sep-19	FRAME SYNCHRONIZER
6484228	19-Apr-20	METHOD AND APPARATUS FOR COMPRESSION, DECOMPRESSION, AND EXECUTION OF PROGRAM CODE
6486049	30-Apr-21	METHOD FOR PROVIDING SEMICONDUCTOR DEVICE AND CORRESPONDING SEMICONDUCTOR DEVICE
6487240	16-Feb-21	APPARATUS FOR RECEIVING AND RECOVERING FREQUENCY SHIFT KEYED SYMBOLS
6487260	05-Apr-19	METHOD AND APPARATUS FOR EFFIC IENT CORRELATION DETECTION
6487395	16-Mar-18	RADIO FREQUENCY ELECTRONIC SWI TCH
6487398	14-Aug-21	LOW NOISE ARCHITECTURE FOR A DIRECT CONVERSION TRANSMITTER
6487670	02-Mar-19	METHOD FOR DETECTING AND FOR RESPONDING TO DETECTION OF A BATTERY TO A LOGIC DEVICE AND SYSTEMS RELATING THERETO
6489083	30-Jan-21	SELECTIVE SIZING OF FEATURES TO COMPENSATE FOR RESIST THICKNESS VARITATIONS IN SEMICONDUCTOR DEVICES
6489211	01-Mar-20	SEIMCONDUCTOR COMPONENT AND METHOD OF MANUFACTURE
6489229	07-Sep-21	METHOD OF FORMING A SEMICONDUCTOR DEVICE HAVING CONDUCTIVE BUMPS WITHOUT USING GOLD
6489914	04-Dec-21	RSD ANALOG TO DIGITAL CONVERTER
6490225	04-Dec-21	MEMORY HAVING A SYNCHRONOUS CONTROLLER AND ASYNCHRONOUS ARRAY AND METHOD THEREOF
6490283	06-Jul-19	COMMUNICATION SYSTEM WITH COMMUNICATION

		CONTROLLER AND MULTIPLE PHYSICAL INTERFACES, AND METHOD
6490440	01-Jun-19	DIGITAL TRANSMITTER CIRCUIT AND METHOD OF OPERATION
6491451	15-Feb-21	WAFER PROCESSING EQUIPMENT AND METHOD FOR PROCESSING WAFERS
6492202	28-Dec-19	METHOD OF ASSEMBLING COMPONENTS ONTO A CIRCUIT BOARD AND ELECTRONIC COMPONENT THEREOF
6492232	15-Jun-18	VERTICAL SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING THE SAME
6492686	02-Oct-17	INTEGRATED CIRCUIT HAVING BUFFERING CIRCUITRY WITH SLEW RATE CONTROL
6492874	30-Jul-21	ACTIVE BIAS CIRCUIT
6493497	26-Sep-20	ELECTRO-OPTIC STRUCTURE AND PROCESS FOR FABRICATING SAME
6493854	01-Oct-19	METHOD AND APPARATUS FOR PLACING REPEATERS IN A NETWORK OF AN INTEGRATED CIRCUIT
6494217	19-Sep-18	LASER CLEANING PROCESS FOR SEMICONDUCTOR MATERIAL AND THE LIKE
6495802	31-May-21	TEMPERATURE-CONTROLLED CHUCK AND METHOD FOR CONTROLLING THE TEMPERATURE OF A SUBSTANTIALLY FLAT OBJECT
6496233	30-Nov-19	COMMAND AND CONTROL ARCHITECTURE FOR A VIDEO DECODER AND AN AUDIO DECODER
6496436	30-Jan-21	REFERENCE VOLTAGE GENERATOR FOR MRAM AND METHOD
6496556	15-Jan-22	STEP-DOWN CLOCK CONTROL AND METHOD FOR IMPROVING CONVERGENCE FOR A DIGITALLY CONTROLLED SELF-CALIBRATING VCO
6496946	10-May-19	ELECTRONIC CONTROL APPARATUS WITH MEMORY VALIDATION AND METHOD
6498066	19-May-20	ULTRA-LATE PROGRAMMING ROM AND METHOD OF MANUFACTURE
6498550	28-Apr-20	FILTERING DEVICE AND METHOD
6499092	17-Apr-18	METHOD AND APPARATUS FOR PERFORMING ACCESS CENSORSHIP IN A DATA PROCESSING SYSTEM
6499116	31-Mar-19	PERFORMANCE OF DATA STREAM TOUCH EVENTS
6500315	07-Nov-19	METHOD AND APPARATUS FOR FORMING A LAYER ON A SUBSTRATE
6500324	14-May-17	PROCESS FOR DEPOSITING A LAYER OF MATERIAL ON A SUBSTRATE
6500723	05-Oct-21	METHOD FOR FORMING A WELL UNDER ISOLATION AND STRUCTURE THEREOF
6500724	21-Aug-20	METHOD OF MAKING SEMICONDUCTOR DEVICE HAVING PASSIVE ELEMENTS INCLUDING FORMING CAPACITOR ELECTRODE AND RESISTOR FROM SAME LAYER OF MATERIA
6500750	12-Jul-20	SEMICONDUCTOR DEVICE AND METHOD OF FORMATION
6501121	15-Nov-20	SEMICONDUCTOR STRUCTURE AND PROCESS FOR FABRICATING SAME
6501144	13-Nov-21	A CONDUCTIVE LINE WITH MULTIPLE TURNS FOR PROGRAMMING A MRAM DEVICE
6501973	30-Jun-20	APPARATUS AND METHOD FOR MEASURING SELECTED PHYSICAL CONDITION OF AN ANIMATE SUBJECT
6503814	19-Jan-21	METHOD FOR FORMING TRENCH ISOLATION

6504246	12-Oct-19	INTEGRATED CIRCUIT HAVING A BALANCED TWIST FOR DIFFERENTIAL SIGNAL LINES
6504427	31-May-21	SWITCHING AMPLIFIER HAVING DIGITAL CORRECTION AND METHOD THEREFOR
6505032	10-Oct-20	CARRIERLESS ULTRA WIDEBAND WIRELESS SIGNALS FOR CONVEYING APPLICATION DATA
6505149	02-Aug-19	METHOD AND SYSTEM FOR VERIFYING A SOURCE-SYNCHRONOUS COMMUNICATION INTERFACE OF A DEVICE
6505290	05-Sep-17	METHOD AND APPARATUS FOR INTER FACING A PROCESSOR TO A COPROCESSOR
6505331	15-Oct-16	A METHOD FOR ROUTING OF NETS IN AN ELECTRONIC DEVICE USING GRAPH REPRESENTATION
6507475	27-Jun-20	CAPACITIVE DEVICE AND METHOD OF MANUFACTURE
6509247	04-May-18	SEMICONDUCTOR DEVICE AND ALIGNMENT METHOD
6509609	18-Jun-21	GROOVED CHANNEL SCHOTTKY MOSFET
6510444	16-Jun-19	DATA PROCESSOR ARCHITECTURE AND INSTRUCTION FORMAT FOR INCREASED EFFICIENCY
6512689	18-Jan-22	MRAM WITHOUT ISOLATION DEVICES
6514126	21-Dec-18	PAD CONDITIONER COUPLING AND END EFFECTOR FOR A CHEMICAL MECHANICAL PLANARIZATION SYSTEM AND METHOD THEREFOR
6514789	26-Oct-19	COMPONENT AND METHOD FOR MANUFACTURE
6514808	30-Nov-21	TRANSISTOR HAVING A HIGH K DIELECTRIC AND SHORT GATE LENGTH AND METHOD THEREFOR
6515341	26-Feb-21	MAGNETOELECTRONICS ELEMENT HAVING A STRESSED OVER-LAYER CONFIGURED FOR ALTERATION OF THE SWITCHING ENERGY BARRIER
6515895	01-Jun-21	NON-VOLATILE MAGNETIC REGISTER
6516036	15-Dec-19	METHOD AND APPARATUS FOR DECIMATING AN OVERSAMPLED SIGNAL
6516376	30-Nov-19	COMMAND AND CONTROL ARCHITECTURE FOR A VIDEO DECODER AND A HOST
6516420	25-Oct-19	DATA SYNCHRONIZER USING A PARALLEL HANDSHAKE PIPELINE WHEREIN VALIDITY INDICATORS GENERATE AND SEND ACKNOWLEDGEMENT SIGNALS TO A DIFFERENT CLOCK DOMAIN
6516666	01-Apr-21	YAW RATE MOTION SENSOR
6517698	06-Oct-20	SYSTEM AND METHOD FOR PROVIDING ROTATION TO PLATING FLOW
6517977	10-Jul-21	LITHOGRAPHIC TEMPLATE AND METHOD OF FORMATION AND USE
6518070	17-May-20	PROCESS FOR FORMING A SEMICONDUCTOR DEVICE AND A SEMICONDUCTOR DEVICE
6518071	28-Mar-22	A MAGNETORESISTIVE RANDOM ACCESS MEMORY DEVICE AND METHOD OF FABRICATION THEREOF
6518106	26-May-21	SEMICONDUCTOR DEVICE AND A METHOD THEREFOR
6518146	09-Jan-22	SEMICONDUCTOR DEVICE STRUCTURE AND METHOD FOR FORMING
6518634	01-Sep-20	STRONTIUM NITRIDE OR STRONTIUM OXYNITRIDE GATE DIELECTRIC
6519287	13-Jul-18	METHOD AND APPARATUS FOR ENCODING AND DECODING VIDEO SIGNALS

6519684	23-Nov-19	LOW OVERHEAD METHOD FOR SELECTING AND UPDATING AN ENTRY IN A CACHE MEMORY
6521961	28-Apr-20	SEMICONDUCTOR DEVICE USING A BARRIER LAYER BETWEEN THE GATE ELECTRODE AND SUBSTRATE AND METHOD THEREFOR
6522195	23-Apr-21	LOW NOISE AMPLIFIER BYPASS CIRCUITRY
6523095	22-Jul-16	METHOD AND DATA PROCESSING SYSTEM FOR USING QUICK DECODE INSTRUCTIONS
6524931	13-Apr-18	METHOD FOR FORMING A TRENCH ISOLATION STRUCTURE IN AN INTEGRATED CIRCUIT
6524967	29-Dec-20	METHOD FOR INCORPORATING NITROGEN INTO A DIELECTRIC LAYER USING A SPECIAL PRECURSOR
6525501	26-May-20	METHOD AND APPARATUS FOR ACCELERATING COMMUNICATION BETWEEN CONTROLLABLE DEVICES
6525957	21-Dec-21	MAGNETO-ELECTRONIC COMPONENT AND METHOD OF MANUFACTURING
6528377	10-Feb-20	SEMICONDUCTOR SUBSTRATE AND METHOD FOR PREPARING THE SAME
6528405	18-Feb-20	ENHANCEMENT MODE RF DEVICE AND FABRICATION METHOD
6528849	18-Sep-20	DUAL-GATE RESURF SUPERJUNCTION LATERAL DMOSFET
6529567	10-Mar-19	ELECTRONIC APPARATUS AND METHOD FOR RECEIVING NOISE SIGNALS
6531344	03-Feb-21	HIGH FREQUENCY GALLIUM ARSENIDE MMIC DIE COATING METHOD
6531384	14-Sep-21	METHOD OF FORMING A BOND PAD AND STRUCTURE THEREOF
6531723	16-Oct-21	MAGNETORESISTANCE RANDOM ACCESS MEMORY FOR IMPROVED SCALABILITY
6531731	15-Jun-21	INTEGRATION OF TWO MEMORY TYPES ON THE SAME INTEGRATED CIRCUIT
6531740	17-Jul-21	INTEGRATED IMPEDANCE MATCHING AND STABILITY NETWORK
6532559	26-Jan-20	METHOD AND APPARATUS FOR TESTING AN INTEGRATED CIRCUIT
6535157	07-Sep-21	LOW POWER CYCLIC A/D CONVERTER
6535552	19-May-19	FAST TRAINING OF EQUALIZERS IN DISCRETE MULTI-TONE (DMT) SYSTEMS
6536026	30-Jun-21	METHOD AND APPARATUS FOR ANALYZING SMALL SIGNAL RESPONSE AND NOISE IN NONLINEAR CIRCUITS
6538940	26-Sep-22	METHOD AND CIRCUITRY FOR IDENTIFYING WEAK BITS IN AN MRAM
6540309	15-Jul-19	FAULT-TOLERANT ELECTRONIC BRAKING SYSTEM
6541280	20-Mar-21	HIGH DIELECTRIC FILM
6541794	08-Sep-20	IMAGING DEVICE AND METHOD
6542940	25-Oct-19	METHOD AND APPARATUS FOR CONTROLLING TASK EXECUTION IN A DIRECT MEMORY ACCESS CONTROLLER
6544801	07-Jan-21	METHOD OF FABRICATING THERMALLY STABLE MTJ CELL AND APPARATUS
6544810	31-Aug-20	CAPACITIVELY SENSED MICROMACHINED COMPONENT AND METHOD OF MANUFACTURING
6545310	18-Jul-21	NON-VOLATILE MEMORY WITH A SERIAL TRANSISTOR STRUCTURE WITH ISOLATED WELL AND METHOD OF

		OPERATION
6545324	12-Jun-20	DUAL METAL GATE TRANSISTORS FOR CMOS PROCESS
6545906	16-Oct-21	A METHOD OF WRITING TO A SCALABLE MAGNETORESISTANCE RANDOM ACCESS TO MEMORY ELEMENT
6549454	18-Mar-21	TMR MATERIAL HAVING A SUBSTANTIALLY SMOOTH AND CONTINUOUS ULTRA-THIN MAGNETIC LAYER
6550013	02-Sep-19	MEMORY CLOCK GENERATOR AND METHOD THEREFOR
6551869	09-Jun-20	LATERAL PNP AND METHOD OF MANUFACTURE
6551919	13-Jul-19	A METHOD FOR FORMING A DUAL INLAID COPPER INTERCONNECT STRUCTURE
6551922	06-Mar-22	METHOD FOR MAKING A SEMICONDUCTOR DEVICE BY VARIABLE CHEMICAL MECHANICAL POLISH DOWNFORCE
6552436	08-Dec-20	SEMICONDUCTOR DEVICE HAVING A BALL GRID ARRAY AND METHOD THEREFOR
6552927	30-Jan-21	MRAM ARCHITECTURE AND SYSTEM
6552947	05-Apr-19	MEMORY TILE FOR USE IN A TILED MEMORY
6553487	07-Jan-20	DEVICE AND METHOD FOR PERFORMING HIGH-SPEED LOW OVERHEAD CONTEXT SWITCH
6554004	25-Jan-21	METHOD FOR REMOVING ETCH RESIDUE RESULTING FROM A PROCESS FOR FORMING A VIA
6555858	19-Dec-20	SELF-ALIGNED MAGNETIC CLAD WRITE LINE AND ITS METHOD OF FORMATION
6555915	22-Oct-21	INTEGRATED CIRCUIT HAVING INTERCONNECT TO A SUBSTRATE AND METHOD THEREFOR
6555946	24-Jul-20	ACOUSTIC WAVE DEVICE AND PROCESS FOR FORMING THE SAME
6556099	09-Sep-21	MULTILAYERED TAPERED TRANSMISSION LINE, DEVICE AND METHOD FOR MAKING THE SAME
6556584	06-Apr-18	SYSTEM AND METHOD OF COMMUNICATING NON- STANDARDIZED ADDRESSES OVER A STANDARDIZED CARRIER NETWORK
6559471	08-Dec-20	QUANTUM WELL PHOTODETECTOR AND METHOD FOR FABRICATING SAME
6559511	17-Dec-21	NARROW GAP CLADDING FIELD ENHANCEMENT FOR LOW POWER PROGRAMMING OF A MRAM DEVICE
6559723	04-Sep-21	SINGLE ENDED INPUT, DIFFERENTIAL OUTPUT AMPLIFIER
6559810	03-May-19	PLANAR ULTRA WIDE BAND ANTENNA WITH INTEGRATED ELECTRONICS
6560447	07-Jun-21	DC OFFSET CORRECTION SCHEME FOR WIRELESS RECEIVERS
6560623	16-Sep-19	METHOD AND APPARATUS FOR PRODUCING CORRECTLY ROUNDED VALUES OF FUNCTIONS USING DOUBLE PRECISION OPERANDS
6560712	16-Nov-19	BUS ARBITRATION IN LOW POWER SYSTEM
6562663	17-May-21	MICROELECTRONIC ASSEMBLY WITH DIE SUPPORT AND METHOD
6563118	08-Dec-20	PYROELECTRIC DEVICE ON A MONOCRYSTALLINE SEMICONDUCTOR SUBSTRATE AND PROCESS FOR FABRICATING SAME
6563181	02-Nov-21	HIGH FREQUENCY SIGNAL ISOLATION IN A SEMICONDUCTOR DEVICE

6563226	23-May-21	BONDING PAD
6564366	13-Oct-18	METHOD FOR CHANNEL ROUTING AND APPARATUS
6564469	25-Jul-21	DEVICE FOR PERFORMING SURFACE TREATMENT ON SEMICONDUCTOR WAFERS
6566264	18-Nov-18	METHOD FOR FORMING AN OPENING IN A SEMICONDUCTOR DEVICE SUBSTRATE
6567424	07-Aug-15	APPARATUS AND METHOD FOR DETERMINING A SYNCHRONIZATION SIGNAL
6567934	21-Oct-19	METHOD AND APPARATUS FOR VERIFYING MULTIPROCESSING DESIGN IN A UNIT SIMULATION ENVIRONMENT
6569740	03-Mar-18	METHOD OF FORMING A SEMICONDUCTOR DEVICE HAVING A BUFFER
6570947	24-Sep-19	A PHASE LOCK LOOP HAVING A ROBUST BANDWIDTH AND A CALIBRATION METHOD THEREOF
6571386	13-Oct-18	APPARATUS AND METHOD FOR PROGRAM OPTIMIZING
6572462	04-May-18	CARRIER ASSEMBLY FOR CHEMICAL MECHANICAL PLANARIZATION SYSTEMS AND METHOD
6573160	26-May-20	METHOD AND APPARATUS FOR FORMING A SEMICONDUCTOR DEVICE UTILIZING A LOW TEMPERATURE PROCESS
6573173	13-Jul-19	A METHOD FOR FORMING A COPPER INTERCONNECT USING A MULTI-PLATEN CHEMICAL MECHANICAL POLISHING (CMP) PROCESS
6573562	31-Oct-21	SEMICONDUCTOR COMPONENT AND METHOD OF OPERATION
6574228	01-Feb-19	COMMUNICATION SYSTEM WITH PHYSICAL INTERFACE AND COMMUNICATION CONTROLLER, AND METHOD
6574284	02-Feb-20	BIT ENCODING SYSTEM AND METHOD
6574707	07-May-21	MEMORY INTERFACE PROTOCOL USING TWO ADDRESSING MODES AND METHOD OF OPERATION
6576520	18-Nov-20	AMORPHOUS CARBON LAYER FOR IMPROVED ADHESION OF PHOTORESIST AND METHOD OF FABRICATION
6576532	30-Nov-21	SEMICONDUCTOR DEVICE AND METHOD THEREFOR
6576967	28-Oct-20	SEMICONDUCTOR STRUCTURE AND PROCESS FOR FORMING A METAL OXY-NITRIDE DIELECTRIC LAYER
6577148	10-Aug-18	APPARATUS, METHOD, AND WAFER USED FOR TESTING INTEGRATED CIRCUITS FORMED ON A PRODUCT WAFER
6577195	16-Jul-22	BIPOLAR DIFFERENTIAL AMPLIFIER
6577851	16-Nov-19	IMPULSE NOISE BLANKER
6579625	17-Jul-21	MAGNETOELECTRONICS ELEMENT HAVING A MAGNETIC LAYER FORMED OF MULTIPLE SUB-ELEMENT LAYERS
6580172	02-Mar-21	LITHOGRAPHIC TEMPLATE AND METHOD OF FORMATION AND USE
6580298	28-Jun-22	THREE INPUT SENSE AMPLIFIER AND METHOD OF OPERATION
6580301	18-Jun-21	METHOD AND APPARATUS FOR A CLOCK CIRCUIT
6580719	06-May-19	METHOD FOR DETERMINING THE NUMBER OF ACCESSES GRANTED DURING WCL AND APPARATUS
6580795	14-Oct-19	ECHO CANCELLER FOR A FULL-DUPLEX COMMUNICATION SYSTEM AND METHOD THEREFOR
6581086	25-Oct-19	MULTIPLY AND ACCUMULATE UNIT (MAC) AND METHOD THEREFOR
6581140	18-Apr-21	METHOD AND APPARATUS FOR IMPROVING ACCESS TIME IN SET-ASSOCIATIVE CACHE SYSTEMS

6583034	22-Nov-20	SEMICONDUCTOR STRUCTURE INCLUDING A COMPLIANT SUBSTRATE HAVING A GRADED MONOCRYSTALLINE LAYER AND METHODS FOR FABRICATING THE STRUCTURE AND SEMICONDUCTOR DEVICES INCLUDING THE STRUCTURE
6583043	27-Jul-21	DIELECTRIC BETWEEN METAL STRUCTURES AND METHOD THEREFOR
6583057	14-Dec-18	METHOD OF FORMING A SEMICONDUCTOR DEVICE HAVING A LAYER DEPOSITED BY VARYING FLOW OF REACTANTS
6583816	01-Jun-18	IMAGING CIRCUIT AND METHOD OF SPATIAL COMPENSATION
6586160	26-Mar-21	METHOD FOR PATTERNING RESIST
6589099	09-Jul-21	METHOD FOR CHEMICAL MECHANICAL POLISHING (CMP) WITH ALTERING THE CONCENTRATION OF OXIDIZING AGENT IN SLURRY
6589856	06-Aug-21	METHOD AND APPARATUS FOR CONTROLLING ANTI-PHASE DOMAINS IN SEMICONDUCTOR STRUCTURES AND DEVICES
6590236	24-Jul-20	SEMICONDUCTOR STRUCTURE FOR USE WITH HIGH-FREQUENCY SIGNALS
6590545	07-Aug-23	ELECTRICALLY SMALL PLANAR UWB ANTENNA APPARATUS AND RELATED SYSTEM
6590818	17-Jun-22	METHOD AND APPARATUS FOR SOFT DEFECT DETECTION IN A MEMORY
6591093	25-Oct-19	CIRCUIT AND METHOD FOR FREQUENCY TRANSLATION
6591378	22-Feb-20	DEBUG CONTROLLER IN A DATA PROCESSOR AND METHOD THEREFOR
6592434	30-Mar-21	WAFER CARRIER AND METHOD OF MATERIAL REMOVAL FROM A SEMICONDUCTOR WAFER
6592708	28-Sep-19	FILTER APPARATUS AND METHOD THEREFOR
6593199	27-Feb-22	METHOD OF MANUFACTURING A SEMICONDUCTOR COMPONENT AND SEMICONDUCTOR COMPONENT THEREOF
6593226	12-Dec-19	METHOD FOR ADDING FEATURES TO A DESIGN LAYOUT AND PROCESS FOR DESIGNING A MASK
6593238	27-Nov-20	METHOD FOR DETERMINING AN ENDPOINT AND SEMICONDUCTOR WAFER
6593254	28-Mar-22	METHOD FOR CLAMPING A SEMICONDUCTOR DEVICE IN A MANUFACTURING PROCESS
6593605	25-Jun-18	ENERGY ROBUST FIELD EFFECT TRANSISTOR
6594317	08-Apr-22	SIMPLE ENCODING/DECODING TECHNIQUE FOR CODE POSITION MODULATION
6594328	28-Jul-19	METHOD AND APPARATUS FOR FACILITATING AN ESTIMATION OF A CARRIER FREQUENCY ERROR IN A RECEIVER OF A WIRELESS COMMUNICATION SYSTEM
6594422	15-Nov-21	OPTO-COUPLING DEVICE STRUCTURE AND METHOD THEREFOR
6594801	10-Jan-21	METHOD FOR COMPRESSING A DATA STRUCTURE REPRESENTING A LAYOUT OF A VLSI DEVICE
6596465	08-Oct-19	METHOD OF MANUFACTURING A SEMICONDUCTOR COMPONENT
6596616	19-Apr-22	METHOD FOR FORMING SERRATED CONTACT OPENING IN THE SEMICONDUCTOR DEVICE
6597234	14-Dec-21	ANTI-FUSE CIRCUIT AND METHOD OF OPERATION
6598107	04-May-20	METHOD FOR COMMUNICATING DATA ON A SERIAL BUS
6598192	28-Feb-20	METHOD AND APPARATUS FOR TESTING AN INTEGRATED

		CIRCUIT
6600344	18-Aug-21	PREDISTORTION CIRCUIT FOR RF DETECTOR
6600690	28-Jun-22	SENSE AMPLIFIER FOR A MEMORY HAVING AT LEAST TWO DISTINCT RESISTANCE STATES
6603157	01-Jun-18	FIELD EFFECT TRANSISTOR
6603388	21-Jan-20	SECURITY SYSTEM AND METHOD
6603766	31-Jan-20	APPARATUS AND METHOD FOR IMPLEMENTING AN ATM AAL2 COMBINED USE TIMER
6605395	06-Oct-21	METHOD AND APPARATUS FOR FORMING A PATTERN ON AN INTEGRATED CIRCUIT USING DIFFERING EXPOSURE CHARACTERISTICS
6605991	08-Jan-22	CIRCUITRY FOR CREATING A SPECTRAL NULL IN A DIFFERENTIAL OUTPUT SWITCHING AMPLIFIER AND METHOD THEREFOR
6606017	05-Dec-20	SWITCHABLE AND TUNABLE COPLANAR WAVEGUIDE FILTERS
6606025	01-Oct-19	SYNCHRONOUS DEMODULATOR
6606044	02-Jan-22	METHOD AND APPARATUS FOR GENERATING A PULSE WIDTH MODULATED SIGNAL
6608789	21-Dec-21	HYSTERESIS REDUCED SENSE AMPLIFIER AND METHOD OF OPERATION
6609174	19-Oct-19	EMBEDDED MRAMS INCLUDING DUAL READ PORTS
6611045	04-Jun-21	METHOD OF FORMING AN INTEGRATED CIRCUIT DEVICE USING DUMMY FEATURES AND STRUCTURE THEREOF
6613688	26-Apr-22	SEMICONDUCTOR DEVICE AND PROCESS FOR GENERATING AN ETCH PATTERN
6614062	17-Jan-21	SEMICONDUCTOR TILING STRUCTURE AND METHOD OF FORMATION
6614091	13-Mar-22	SEMICONDUCTOR DEVICE HAVING A WIRE BOND PAD AND METHOD THEREFOR
6614197	07-Feb-22	ODD HARMONICS REDUCTION OF PHASE ANGLE CONTROLLED LOADS
6614307	02-Aug-22	HYBRID STRUCTURE FOR DISTRIBUTED POWER AMPLIFIERS
6616854	01-Mar-22	METHOD OF BONDING AND TRANSFERRING A MATERIAL TO FORM A SEMICONDUCTOR DEVICE
6617214	23-Apr-21	INTEGRATED CIRCUIT STRUCTURE AND METHOD THEREFORE
6617524	11-Dec-21	PACKAGED INTEGRATED CIRCUIT AND METHOD THEREFOR
6617587	23-Nov-20	ELECTRON OPTICS FOR MULTI-BEAM ELECTRON BEAM LITHOGRAPHY TOOL
6617920	16-Jun-20	LINEAR ENVELOPE TRACKING RF POWER AMPLIFIER WITH ADAPTIVE ANALOG SIGNAL PROCESSING
6620301	28-Mar-22	METHOD FOR FORMING A SPUTTERED LAYER AND APPARATUS THEREFOR
6620563	13-Jul-21	LITHOGRAPHY METHOD FOR FORMING SEMICONDUCTOR DEVICES ON A WAFER UTILIZING ATOMIC FORCE MICROSCOPY
6620656	19-Dec-21	BODY-TIED SILICON ON INSULATOR SEMICONDUCTOR DEVICE AND METHOD THEREFOR
6621348	25-Oct-21	VARIABLE GAIN AMPLIFIER WITH AUTOBIASING SUPPLY REGULATION
6621351	19-Dec-21	RF AMPLIFIER AND METHOD THEREFOR
6621438	30-Apr-22	METHOD AND APPARATUS FOR CONVERTING DIGITAL-TO-

		ANALOG SIGNALS
6621729	28-Jun-22	SENSE AMPLIFIER INCORPORATING A SYMMETRIC MIDPOINT REFERENCE
6621730	27-Aug-22	MAGNETIC RANDOM ACCESS MEMORY HAVING A VERTICAL WRITE LINE
6625232	22-May-20	SMART DC OFFSET CORRECTION LOOP
6625233	29-Sep-19	METHOD AND APPARATUS IN A WIRELESS RECEIVER FOR DEMODULATING A CONTINUOUS-PHASE FREQUENCY-SHIFT-KEYED SIGNAL
6625727	23-Nov-19	APPARATUS AND METHOD FOR CONFIGURING A DATA PROCESSING SYSTEM BY RETRIEVING A CONFIGURATION VALUE FROM STORAGE DEVICE USING RESET VECTOR AND CONFIGURING PARAMETERS AFTER RESET
6625777	19-Oct-19	METHOD OF IDENTIFYING AN IMPROVED CONFIGURATION FOR A COMMUNICATION SYSTEM USING CODING GAIN AND AN APPARATUS THEREFOR
6627511	24-Aug-18	REDUCED STRESS ISOLATION FOR SOI DEVICES AND A METHOD FOR FABRICATING
6629270	16-Jan-22	A SYSTEM FOR INITIALIZING A DISTRIBUTED COMPUTER SYSTEM AND A METHOD THEREOF
6629367	22-Dec-20	METHOD FOR FORMING AN ELECTRICALLY ISOLATED VIA IN A MULTILAYER CERAMIC PACKAGE AND AN ELECTRICAL CONNECTION FORMED WITH THE VIA
6630700	05-Oct-21	STACKED NMOS DEVICE BIASING ON MOS INTEGRATED CIRCUITS AND METHODS THEREFOR
6630725	06-Oct-20	ELECTRONIC COMPONENT AND METHOD OF MANUFACTURE
6630746	06-Jul-18	SEMICONDUCTOR DEVICE AND METHOD OF MAKING THE SAME
6632689	30-Jan-22	METHOD FOR PROCESSING SEMICONDUCTOR WAFERS IN AN ENCLOSURE WITH A TREATED INTERIOR SURFACE
6633379	05-Sep-21	APPARATUS AND METHOD FOR MEASURING THE DEGRADATION OF A TOOL
6633498	21-Jun-22	MAGNETORESISTIVE RANDOM ACCESS MEMORY WITH REDUCED SWITCHING FIELD
6633716	04-Oct-21	OPTICAL DEVICE AND METHOD THEREFOR
6634385	21-Dec-21	APPARATUS FOR CONVEYING FLUIDS AND BASE PLATE
6636402	15-Nov-21	HIGH VOLTAGE PROTECTION CIRCUIT
6636980	19-Aug-19	BUS INTERFACE APPARATUS AND METHOD THEREFOR
6638838	02-Oct-20	SEMICONDUCTOR STRUCTURE INCLUDING A PARTIALLY ANNEALED LAYER AND METHOD OF FORMING THE SAME
6638872	26-Sep-22	INTEGRATION OF MONOCRYSTALLINE OXIDE DEVICES WITH FULLY DEPLETED CMOS ON NON-SILICON SUBSTRATES
6645678	17-Mar-21	METHOD AND APPARATUS FOR MAKING AN INTEGRATED CIRCUIT USING POLARIZATION PROPERTIES OF LIGHT
6646293	18-Jul-21	STRUCTURE AND METHOD FOR FABRICATING HETEROJUNCTION BIPOLAR TRANSISTORS AND HIGH ELECTRON MOBILITY TRANSISTORS UTILIZING THE FORMATION OF A COMPLIANT SUBSTRATE FOR MATERIALS USED TO FORM THE SAME
6646347	30-Nov-21	SEMICONDUCTOR POWER DEVICE AND METHOD OF FORMATION
6646500	13-Apr-22	INTEGRATED CIRCUIT HAVING AN FM DEMODULATOR AND METHOD THEREFOR

6646844	15-Dec-19	APPARATUS FOR POWER-ON DISABLE IN A MULTIPLE POWER SUPPLY SYSTEM AND A METHOD THEREFOR
6646948	29-Aug-22	DATA STORAGE SYSTEM UTILIZING A NON-VOLATILE IC BASED MEMORY FOR REDUCTION OF DATA RETRIEVAL TIME
6647116	06-May-19	CURRENT SENSING CIRCUIT AND ADSL INTERFACE CIRCUIT
6647462	24-Mar-21	AN APPARATUS AND A METHOD FOR PROVIDING DECODED INFORMATION
6648999	03-Apr-22	LOW-PRESSURE LAMINATED CERAMIC DEVICES AND METHOD
6649445	11-Sep-22	WAFER COATING AND SINGULATION METHOD
6649452	28-Feb-22	METHOD FOR MANUFACTURING A LITHOGRAPHIC RETICLE FOR TRANSFERRING AN INTEGRATED CIRCUIT DESIGN TO A SEMICONDUCTOR WAFER AND STRUCTURE THEREOF
6650022	11-Sep-22	SEMICONDUCTOR DEVICE EXHIBITING ENHANCED PATTERN RECOGNITION WHEN ILLUMINATED IN A MACHINE VISION SYSTEM
6650092	24-May-22	SYSTEM AND METHOD FOR REGULATING A POWER SYSTEM WITH FEEDBACK USING CURRENT SENSING
6650135	03-Oct-20	MEASUREMENT CHUCK HAVING PIEZOELECTRIC ELEMENTS AND METHOD
6651227	30-Jan-22	METHOD FOR GENERATING TRANSITION DELAY FAULT TEST PATTERNS
6653053	23-Dec-21	METHOD OF FORMING A PATTERN ON A SEMICONDUCTOR WAFER USING AN ATTENUATED PHASE SHIFTING REFLECTIVE MASK
6653911	10-Apr-22	A BROAD BAND IMPEDANCE MATCHING DEVICE WITH REDUCED LINE WIDTH
6654278	31-Jul-22	MAGNETORESISTANCE RANDOM ACCESS MEMORY
6654871	09-Nov-19	A DEVICE AND METHOD FOR PERFORMING STACK OPERATIONS IN A PROCESSING SYSTEM
6656761	21-Nov-21	METHOD FOR FORMING A SEMICONDUCTOR DEVICE FOR DETECTING LIGHT
6657502	01-Oct-21	MULTIPHASE VOLTAGE CONTROLLED OSCILLATOR
6657683	13-Mar-20	FRONT ILLUMINATOR FOR A LIQUID CRYSTAL DISPLAY AND METHOD OF MAKING SAME
6657889	28-Jun-22	MEMORY HAVING WRITE CURRENT RAMP RATE CONTROL
6657977	26-Mar-19	RADIO WITH BURST EVENT EXECUTION APPARATUS AND METHOD THEREFOR
6658245	09-May-22	RADIO RECEIVER HAVING A DYNAMIC BANDWIDTH FILTER AND METHOD THEREFOR
6658440	24-Feb-20	MULTICHANNEL FILTERING DEVICE AND METHOD
6658651	29-Aug-21	METHOD AND APPARATUS FOR ANALYZING SOFTWARE IN A LANGUAGE-INDEPENDENT MANNER
6662328	11-Feb-19	METHOD OF MAKING LOGIC DEVICES
6663340	30-Aug-22	WAFER CARRIER TRANSPORT SYSTEM FOR TOOL BAYS
6663674	19-Apr-22	METHOD OF HANDLING A SILICON WAFER
6664200	28-Apr-20	METHOD OF MANUFACTURING A SEMICONDUCTOR COMPONENT AND POLYIMIDE ETCHANT THEREOF
6664606	23-Apr-22	MULTILAYER CIRCUIT APPARATUS WITH REDUCED COUPLING
6664826	20-Jul-20	VARIABLE GAIN LOOP FILTER FOR IMPROVED PHASE MARGIN AND DECREASED PHASE NOISE WITH WIDEBAND BOC'S
6664852	28-Feb-22	APPARATUS AND METHOD FOR DIGITAL CONTROL SYSTEMS

6664935	31-Jul-22	BROAD BAND IMPEDANCE MATCHING DEVICE WITH COUPLED TRANSMISSION LINES
6665298	31-Jan-20	A REASSEMBLY UNIT AND A METHOD THEREOF
6665338	05-Jan-20	CIRCUITRY FOR CONVERTING A SAMPLED DIGITAL SIGNAL TO A NATURALLY SAMPLED DIGITAL SIGNAL AND METHOD THEREFOR
6665527	15-May-22	DOUBLE BALANCED MIXER CIRCUIT
6665656	05-Oct-19	METHOD AND APPARATUS FOR EVALUATING DOCUMENTS WITH CORRELATING INFORMATION
6667196	25-Jul-21	METHOD FOR REAL-TIME MONITORING AND CONTROLLING PEROVSKITE OXIDE FILM GROWTH AND SEMICONDUCTOR STRUCTURE FORMED USING THE METHOD
6667500	23-Feb-20	SEMICONDUCTOR DEVICE AND METHOD FOR PROTECTING SUCH DEVICE FROM A REVERSED DRAIN VOLTAGE
6667543	29-Oct-22	OPTICAL SENSOR PACKAGE
6667701	16-Jul-22	VARIABLE LENGTH DECODER
6667899	27-Mar-23	MAGNETIC MEMORY AND METHOD OF BI-DIRECTIONAL WRITE CURRENT PROGRAMMING
6669079	01-Jun-19	CONDUCTIVE PASTE AND SEMICONDUCTOR COMPONENT HAVING CONDUCTIVE BUMPS MADE FROM THE CONDUCTIVE PASTE
6671059	26-Oct-21	METHOD AND SYSTEM FOR DETERMINING A THICKNESS OF A LAYER
6671336	16-May-20	GAIN CONTROLLER FOR CIRCUIT HAVING IN-PHASE AND QUADRATURE CHANNELS, AND METHOD
6671709	18-Dec-18	MULTIPLIER CELL AND METHOD OF COMPUTING
6673520	06-Jan-22	METHOD OF MAKING AN INTEGRATED CIRCUIT USING A REFLECTIVE MASK
6673646	28-Feb-21	GROWTH OF COMPOUND SEMICONDUCTOR STRUCTURES ON PATTERNED OXIDE FILMS AND PROCESS FOR FABRICATING SAME
6673667	15-Aug-21	METHOD FOR MANUFACTURING A SUBSTANTIALLY INTEGRAL MONOLITHIC APPARATUS INCLUDING A PLURALITY OF SEMI CONDUCTOR MATERIALS
6674304	26-Feb-19	OUTPUT BUFFER CIRCUIT AND METHOD OF OPERATION
6674333	15-Oct-22	BAND SWITCHABLE VOLTAGE CONTROLLED OSCILLATOR WITH SUBSTANTIALLY CONSTANT TUNING RANGE
6675235	25-Oct-19	METHOD FOR AN EXECUTION UNIT INTERFACE PROTOCOL AND APPARATUS THEREFOR
6677226	31-Jul-20	METHOD FOR FORMING AN INTEGRATED CIRCUIT
6677875	29-Apr-22	SIGMA-DELTA ANALOG-TO-DIGITAL CONVERTER AND METHOD
6677876	27-Aug-22	DIFFERENTIAL SIGMA-DELTA DAC WITH DYNAMIC SPECTRAL SHAPING
6678340	24-Mar-20	APPARATUS FOR RECEIVING AND PROCESSING A RADIO FREQUENCY SIGNAL
6678765	07-Feb-20	EMBEDDED MODEM
6678773	10-May-22	BUS PROTOCOL INDEPENDENT METHOD AND STRUCTURE FOR MANAGING TRANSACTION PRIORITY, ORDERING AND DEADLOCKS IN A MULTI-PROCESSING SYSTEM
6678884	14-Dec-21	A METHOD FOR DETERMINING THE STATUS OF VARIABLES DURING THE EXECUTION OF OPTIMIZED CODE
6680971	18-May-18	PASSBAND EQUALIZER FOR A VESTIGIAL SIDEBAND SIGNAL

		RECEIVER
6683370	15-Apr-23	SEMICONDUCTOR COMPONENT AND METHOD OF MANUFACTURING SAME
6683859	12-Nov-19	METHOD AND APPARATUS FOR ECHO CANCELLATION UPDATES IN A MULTICARRIER TRANSCEIVER SYSTEM
6683926	09-Jul-22	GAIN CONTROLLER WITH COMPARATOR OFFSET COMPENSATION FOR CIRCUIT HAVING IN-PHASE AND QUADRATURE CHANNELS
6686245	20-Dec-22	VERTICAL MOSFET WITH ASYMMETRIC GATE STRUCTURE
6686254	06-Jun-21	SEMICONDUCTOR STRUCTURE AND METHOD FOR REDUCING CHARGE DAMAGE
6686282	31-Mar-23	PLATED METAL TRANSISTOR GATE AND METHOD OF FORMATION
6686633	02-May-18	SEMICONDUCTOR DEVICE, MEMORY CELL AND PROCESSES FOR FORMING THEM
6686859	27-Jan-23	DIGITAL-TO-ANALOG CONVERTER
6687813	21-Jan-20	DATA PROCESSING SYSTEM AND METHOD FOR IMPLEMENTING ZERO OVERHEAD LOOPS USING A FIRST OR SECOND PREFIX INSTRUCTION FOR INITIATING CONDITIONAL JUMP OPERATIONS
6689676	26-Jul-22	METHOD FOR FORMING A SEMICONDUCTOR DEVICE STRUCTURE IN A SEMICONDUCTOR LAYER
6689680	14-Jul-21	SEMICONDUCTOR DEVICE AND METHOD OF FORMATION
6690144	09-Aug-22	OPEN LOOP INDUCTOR CURRENT CONTROL SYSTEM AND METHOD
6690580	07-Mar-22	INTEGRATED CIRCUIT STRUCTURE WITH DIELECTRIC ISLANDS IN METALLIZED REGIONS
6690748	26-Feb-22	RECEIVER WITH IMPROVED DIGITAL INTERMEDIATE TO BASE BAND DEMODULATOR
6690945	26-Mar-20	METHOD FOR SUPPRESSING TRANSIENTS USING A PULSE-SHAPING LOOK-UP TABLE
6691284	02-Aug-22	METHOD FOR CREATING A CHARACTERIZED DIGITAL LIBRARY FOR A DIGITAL CIRCUIT DESIGN
6693020	12-Mar-21	METHOD OF PREPARING COPPER METALLIZATION DIE FOR WIRE BONDING
6693033	10-Feb-20	METHOD OF REMOVING AN AMORPHOUS OXIDE FROM A MONOCRYSTALLINE SURFACE
6693298	20-Jul-21	STRUCTURE AND METHOD FOR FABRICATING EPITAXIAL SEMICONDUCTOR ON INSULATOR (SOI) STRUCTURES AND DEVICES UTILIZING THE FORMATION OF A COMPLIANT SUBSTRATE FOR MATERIALS USED TO FORM SAME
6693339	14-Mar-23	SEMICONDUCTOR COMPONENT AND METHOD OF MANUFACTURING SAME
6693355	27-May-23	METHOD OF MANUFACTURING A SEMICONDUCTOR DEVICE WITH AN AIR GAP FORMED USING A PHOTSENSITIVE MATERIAL
6693490	09-Apr-22	IMPROVED ADJUSTABLE GAIN CONTROL SYSTEM AND METHOD THEREOF
6693572	04-Feb-23	DIGITAL TUNING SCHEME FOR CONTINUOUS-TIME SIGMA DELTA MODULATION
6693824	28-Jun-22	CIRCUIT AND METHOD OF WRITING A TOGGLE MEMORY
6697001	19-Dec-22	CONTINUOUS-TIME SIGMA-DELTA MODULATOR WITH DISCRETE TIME COMMON-MODE FEEDBACK

6697956	31-Jan-20	METHOD AND APPARATUS FOR PHRASE SYNCHRONIZING A PLURALITY OF MICROCONTROLLERS OF A DISTRIBUTED MICROCONTROLLER NETWORK IN A BRAKE-BY-WIRE AUTOMOBILE BRAKING SYSTEM
6700451	30-Nov-22	CROSS COUPLED CASCODE VOLTAGE CONTROLLED OSCILLATOR
6700520	16-Oct-22	MULTI-BIT CONTINUOUS TIME SIGMA-DELTA ADC
6700814	14-Dec-22	SENSE AMPLIFIER BIAS CIRCUIT FOR A MEMORY HAVING AT LEAST TWO DISTINCT RESISTANCE STATES
6700939	11-Dec-18	ULTRA WIDE BANDWIDTH SPREAD-SPECTRUM COMMUNICATIONS SYSTEM
6701476	18-Mar-22	TEST ACCESS MECHANISM FOR SUPPORTING A CONFIGURABLE BUILT-IN SELF-TEST CIRCUIT AND METHOD THEREOF
6703258	31-Oct-20	ENHANCED PROBE FOR GATHERING DATA FROM SEMICONDUCTOR DEVICES
6703895	26-Sep-22	SEMICONDUCTOR COMPONENT AND METHOD OF OPERATING SAME
6704358	07-May-18	METHOD AND APPARATUS FOR RESIZING IMAGE INFORMATION
6706548	25-Jan-22	METHOD OF MAKING A MICROMECHANICAL DEVICE
6706599	20-Mar-23	MULTI-BIT NON-VOLATILE MEMORY DEVICE AND METHOD THEREFOR
6707339	22-Nov-22	CONTROLLED BIAS CURRENT BUFFER AND METHOD THEREFOR
6709312	26-Jun-22	METHOD AND APPARATUS FOR MONITORING A POLISHING CONDITION OF A SURFACE OF A WAFER IN A POLISHING PROCESS
6709793	03-Nov-22	METHOD OF MANUFACTURING RETICLES USING SUBRESOLUTION TEST PATTERNS
6709888	26-Jul-22	METHOD OF DECAPSULATING A PACKAGED COPPER TECHNOLOGY INTEGRATED CIRCUIT
6709989	25-Oct-19	METHOD FOR FABRICATING A SEMICONDUCTOR STRUCTURE INCLUDING A MONOCRYSTALLINE METAL OXIDE INTERFACE WITH SILICON
6710265	15-Oct-19	MULTI-STRAND SUBSTRATE FOR BALL-GRID ARRAY ASSEMBLIES AND METHOD
6711052	28-Jun-22	MEMORY HAVING A PRECHARGE CIRCUIT AND METHOD THEREFOR
6711068	28-Jun-22	BALANCED LOAD MEMORY AND METHOD OF OPERATION
6711661	01-Apr-22	METHOD AND APPARATUS FOR PERFORMING HIERARCHICAL ADDRESS TRANSLATION
6713381	05-Apr-19	METHOD OF FORMING SEMICONDUCTOR DEVICE INCLUDING INTERCONNECT BARRIER LAYERS
6713812	09-Oct-22	NON-VOLATILE MEMORY DEVICE HAVING AN ANTI-PUNCH THROUGH (APT) REGION
6714081	11-Sep-22	ACTIVE CURRENT BIAS NETWORK FOR COMPENSATING HOT-CARRIER INJECTION INDUCED BIAS DRIFT
6714095	22-Jun-22	TAPERED CONSTANT "R" NETWORK FOR USE IN DISTRIBUTED AMPLIFIERS
6714436	20-Mar-23	WRITE OPERATION FOR CAPACITORLESS RAM
6714440	28-Jun-22	MEMORY ARCHITECTURE WITH WRITE CIRCUITRY AND METHOD THEREFOR

6714442	17-Jan-23	MRAM ARCHITECTURE WITH A GROUNDED WRITE BIT LINE AND ELECTRICALLY ISOLATED READ BIT LINE
6714446	13-May-23	MAGNETOELECTRONICS INFORMATION DEVICE HAVING A COMPOUND MAGNETIC FREE LAYER
6717226	15-Mar-22	GATE DIELECTRIC AND METHOD THEREFOR
6717269	27-Jul-21	DIELECTRIC BETWEEN METAL STRUCTURES AND METHOD THEREFOR
6717270	09-Apr-23	INTEGRATED CIRCUIT DIE I/O CELLS
6717430	12-Apr-22	INTEGRATED CIRCUIT TESTING WITH A VISUAL INDICATOR
6717533	31-May-21	METHOD AND APPARATUS FOR COMBINING A WIRELESS RECEIVER AND A NON-WIRELESS RECEIVER
6720597	05-Mar-22	CLADDING OF A CONDUCTIVE INTERCONNECT FOR PROGRAMMING A MRAM DEVICE USING MULTIPLE MAGNETIC LAYERS
6720635	17-Dec-19	ELECTRONIC COMPONENT AND METHOD OF MANUFACTURE
6724032	25-Jul-22	MULTI-BIT NON-VOLATILE MEMORY CELL AND METHOD THEREFOR
6724048	06-Mar-22	BODY-TIED SILICON ON INSULATOR SEMICONDUCTOR DEVICE AND METHOD THEREFOR
6724079	04-Jan-22	WIRE BOND-LESS ELECTRONIC COMPONENT FOR USE WITH AN EXTERNAL CIRCUIT AND METHOD OF MANUFACTURE
6724603	05-Oct-22	ELECTROSTATIC DISCHARGE PROTECTION CIRCUITRY AND METHOD OF OPERATION
6725346	04-Apr-20	METHOD AND APPARATUS FOR OVERLAYING MEMORY IN A DATA PROCESSING SYSTEM
6726826	01-Dec-19	METHOD OF MANUFACTURING A SEMICONDUCTOR COMPONENT AND PLATING TOOL THEREFOR
6728954	25-Dec-21	A METHOD FOR ALLOWING EXECUTION MANAGEMENT OF OPTIMIZED CODE
6730623	01-Oct-22	COFIREABLE DIELECTRIC COMPOSITION
6732855	09-Feb-21	CONVEYING ELEMENT AND CONVEYOR MEANS FOR CONVEYING WAFER RECEPTACLES, AND METHOD
6734524	31-Dec-22	ELECTRONIC COMPONENT AND METHOD OF MANUFACTURING SAME
6735238	10-Oct-20	ULTRA WIDEBAND COMMUNICATION SYSTEM, METHOD, AND DEVICE WITH LOW NOISE PULSE FORMATION
6737202	06-Mar-22	METHOD OF FABRICATING A TIERED STRUCTURE USING A MULTI-LAYERED RESIST STACK AND USE
6737205	30-Apr-22	ARRANGEMENT AND METHOD FOR TRANSFERRING A PATTERN FROM A MASK TO A WAFER
6737929	27-Nov-22	HYBRID N+ AND P+ GATE-DOPED VOLTAGE VARIABLE CAPACITORS TO IMPROVE LINEAR TUNING RANGE IN VOLTAGE CONTROLLED OSCILLATORS
6738303	27-Nov-22	TECHNIQUE FOR SENSING THE STATE OF A MAGNETO-RESISTIVE RANDOM ACCESS MEMORY
6738420	24-Jul-20	DIGITAL FILTER HAVING AN UPSAMPLER OPERATIONAL AT A FRACTIONAL CLOCK RATE
6740544	14-May-22	SOLDER COMPOSITIONS FOR ATTACHING A DIE TO A SUBSTRATE
6741194	23-Dec-22	METHODS AND APPARATUS FOR DETECTING OUT-OF-RANGE SIGNALS IN AN ANALOG-TO-DIGITAL CONVERTER
6743668	18-Sep-20	PROCESS FOR FORMING A METAL OXY-NITRIDE DIELECTRIC

		LAYER BY VARYING THE FLOW RATE OF NITROGEN INTO THE CHAMBER
6744117	13-Apr-22	HIGH FREQUENCY SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURE
6744264	28-Apr-22	TESTING CIRCUIT AND METHOD FOR MEMS SENSOR PACKAGED WITH AN INTEGRATED CIRCUIT
6744494	11-Jul-22	CONTINUOUSLY ADJUSTABLE NEUTRAL DENSITY AREA FILTER
6744663	03-Jul-22	CIRCUIT AND METHOD FOR READING A TOGGLE MEMORY CELL
6747332	01-Apr-22	SEMICONDUCTOR COMPONENT HAVING HIGH VOLTAGE MOSFET AND METHOD OF MANUFACTURE
6747434	25-May-22	METHODS AND DEVICES FOR CONTROLLING STEPPER MOTORS
6747494	15-Feb-22	PLL ARRANGEMENT, CHARGE PUMP, METHOD AND MOBILE TRANSCEIVER
6748558	10-May-20	PERFORMANCE MONITOR SYSTEM AND METHOD SUITABLE FOR USE IN AN INTEGRATED CIRCUIT
6749968	21-May-22	METHOD FOR FABRICATING THIN-MEMBRANE STENCIL MASK AND METHOD FOR MAKNG A SEMICONDUCTOR DEVICE USING THE SAME
6750067	24-Jul-20	MICROELECTRONIC PIEZOELECTRIC STRUCTURE AND METHOD OF FORMING THE SAME
6750068	22-Mar-20	MAGNETIC ELEMENT WITH AN IMPROVED MAGNETORESISTANCE RATIO AND FABRICATING METHOD THEREOF
6750524	18-May-22	TRENCH MOS RESURF SUPER-JUNCTION DEVICES
6750664	21-Aug-21	APPARATUS AND METHOD FOR MANAGING AN INTEGRATED CIRCUIT
6750704	09-Jan-23	OFFSET COMPENSATED DIFFERENTIAL AMPLIFIER
6750721	30-Apr-22	HBT LINEARIZER AND POWER BOOSTER
6750722	28-Jun-22	BIAS CONTROL FOR HBT POWER AMPLIFIERS
6751125	04-Nov-22	GATE VOLTAGE REDUCTION IN A MEMORY READ
6751264	26-Mar-22	RECEIVER AND METHOD THEREFOR
6751724	19-Apr-20	METHOD AND APPARATUS FOR INSTRUCTION FETCHING
6751759	07-May-22	METHOD AND APPARATUS FOR PIPELINE HAZARD DETECTION
6752694	08-Nov-22	APPARATUS FOR AND METHOD OF WAFER GRINDING
6753216	31-Oct-22	MULTIPLE GATE TRANSISTOR EMPLOYING MONOCRYSTALLINE SILICON WALLS
6753242	19-Mar-22	INTEGRATED CIRCUIT DEVICE AND METHOD THEREFOR
6753719	21-Sep-22	SYSTEM AND CIRCUIT FOR CONTROLLING WELL BIASING AND METHOD THEREOF
6754752	03-May-22	MULTIPLE MEMORY COHERENCE GROUPS IN A SINGLE SYSTEM AND METHOD THEREFOR
6756320	18-Jan-22	ARTICLE COMPRISING AN OXIDE LAYER ON A GaAs-BASED SEMICONDUCTOR STRUCTURE AND METHOD OF FORMING SAME
6757336	04-May-20	A DEVICE AND METHOD FOR PERFORMING A CARRIER RECOVERY
6757701	22-Feb-22	APPARATUS AND METHOD FOR IMPLEMENTING A LINEARLY APPROXIMATED LOG MAP ALGORITHM
6757852	05-Mar-22	SELF RESETTING HIGH SPEED REDUNDANCY CIRCUIT AND

		METHOD THEREFOR
6759248	28-Sep-21	SEMICONDUCTOR WAFER IDENTIFICATION
6759675	02-May-21	OPTICAL DEVICE AND METHOD THEREFOR
6759914	19-Jul-21	OSCILLATOR CIRCUIT
6760266	18-Oct-22	SENSE AMPLIFIER AND METHOD FOR PERFORMING A READ OPERATION IN A MRAM
6760268	02-Jan-23	METHOD AND APPARATUS FOR ESTABLISHING A REFERENCE VOLTAGE IN A MEMORY
6760270	27-Nov-22	ERASE OF A NON-VOLATILE MEMORY
6760386	10-May-22	RECEIVER AND METHOD THEREFOR
6760864	06-Sep-22	DATA PROCESSING SYSTEM WITH ON-CHIP FIFO FOR STORING DEBUG INFORMATION AND METHOD THEREFOR
6760865	21-Nov-22	MULTIPLE LEVEL BUILT-IN SELF-TEST CONTROLLER AND METHOD THEREFOR
6762706	12-Jun-22	REDUCED POWER ANALOG-TO-DIGITAL CONVERTER AND METHOD THEREOF
6763150	02-Jul-22	IMAGE PROCESSING SYSTEM WITH MULTIPLE PROCESSING UNITS
6764864	17-Apr-23	BST ON LOW-LOSS SUBSTRATES FOR FREQUENCY AGILE APPLICATIONS
6764919	20-Dec-22	METHOD FOR FORMING A PASSIVATION LAYER FOR AIR GAP FORMATION AND STRUCTURE THEREOF
6765396	04-Apr-22	METHOD, APPARATUS AND SOFTWARE FOR TESTING A DEVICE INCLUDING BOTH ELECTRICAL AND OPTICAL PORTIONS
6765778	04-Apr-23	INTEGRATED VERTICAL STACK CAPACITOR
6765816	08-Nov-22	STORAGE CIRCUIT HAVING SINGLE-ENDED WRITE CIRCUITRY
6766431	29-Jun-22	DATA PROCESSING SYSTEM AND METHOD FOR A SECTOR CACHE
6766433	03-Jun-22	SYSTEM HAVING USER PROGRAMMABLE ADDRESSING MODES AND METHOD THEREFOR
6768353	06-Aug-22	PRESCALER METHOD AND APPARATUS
6769076	07-Feb-20	REAL-TIME PROCESSOR DEBUG SYSTEM
6769319	20-Oct-21	COMPONENT HAVING A FILTER
6770506	23-Dec-22	RELEASE ETCH METHOD FOR MICROMACHINED SENSORS
6770569	23-Sep-22	LOW TEMPERATURE PLASMA SI OR SIGE FOR MEMS APPLICATIONS
6770923	20-Mar-21	HIGH K DIELECTRIC FILM
6770929	18-Oct-20	METHOD FOR UNIFORM POLISH IN MICROELECTRONIC DEVICE
6771630	04-Feb-20	MULTI-CHANNEL CONTROLLER
6772370	24-Mar-22	METHOD AND APPARATUS FOR GENERATION OF PIPELINE HAZARD TEST SEQUENCES
6772813	24-Jan-23	REMOVABLE HEATED END EFFECTOR
6773977	14-Aug-22	METHOD OF FORMING A DIODE FOR INTEGRATION WITH A SEMICONDUCTOR DEVICE AND METHOD OF FORMING A TRANSISTOR DEVICE HAVING AN INTEGRATED DIODE
6774053	30-Mar-23	METHOD AND STRUCTURE FOR LOW-K DIELECTRIC CONSTANT APPLICATIONS
6774497	28-Mar-23	FLIP-CHIP ASSEMBLY WITH THIN UNDERFILL AND THICK SOLDER MASK

6774732	14-Feb-23	SYSTEM AND METHOD FOR COARSE TUNING A PHASE LOCKED LOOP (PLL) SYNTHESIZER USING 2-PI SLIP DETECTION
6775241	25-Sep-17	METHOD AND APPARATUS FOR CONFIGURING A COMMUNICATION SYSTEM
6775727	18-Dec-22	SYSTEM AND METHOD FOR CONTROLLING BUS ARBITRATION DURING CACHE MEMORY BURST CYCLES
6775765	07-Feb-20	DATA PROCESSING SYSTEM HAVING INSTRUCTION FOLDING AND METHOD THEREOF
6777662	06-Sep-22	SYSTEM, CIRCUIT AND METHOD PROVIDING A DYNAMIC RANGE PIXEL CELL WITH BLOOMING PROTECTION
6778457	19-Feb-23	VARIABLE REFRESH CONTROL FOR A MEMORY
6779055	15-Oct-22	FIRST-IN FIRST-OUT MEMORY SYSTEM AND METHOD THEREOF
6780703	27-Aug-22	METHOD FOR FORMING A SEMICONDUCTOR DEVICE
6780751	09-Oct-22	METHOD FOR ELIMINATING VOIDING IN PLATED SOLDER
6781474	07-Mar-23	APPARATUS AND METHOD FOR TUNING A FILTER
6781908	19-Feb-23	MEMORY HAVING A VARIABLE REFRESH CONTROL AND METHOD THEREFOR
6781965	12-Nov-19	METHOD AND APPARATUS FOR ECHO CANCELLATION UPDATES IN A MULTICARRIER TRANSCEIVER SYSTEM
6782038	30-Jul-19	METHOD AND APPARATUS FOR RADIO COMMUNICATION.
6783637	03-Dec-22	HIGH THROUGHPUT DUAL ION BEAM DEPOSITION APPARATUS
6783904	05-Nov-22	LITHOGRAPHY CORRECTION METHOD AND DEVICE
6783994	26-Apr-22	A METHOD OF FABRICATING A SELF-ALIGNED MAGNETIC TUNNELING JUNCTION AND VIA CONTACT
6784103	21-May-23	METHOD OF FORMATION OF NANOCRYSTALS ON A SEMICONDUCTOR STRUCTURE
6784510	16-Apr-23	MAGNETORESISTIVE RANDOM ACCESS MEMORY DEVICE STRUCTURES AND METHODS FOR FABRICATING THE SAME
6784725	18-Apr-23	SWITCHED CAPACITOR CURRENT REFERENCE CIRCUIT
6785177	13-Feb-23	METHOD OF ACCESSING MEMORY AND DEVICE THEREOF
6785326	03-Oct-22	METHOD AND APPARATUS FOR DETECTING AND COMPENSATING DIGITAL LOSSES IN A COMMUNICATIONS NETWORK
6785772	12-Dec-22	DATA PREFETCHING APPARATUS IN A DATA PROCESSING SYSTEM AND METHOD THEREFOR
6786222	25-Oct-22	METHOD FOR REMOVING PARTICLES FROM A SEMICONDUCTOR PROCESSING TOOL
6787421	15-Aug-22	METHOD FOR FORMING A DUAL GATE OXIDE DEVICE USING A METAL OXIDE AND RESULTING DEVICE
6787858	16-Oct-22	CARRIER INJECTION PROTECTION STRUCTURE
6788117	06-Mar-23	METHOD AND APPARATUS FOR GENERATING FREQUENCY-STABLE WAVELETS
6788134	20-Dec-22	LOW VOLTAGE CURRENT SOURCES/CURRENT MIRRORS
6788234	05-Feb-23	METHOD OF SELECTING CELLS FOR AN INPUT CODE IN DIGITAL-TO-ANALOG CONVERTER
6790719	09-Aug-23	PROCESS FOR FORMING DUAL METAL GATE STRUCTURES
6790727	23-Jun-21	INTEGRATION OF TWO MEMORY TYPES ON THE SAME INTEGRATED CIRCUIT
6790759	31-Jul-23	SEMICONDUCTOR DEVICE WITH STRAIN RELIEVING BUMP

		DESIGN
6791125	30-Sep-22	SEMICONDUCTOR DEVICE STRUCTURES WHICH UTILIZE METAL SULFIDES
6791883	17-Sep-22	PROGRAM AND ERASE IN A THIN FILM STORAGE NON-VOLATILE MEMORY
6792481	28-Sep-22	DMA CONTROLLER
6792502	13-Jun-21	A MICROPROCESSOR HAVING A CONTENT ADDRESSABLE MEMORY (CAM) DEVICE AS A FUNCTIONAL UNIT THEREIN AND METHOD OF OPERATION
6794101	06-Jun-22	MICRO-ELECTRO-MECHANICAL DEVICE AND METHOD OF MAKING
6794281	08-Sep-20	DUAL METAL GATE TRANSISTORS FOR CMOS PROCESS
6794949	28-Mar-23	FREQUENCY GENERATING DEVICE AND METHOD THEREOF
6795191	04-Jan-22	ULTRASONICALLY ASSISTED OPTICAL MEDIA SENSOR SYSTEM
6795908	20-Jan-22	METHOD AND APPARATUS FOR INSTRUCTION EXECUTION IN A DATA PROCESSING SYSTEM
6796482	31-Oct-22	PHASE SEPARATED SYSTEM FOR FLUXING
6796885	07-Jun-20	PAD CONDITIONER COUPLING AND END EFFECTOR FOR A CHEMICAL MECHANICAL PLANARIZATION SYSTEM AND METHOD THEREFOR
6797440	04-Jan-23	METHOD OF FORMING A RIM PHASE SHIFTING MASK AND USING THE RIM PHASE SHIFTING MASK TO FORM A SEMICONDUCTOR DEVICE
6798004	22-Apr-23	MAGNETORESISTIVE RANDOM ACCESS MEMORY DEVICES AND METHODS FOR FABRICATING THE SAME
6798064	29-May-21	ELECTRONIC COMPONENT AND METHOD OF MANUFACTURE
6798074	04-Mar-22	METHOD OF ATTACHING A DIE TO A SUBSTRATE
6798152	21-Aug-22	CLOSED LOOP CURRENT CONTROL CIRCUIT AND METHOD THEREOF
6798289	31-May-22	SYSTEM, APPARATUS AND METHOD FOR VOLTAGE TO CURRENT CONVERSION
6799153	20-Apr-20	CROSS COUPLING DELAY CHARACTERIZATION FOR INTEGRATED CIRCUITS
6800946	27-Jan-23	SELECTIVE UNDERFILL FOR OPTO-ELECTRONIC FLIP CHIPS AND FLIP-CHIP ASSEMBLIES
6801322	23-Jul-22	METHOD AND APPARATUS FOR MEASURING A REQUIRED FEATURE OF A LAYER DURING A POLISHING PROCESS
6801415	24-Jan-23	NANOCRYSTALLINE LAYERS FOR IMPROVED MRAM TUNNEL JUNCTIONS
6803248	12-Sep-22	A CHEMISTRY FOR ETCHING QUATERNARY INTERFACE LAYERS ON InGaAsP MOSTLY FORMED BETWEEN GaAs AND InxGa(1-x) P LAYERS
6803302	30-Sep-20	METHOD FOR FORMING A SEMICONDUCTOR DEVICE HAVING A MECHANICALLY ROBUST PAD INTERFACE
6803323	10-Jun-22	METHOD OF FORMING A COMPONENT OVERLYING A SEMICONDUCTOR SUBSTRATE
6803832	06-Sep-22	OSCILLATOR CIRCUIT HAVING REDUCED LAYOUT AREA AND LOWER POWER SUPPLY TRANSIENTS
6803836	27-Sep-22	MULTILAYER CERAMIC PACKAGE TRANSMISSION LINE PROBE
6804501	25-Sep-20	RECEIVER HAVING GAIN CONTROL AND NARROWBAND INTERFERENCE DETECTION
6806127	05-Dec-22	METHOD AND STRUCTURE FOR CONTACTING AN OVERLYING

6806202	03-Dec-22	ELECTRODE FOR A MAGNETOELECTRONICS ELEMENT METHOD OF REMOVING SILICON OXIDE FROM A SURFACE OF A SUBSTRATE
6808986	16-Jun-20	METHOD OF FORMING NANOCRYSTALS IN A MEMORY DEVICE
6809593	28-Apr-23	POWER AMPLIFIER DEVICE AND METHOD THEREOF
6810078	08-Dec-20	BLIND RATE DETERMINATION
6810266	16-Nov-19	DIGITALLY CONTROLLED RADIO BACK-END
6811714	30-Dec-21	MICROMACHINED COMPONENT AND METHOD OF MANUFACTURE
6811936	04-Apr-23	STRUCTURE AND PROCESS FOR A PELLICLE MEMBRANE FOR 157 NANOMETER LITHOGRAPHY
6812040	17-May-22	A METHOD OF FABRICATING A SELF-ALIGNED VIA CONTACT FOR A MAGNETIC MEMORY ELEMENT
6812517	30-Aug-22	DIELECTRIC STORAGE MEMORY CELL HAVING HIGH PERMITTIVITY TOP DIELECTRIC AND METHOD THEREFOR
6812580	09-Jun-23	SEMICONDUCTOR PACKAGE HAVING OPTIMIZED WIRE BOND POSITIONING
6812762	06-Sep-22	FAST MONO-CYCLE GENERATING CIRCUIT USING FULL RAIL SWING LOGIC CIRCUITS
6812802	06-Jun-23	METHOD AND APPARATUS FOR CONTROLLING A VOLTAGE CONTROLLED OSCILLATOR
6813666	24-Jun-22	SCALEABLE ARBITRATION AND PRIORITIZATION OF MULTIPLE INTERRUPTS
6813762	22-Feb-20	METHOD FOR PROCESSING PROGRAM FILES
6815254	21-Apr-23	SEMICONDUCTOR PACKAGE WITH MULTIPLE SIDES HAVING PACKAGE CONTACTS
6815780	15-Apr-23	SEMICONDUCTOR COMPONENT AND METHOD OF MANUFACTURING SAME
6815820	25-May-22	MULTIPLE THICKNESS SEMICONDUCTOR INTERCONNECT AND METHOD THEREFOR
6816414	31-Jul-23	NONVOLATILE MEMORY AND METHOD OF MAKING SAME
6817602	13-Jul-22	ELECTRONIC DEVICE FOR A LITHOGRAPHY MASK CONTAINER, SEMICONDUCTOR MANUFACTURING SYSTEM, AND METHOD
6818362	19-Feb-24	PHOTOLITHOGRAPHY RETICLE DESIGN
6818493	26-Jul-21	SELECTIVE METAL OXIDE REMOVAL
6818961	30-Jun-23	OBLIQUE DEPOSITION TO INDUCE MAGNETIC ANISOTROPY FOR MRAM CELLS
6819131	24-Dec-22	PASSIVE, GREASE-FREE COOLED DEVICE FIXTURES
6819200	26-Jul-22	BROADBAND BALUN AND IMPEDANCE TRANSFORMER FOR PUSH-PULL AMPLIFIERS
6819538	05-Sep-22	METHOD AND APPARATUS FOR CONTROLLING CURRENT DEMAND IN AN INTEGRATED CIRCUIT
6819912	27-Mar-22	VARIABLE FREQUENCY SWITCHING AMPLIFIER AND METHOD THEREOF
6821082	26-Jun-22	WAFER MANAGEMENT SYSTEM AND METHODS FOR MANAGING WAFERS
6821829	05-Sep-20	METHOD OF MANUFACTURING A SEMICONDUCTOR COMPONENT AND SEMICONDUCTOR COMPONENT THEREOF
6821878	27-Feb-23	AREA-ARRAY DEVICE ASSEMBLY WITH PRE-APPLIED UNDERFILL LAYERS ON PRINTED WIRING BOARD

6823070	28-Mar-20	METHOD FOR KEY ESCROW IN A COMMUNICATION SYSTEM AND APPARATUS THEREFOR
6823224	13-May-22	DATA PROCESSING SYSTEM HAVING AN ON-CHIP BACKGROUND DEBUG SYSTEM AND METHOD THEREFOR
6825092	21-Aug-20	SEMICONDUCTOR DEVICE HAVING PASSIVE ELEMENTS AND METHOD OF MAKING SAME
6825641	22-Jan-23	HIGH EFFICIENCY ELECTRICAL SWITCH AND DC-DC CONVERTER INCORPORATING SAME
6825716	30-Apr-22	SYSTEM AND APPARATUS FOR REDUCING OFFSET VOLTAGES IN FOLDING AMPLIFIERS
6825727	27-Jun-23	RADIO FREQUENCY POWER TRANSISTOR AVALANCHE BREAKDOWN DETECTION CIRCUIT AND METHOD THEREFOR
6825736	17-Jul-23	METHOD AND APPARATUS FOR CONTROLLING A VOLTAGE CONTROLLED OSCILLATOR
6826103	30-Oct-22	AUTO-TUNEABLE REFERENCE CIRCUIT FOR FLASH EEPROM PRODUCTS
6826188	20-Dec-22	METHOD AND CIRCUIT FOR FORMING AN ATM CELL
6826691	15-Nov-19	ARRANGEMENT FOR ENCRYPTION/DECRYPTION OF DATA AND DATA CARRIER INCORPORATING SAME
6828618	30-Oct-22	SPLIT-GATE THIN-FILM STORAGE NVM CELL
6828650	31-May-22	BIPOLAR JUNCTION TRANSISTOR STRUCTURE WITH IMPROVED CURRENT GAIN CHARACTERISTICS
6829158	06-Apr-23	MAGNETORESISTIVE LEVEL GENERATOR AND METHOD
6831310	10-Nov-23	INTEGRATED CIRCUIT HAVING MULTIPLE MEMORY TYPES AND METHOD OF FORMATION
6831312	30-Aug-22	AMORPHOUS ALLOYS FOR MAGNETIC DEVICES
6831350	02-Oct-23	SEMICONDUCTOR STRUCTURE WITH DIFFERENT LATTICE CONSTANT MATERIALS AND METHOD FOR FORMING THE SAME
6832280	13-Jan-23	DATA PROCESSING SYSTEM HAVING AN ADAPTIVE PRIORITY CONTROLLER
6833761	28-Feb-23	AMPLIFIER APPARATUS AND METHOD THEREOF
6834073	11-Oct-22	SYSTEM AND METHOD FOR BASEBAND REMOVAL OF NARROWBAND INTERFERENCE IN ULTRA WIDEBAND SIGNALS
6834086	29-Nov-22	PHASE DETECTOR AND METHOD FOR SECOND HARMONIC CANCELLATION
6834216	03-Feb-22	METHOD AND APPARATUS FOR THE AUTOMATIC SYNCHRONIZATION OF DYNAMIC ANGULAR AND TIME DOMAIN CONTROL SYSTEMS
6835423	06-Jul-20	MAGNETIC ELEMENT WITH INSULATING VEILS AND FABRICATING METHOD THEREOF
6835671	29-Sep-22	METHOD OF MAKING AN INTEGRATED CIRCUIT USING AN EUV MASK FORMED BY ATOMIC LAYER DEPOSITION
6836435	09-Jan-23	COMPACTION SCHEME IN NVM
6837293	16-Jun-23	HEATED NOZZLE ASSEMBLY
6838322	01-May-23	METHOD FOR FORMING A DOUBLE-GATED SEMICONDUCTOR DEVICE
6838332	15-Aug-23	METHOD FOR FORMING A SEMICONDUCTOR DEVICE HAVING ELECTRICAL CONTACT FROM OPPOSITE SIDES
6838354	20-Dec-22	METHOD FOR FORMING A PASSIVATION LAYER FOR AIR GAP FORMATION AND STRUCTURE THEREOF
6838721	25-Apr-23	INTEGRATED CIRCUIT WITH A TRANSISTOR OVER AN

		INTERCONNECT LAYER
6838751	06-Mar-22	MULTI-ROW LEADFRAME
6838776	18-Apr-23	CIRCUIT DEVICE WITH AT LEAST PARTIAL PACKAGING AND METHOD FOR FORMING
6838930	27-Nov-22	SWITCHED CAPACITOR AMPLIFIER WITH HIGH THROUGHPUT ARCHITECTURE
6839011	31-Mar-23	SYSTEM AND METHOD OF FILTERING
6839280	27-Jun-23	VARIABLE GATE BIAS FOR A REFERENCE TRANSISTOR IN A NON-VOLATILE MEMORY
6839381	12-Jan-20	METHOD AND APPARATUS FOR COHERENT DETECTION IN A TELECOMMUNICATIONS SYSTEM
6840106	14-Aug-23	SENSOR USING AN ACTUATOR FOR SELF-TEST AND METHOD THEREFOR
6841736	26-Sep-22	CURRENT-CARRYING ELECTRONIC COMPONENT AND METHOD OF MANUFACTURING SAME
6841869	21-Jun-19	ELECTRONIC ASSEMBLY PACKAGE
6842365	05-Sep-23	WRITE DRIVER FOR A MAGNETO-RESISTIVE MEMORY
6842822	25-Jan-23	SYSTEM AND METHOD FOR CACHE EXTERNAL WRITING
6842888	23-Apr-22	METHOD AND APPARATUS FOR HIERARCHICALLY RESTRUCTURING PORTIONS OF A HIERARCHICAL DATABASE BASED ON SELECTED ATTRIBUTES
6842895	27-Aug-22	SINGLE INSTRUCTION FOR MULTIPLE LOOP
6844221	04-Jan-22	WIRE BOND-LESS ELECTRONIC COMPONENT FOR USE WITH AN EXTERNAL CIRCUIT AND METHOD OF MANUFACTURE
6844224	15-Nov-21	SUBSTRATE CONTACT IN SOI AND METHOD THEREFOR
6844588	15-Aug-20	NON-VOLATILE MEMORY AND METHOD OF FORMING THEREOF
6844597	10-Feb-23	LOW VOLTAGE NMOS-BASED ELECTROSTATIC DISCHARGE CLAMP
6844631	13-Mar-22	SEMICONDUCTOR DEVICE HAVING A BOND PAD AND METHOD THEREFOR
6844762	07-Nov-22	CAPACITIVE CHARGE PUMP
6845233	25-Jul-23	RF RECEIVERS WITH REDUCED SPURIOUS RESPONSE FOR MOBILE STATIONS AND METHODS THEREFOR
6845419	24-Jan-20	FLEXIBLE INTERRUPT CONTROLLER THAT INCLUDES AN INTERRUPT FORCE REGISTER
6845670	08-Jul-23	SINGLE PROOF MASS, 3 AXIS MEMS TRANSDUCER
6846716	19-Mar-22	INTEGRATED CIRCUIT DEVICE AND METHOD THEREFOR
6846717	13-Mar-22	SEMICONDUCTOR DEVICE HAVING A WIRE BOND PAD AND METHOD THEREFOR
6847102	08-Nov-22	SEMICONDUCTOR DEVICE AND METHOD THEREFOR
6847548	29-Jun-23	MEMORY WITH MULTIPLE STATE CELLS AND SENSING METHOD
6847990	19-Jul-23	DATA TRANSFER UNIT WITH SUPPORT FOR MULTIPLE COHERENCY GRANULES
6848030	20-Jul-21	METHOD AND APPARATUS FOR FILLING LINES IN A CACHE
6849487	21-Feb-24	METHOD FOR FORMING AN ELECTRONIC STRUCTURE USING ETCH
6849515	25-Sep-23	SEMICONDUCTOR PROCESS FOR DISPOSABLE SIDEWALL SPACERS AND STRUCTURE
6850733	10-Oct-20	CARRIERLESS ULTRA WIDEBAND WIRELESS SIGNALS FOR

		CONVEYING DATA
6852454	07-Feb-23	MULTI-TIERED LITHOGRAPHIC TEMPLATE AND METHOD OF FORMATION AND USE
6852588	30-Jun-24	METHODS OF FABRICATING SEMICONDUCTOR STRUCTURES COMPRISING EPITAXIAL HF3SI2 LAYERS
6853586	21-May-23	NON-VOLATILE MEMORY ARCHITECTURE AND METHOD THEREOF
6854637	20-Feb-23	WIREBONDING INSULATED WIRE
6855965	06-May-21	METHOD OF MANUFACTURING A SEMICONDUCTOR COMPONENT AND SEMICONDUCTOR COMPONENT THEREOF
6855979	20-Mar-23	MULTI-BIT NON-VOLATILE MEMORY DEVICE AND METHOD THEREFOR
6855992	24-Jul-21	STRUCTURE AND METHOD FOR FABRICATING CONFIGURABLE TRANSISTOR DEVICES UTILIZING THE FORMATION OF A COMPLIANT SUBSTRATE FOR MATERIALS USED TO FORM THE SAME
6856173	12-Sep-23	MULTIPLEXING OF DIGITAL SIGNALS AT MULTIPLE SUPPLY VOLTAGES IN AN INTEGRATED CIRCUIT
6856266	09-Sep-22	MULTI-RATE ANALOG-TO-DIGITAL CONVERTER
6857063	30-Oct-22	DATA PROCESSOR AND METHOD OF OPERATION
6858542	22-Feb-23	SEMICONDUCTOR FABRICATION METHOD FOR MAKING SMALL FEATURES
6858932	07-Feb-22	PACKAGED SEMICONDUCTOR DEVICE AND METHOD OF FORMATION
6859388	05-Sep-23	CIRCUIT FOR WRITE FIELD DISTURBANCE CANCELLATION IN AN MRAM AND METHOD OF OPERATION
6859506	24-Oct-22	ULTRA WIDEBAND COMMUNICATION SYSTEM, METHOD, AND DEVICE WITH LOW NOISE RECEPTION
6859875	07-Feb-22	PROCESSOR HAVING SELECTIVE BRANCH PREDICTION
6861689	27-Mar-23	ONE TRANSISTOR DRAM CELL STRUCTURE AND METHOD FOR FORMING
6861817	01-Mar-22	METHOD AND APPARATUS FOR DETECTING A STALL CONDITION IN A STEPPING MOTOR
6862208	11-Apr-23	MEMORY DEVICE WITH SENSE AMPLIFIER AND SELF-TIMED LATCH
6862240	19-Feb-23	VARIABLE REFRESH CONTROL FOR A MEMORY
6862283	29-Mar-23	METHOD AND APPARATUS FOR MAINTAINING PACKET ORDERING WITH ERROR RECOVERY AMONG MULTIPLE OUTSTANDING PACKETS BETWEEN TWO DEVICES
6864135	31-Oct-22	SEMICONDUCTOR FABRICATION PROCESS USING TRANSISTOR SPACERS OF DIFFERING WIDTHS
6864758	30-Apr-22	APPARATUS AND RESONANT CIRCUIT EMPLOYING A VARACTOR DIODE IN PARALLEL WITH A TRANSMISSION LINE AND METHOD THEREOF
6864817	30-Dec-23	SIGNALING DEPENDENT ADAPTIVE ANALOG-TO-DIGITAL CONVERTER (ADC) SYSTEM AND METHOD OF USING SAME
6865667	24-Sep-22	DATA PROCESSING SYSTEM HAVING REDIRECTING CIRCUITRY AND METHOD THEREOF
6867072	07-Jan-24	FLIPCHIP QFN PACKAGE AND METHOD THEREFOR
6867078	19-Nov-23	METHOD FOR FORMING A MICROWAVE FIELD EFFECT TRANSISTOR WITH HIGH OPERATING VOLTAGE
6868129	07-Apr-23	DEMODULATOR FOR A RADIO RECEIVER AND METHOD OF

		OPERATION
6868431	25-Oct-19	CIRCUIT AND METHOD FOR PROCESSING DATA
6870219	31-Jul-22	FIELD EFFECT TRANSISTOR AND METHOD OF MANUFACTURING SAME
6870243	27-Nov-22	THIN GAAS WITH COPPER BACK-METAL STRUCTURE
6870444	24-Sep-23	ELECTROMECHANICAL RESONATOR AND METHOD OF OPERATING SAME
6871176	15-Sep-23	PHASE EXCITED LINEAR PREDICTION ENCODER
6871246	07-May-23	PREFETCH CONTROL IN A DATA PROCESSING SYSTEM
6872958	05-Apr-20	PLATFORM POSITIONING SYSTEM
6873218	16-Jul-21	FREQUENCY MODULATOR USING A WAVEFORM GENERATOR
6875546	05-Jun-23	METHOD OF PATTERNING PHOTORESIST ON A WAFER USING AN ATTENUATED PHASE SHIFT MASK
6875635	04-Mar-22	METHOD OF ATTACHING A DIE TO A SUBSTRATE
6877123	24-Mar-23	SCAN CLOCK CIRCUIT AND METHOD THEREFOR
6878633	17-Mar-23	FLIP-CHIP STRUCTURE AND METHOD FOR HIGH QUALITY INDUCTORS AND TRANSFORMERS
6879028	21-Feb-23	MULTI-DIE SEMICONDUCTOR PACKAGE
6879476	13-Jun-23	ELECTROSTATIC DISCHARGE CIRCUIT AND METHOD THEREFOR
6880134	08-May-23	METHOD FOR IMPROVING CAPACITOR NOISE AND MISMATCH CONSTRAINTS IN A SEMICONDUCTOR DEVICE
6881351	24-Jun-23	METHODS FOR CONTACTING CONDUCTING LAYERS OVERLYING MAGNETOELECTRONIC ELEMENTS OF MRAM DEVICES
6881681	22-Nov-22	FILM DEPOSITION ON A SEMICONDUCTOR WAFER
6882023	31-Oct-22	SEMICONDUCTOR COMPONENT AND METHOD OF MANUFACTURING SAME
6882582	29-Mar-22	EEPROM CIRCUIT VOLTAGE REFERENCE CIRCUIT AND METHOD FOR PROVIDING A LOW TEMPERATURE-COEFFICIENT VOLTAGE REFERENCE
6882745	19-Dec-22	METHOD AND APPARATUS FOR TRANSLATING DETECTED WAFER DEFECT COORDINATES TO RETICLE COORDINATES USING CAD DATA
6884685	14-Feb-23	RADICAL OXIDATION AND/OR NITRIDATION DURING METAL OXIDE LAYER DEPOSITION PROCESS
6884727	29-Aug-22	SEMICONDUCTOR FABRICATION PROCESS FOR MODIFYING THE PROFILES OF PATTERNED FEATURES
6885065	20-Nov-22	A FERROMAGNETIC SEMICONDUCTOR STRUCTURE AND METHOD FOR FORMING THE SAME
6885074	27-Nov-22	CLADDED CONDUCTOR FOR USE IN A MAGNETOELECTRONICS DEVICE AND METHOD FOR FABRICATING THE SAME
6885093	28-Mar-22	STACKED DIE SEMICONDUCTOR DEVICE
6887138	20-Jun-23	CHEMICAL MECHANICAL POLISH (CMP) CONDITIONING-DISK HOLDER
6887758	09-Oct-22	NON-VOLATILE MEMORY DEVICE AND METHOD FOR FORMING
6888246	03-Jan-22	SEMICONDUCTOR POWER DEVICE WITH SHEAR STRESS COMPENSATION
6888743	24-Mar-23	MRAM ARCHITECTURE
6889036	04-Jun-23	INTEGRATED FREQUENCY SELECTABLE RESONANT COUPLING

		NETWORK AND METHOD THEREOF
6889427	12-Aug-22	PROCESS AND APPARATUS FOR DISENGAGING SEMICONDUCTOR DIE FROM AN ADHESIVE FILM
6890688	06-Feb-22	LITHOGRAPHIC TEMPLATE AND METHOD OF FORMATION AND USE
6890770	16-Apr-23	MAGNETORESISTIVE RANDOM ACCESS MEMORY DEVICE STRUCTURES AND METHODS FOR FABRICATING THE SAME
6890816	07-Feb-23	COMPOUND SEMICONDUCTOR STRUCTURE INCLUDING AN EPITAXIAL PEROVSKITE LAYER AND METHOD FOR FABRICATING SEMICONDUCTOR STRUCTURES AND DEVICES
6891229	24-Jul-23	INVERTED ISOLATION FORMED WITH SPACERS
6891846	18-Oct-23	METHOD AND APPARATUS FOR A TRAFFIC SHAPER
6892260	05-Mar-23	INTERRUPT PROCESSING IN A DATA PROCESSING SYSTEM
6892339	20-Sep-19	DISCRETE MULTI-TONE (DMT) SYSTEM AND METHOD THAT COMMUNICATES A DATA PUMP DATA STREAM BETWEEN A GENERAL PURPOSE CPU AND A DSP VIA A BUFFERING SCHEME
6893947	21-Nov-22	ADVANCED RF ENHANCEMENT-MODE FETS WITH IMPROVED GATE PROPERTIES
6894353	22-Sep-22	CAPPED DUAL METAL GATE TRANSISTOR FOR CMOS PROCESS AND METHOD FOR MAKING THE SAME
6894540	17-Dec-23	GLITCH REMOVAL CIRCUIT
6894937	19-Nov-23	ACCELERATED LIFE TEST OF MRAM CELLS
6895294	28-Nov-22	ASSEMBLY COMPRISING A PLURALITY OF MASK CONTAINERS, MANUFACTURING SYSTEM FOR MANUFACTURING SEMICONDUCTOR DEVICES, AND METHOD
6895530	06-Jun-23	METHOD AND APPARATUS FOR CONTROLLING A DATA PROCESSING SYSTEM DURING DEBUG
6895596	23-Dec-17	CIRCUIT AND METHOD FOR INTERLEAVING A DATA STREAM
6897095	12-May-24	SEMICONDUCTOR PROCESS AND INTEGRATED CIRCUIT HAVING DUAL METAL OXIDE GATE DIELECTRIC WITH SINGLE METAL GATE ELECTRODE
6897562	11-Apr-23	ELECTRONIC COMPONENT AND METHOD OF MANUFACTURING SAME
6898112	29-Apr-23	SYNTHETIC ANTIFERROMAGNETIC STRUCTURE FOR MAGNETOELECTRONIC DEVICES
6898128	18-Jul-23	PROGRAMMING OF A MEMORY WITH DISCRETE CHARGE STORAGE ELEMENTS
6898129	27-Mar-23	ERASE OF A MEMORY HAVING A NON-CONDUCTIVE STORAGE MEDIUM
6898682	07-Aug-23	AUTOMATIC READ LATENCY CALCULATION WITHOUT SOFTWARE INTERVENTION FOR A SOURCE-SYNCHRONOUS INTERFACE
6900105	23-Apr-20	SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURE
6900531	25-Oct-22	IMAGE SENSOR DEVICE
6900970	13-Jun-23	ELECTROSTATIC DISCHARGE CIRCUIT AND METHOD THEREFOR
6901112	23-Mar-18	ULTRA WIDE BANDWIDTH SPREAD-SPECTRUM COMMUNICATIONS SYSTEM
6902440	21-Oct-23	METHOD OF FORMING A LOW K DIELECTRIC IN A SEMICONDUCTOR MANUFACTURING PROCESS
6902969	07-Aug-23	PROCESS FOR FORMING DUAL METAL GATE STRUCTURES

6902971	04-Sep-23	TRANSISTOR SIDEWALL SPACER STRESS MODULATION
6902986	25-Jun-23	METHOD FOR DEFINING ALIGNMENT MARKS IN A SEMICONDUCTOR WAFER
6903004	11-Sep-23	METHOD OF MAKING A SEMICONDUCTOR DEVICE HAVING A LOW K DIELECTRIC
6903964	05-Aug-22	MRAM ARCHITECTURE WITH ELECTRICALLY ISOLATED READ AND WRITE CIRCUITRY
6903967	22-May-23	MEMORY WITH CHARGE STORAGE LOCATIONS
6904446	18-May-23	FLOATING POINT MULTIPLIER/ACCUMULATOR WITH REDUCED LATENCY AND METHOD THEREOF
6905392	30-Jun-23	POLISHING SYSTEM HAVING A CARRIER HEAD WITH SUBSTRATE PRESENCE SENSING
6905891	28-Feb-22	METHOD FOR PROCESSING MULTIPLE SEMICONDUCTOR DEVICES FOR TEST
6905910	06-Jan-24	METHOD OF PACKAGING AN OPTICAL SENSOR
6905967	31-Mar-23	METHOD FOR IMPROVING PLANARITY OF SHALLOW TRENCH ISOLATION USING MULTIPLE SIMULTANEOUS TILING SYSTEMS
6906302	02-Dec-22	PIXEL SENSOR CIRCUIT DEVICE AND METHOD THEREOF
6906381	05-Aug-21	LATERAL SEMICONDUCTOR DEVICE WITH LOW ON-RESISTANCE AND METHOD OF MAKING THE SAME
6906406	20-Dec-22	MULTIPLE DICE PACKAGE
6906582	29-Aug-23	CIRCUIT VOLTAGE REGULATION
6906900	10-Dec-23	STRUCTURE AND METHOD OF THERMALLY PROTECTING POWER DEVICES FOR AIR-BAG DEPLOYMENT
6908822	15-Sep-23	SEMICONDUCTOR DEVICE HAVING AN INSULATING LAYER AND METHOD FOR FORMING
6908852	29-Jan-23	METHOD OF FORMING AN ARC LAYER FOR A SEMICONDUCTOR DEVICE
6909320	19-Jun-23	METHOD AND APPARATUS FOR DUAL OUTPUT VOLTAGE REGULATION
6909393	12-Oct-23	SPACE EFFICIENT LOW POWER CYCLIC A/D CONVERTER
6909631	04-Apr-23	MRAM AND METHODS FOR READING THE MRAM
6909638	17-Jun-23	NON-VOLATILE MEMORY HAVING A BIAS ON THE SOURCE ELECTRODE FOR HCI PROGRAMMING
6909877	10-Oct-20	CARRIERLESS ULTRA WIDEBAND WIRELESS SIGNALS FOR CONVEYING DATA
6910025	24-Jan-23	MODELING BEHAVIOR OF AN ELECTRICAL CURRENT
6911156	01-Oct-23	METHODS FOR FABRICATING MRAM DEVICE STRUCTURES
6911360	10-May-23	FUSE AND METHOD FOR FORMING
6912107	28-Apr-20	MAGNETIC ELEMENT WITH INSULATING VEILS AND FABRICATING METHOD THEREOF
6912372	17-Nov-21	ULTRA WIDEBAND SIGNALS FOR CONVEYING DATA
6913941	15-Dec-22	SOI POLYSILICON TRENCH REFILL PERIMETER OXIDE ANCHOR SCHEME
6914006	28-Jun-22	WAFER SCRIBING METHOD AND WAFER SCRIBING DEVICE
6914012	18-Jan-22	ARTICLE COMPRISING AN OXIDE LAYER ON A GAAS-BASED SEMICONDUCTOR STRUCTURE AND METHOD OF FORMING SAME
6914573	07-Aug-20	ELECTRICALLY SMALL PLANAR UWB ANTENNA APPARATUS AND RELATED SYSTEM

6914935	03-Feb-23	FRACTIONAL N SYNTHESIZER WITH REDUCED FRACTIONALIZATION SPURS
6916669	15-Nov-20	SELF-ALIGNED MAGNETIC CLAD WRITE LINE AND ITS METHOD OF FORMATION
6916682	08-Nov-21	SEMICONDUCTOR PACKAGE DEVICE FOR USE WITH MULTIPLE INTEGRATED CIRCUITS IN A STACKED CONFIGURATION AND METHOD OF FORMATION AND TESTING
6916717	22-Jun-22	METHOD FOR GROWING A MONOCRYSTALLINE OXIDE LAYER AND FOR FABRICATING A SEMICONDUCTOR DEVICE ON A MONOCRYSTALLINE SUBSTRATE
6916728	23-Dec-22	SEMICONDUCTOR STRUCTURE AND METHOD OF MANUFACTURE
6917097	28-Sep-23	DUAL GAUGE LEADFRAME
6917555	31-Dec-23	INTEGRATED CIRCUIT POWER MANAGEMENT FOR REDUCING LEAKAGE CURRENT IN CIRCUIT ARRAYS AND METHOD THEREFOR
6919244	10-Mar-24	METHOD OF MAKING A SEMICONDUCTOR DEVICE, AND SEMICONDUCTOR DEVICE MADE THEREBY
6919258	02-Oct-23	SEMICONDUCTOR DEVICE INCORPORATING A DEFECT CONTROLLED STRAINED CHANNEL STRUCTURE AND METHOD OF MAKING THE SAME
6919590	29-Aug-23	SEMICONDUCTOR COMPONENT AND METHOD OF MANUFACTURING SAME
6920316	23-Apr-23	HIGH PERFORMANCE INTEGRATED CIRCUIT REGULATOR WITH SUBSTRATE TRANSIENT SUPPRESSION
6920586	23-Jan-24	REAL-TIME DEBUG SUPPORT FOR A DMA DEVICE AND METHOD THEREOF
6921700	02-Oct-23	METHOD OF FORMING A TRANSISTOR HAVING MULTIPLE CHANNELS
6921961	15-Aug-23	SEMICONDUCTOR DEVICE HAVING ELECTRICAL CONTACT FROM OPPOSITE SIDES INCLUDING A VIA WITH AN END FORMED AT A BOTTOM SURFACE OF THE DIFFUSION REGION
6921975	18-Apr-23	CIRCUIT DEVICE WITH AT LEAST PARTIAL PACKAGING, EXPOSED ACTIVE SURFACE AND A VOLTAGE REFERENCE PLANE
6921979	13-Mar-22	SEMICONDUCTOR DEVICE HAVING A BOND PAD AND METHOD THEREFOR
6922100	29-Jul-23	METHOD AND APPARATUS FOR SWITCHING AMPLIFICATION HAVING VARIABLE SAMPLE POINT AND VARIABLE ORDER CORRECTION
6924172	24-Sep-23	METHOD OF FORMING A BOND PAD
6924184	28-May-23	SEMICONDUCTOR DEVICE AND METHOD FOR FORMING A SEMICONDUCTOR DEVICE USING POST GATE STACK PLANARIZATION
6924232	02-Sep-23	SEMICONDUCTOR PROCESS AND COMPOSITION FOR FORMING A BARRIER MATERIAL OVERLYING COPPER
6924697	27-Feb-23	SEMICONDUCTOR DEVICE AND METHOD THEREFOR
6925108	05-Apr-22	ULTRAWIDE BANDWIDTH SYSTEM AND METHOD FOR FAST SYNCHRONIZATION
6925542	21-Mar-23	MEMORY MANAGEMENT IN A DATA PROCESSING SYSTEM
6925622	22-Jan-23	SYSTEM AND METHOD FOR CORRELATED CLOCK NETWORKS
6927072	30-May-23	METHOD OF APPLYING CLADDING MATERIAL ON CONDUCTIVE LINES OF MRAM DEVICES

6927429	22-Mar-23	INTEGRATED CIRCUIT WELL BIAS CIRCUITRY
6927613	25-Dec-22	CIRCUIT GENERATING CONSTANT NARROW-PULSE-WIDTH BOPOLARITY CYCE MONOCYCLES USING CMOS CIRCUITS
6927722	06-Aug-23	SERIES CAPACITIVE COMPONENT FOR SWITCHED-CAPACITOR CIRCUITS CONSISTING OF SERIES-CONNECTED CAPACITORS
6928005	25-Feb-24	DOMINO COMPARATOR CAPABLE FOR USE IN A MEMORY ARRAY
6928409	14-Dec-22	SPEECH RECOGNITION USING POLYNOMIAL EXPANSION AND HIDDEN MARKOV MODELS
6930027	12-Sep-23	METHOD OF MANUFACTURING A SEMICONDUCTOR COMPONENT
6930032	21-Oct-22	UNDER BUMP METALLURGY STRUCTURAL DESIGN FOR HIGH RELIABILITY BUMPED PACKAGES
6930554	18-Jul-23	VARIABLE GAIN LOW NOISE AMPLIFIER AND METHOD
6931078	12-Dec-17	ULTRA WIDE BANDWIDTH SPREAD-SPECTRUM COMMUNICATIONS SYSTEM
6931241	28-Dec-22	DUAL DIGITAL LOW IF COMPLEX RECEIVER
6931611	06-Oct-22	DESIGN VERIFICATION SYSTEM FOR AVOIDING FALSE FAILURES AND METHOD THEREFOR
6933227	23-Oct-23	SEMICONDUCTOR DEVICE AND METHOD OF FORMING THE SAME
6933523	12-Jun-23	SEMICONDUCTOR ALIGNMENT AID
6933546	29-Oct-23	SEMICONDUCTOR COMPONENT AND METHOD FOR MANUFACTURING SAME
6933599	27-Oct-23	ELECTROMATIC NOISE SHIELDING IN SEMICONDUCTOR PACKAGES USING CAGED INTERCONNECT STRUCTURES
6933614	20-Sep-23	INTEGRATED CIRCUIT DIE HAVING A COPPER CONTACT AND METHOD THEREFOR
6933766	16-Jul-21	APPARATUS AND METHOD FOR IMPROVED CHOPPING MIXER
6933772	07-Feb-24	VOLTAGE REGULATOR WITH IMPROVED LOAD REGULATION USING ADAPTIVE BIASING
6936492	08-Jul-23	SINGLE PROOF MASS, 3 AXIS MEMS TRANSDUCER
6936763	07-Mar-23	MAGNETIC SHIELDING FOR ELECTRONIC CIRCUITS WHICH INCLUDE MAGNETIC MATERIALS
6936896	21-Dec-21	SEMICONDUCTOR APPARATUS
6937047	02-Dec-23	INTEGRATED CIRCUIT WITH TEST PAD STRUCTURE AND METHOD OF TESTING
6937089	30-Dec-23	OFFSET, DELAY AND PARASITICALLY IMMUNE RESISTOR-CAPACITOR (RC) TRACKING LOOP AND METHOD OF USING SAME
6937646	27-Dec-22	LEAKAGE NULLING RECEIVER CORRELATOR STRUCTURE AND METHOD FOR ULTRA WIDE BANDWIDTH COMMUNICATION SYSTEM
6937961	26-Dec-22	PERFORMANCE MONITOR AND METHOD THEREFOR
6939650	06-Aug-23	METHOD OF PATTERNING PHOTORESIST ON A WAFER USING A TRANSMISSION MASK WITH A CARBON LAYER
6939767	19-Nov-23	MULTI-BIT NON-VOLATILE INTEGRATED CIRCUIT MEMORY AND METHOD THEREFOR
6939781	27-Jun-23	METHOD OF MANUFACTURING A SEMICONDUCTOR COMPONENT THAT INCLUDES SELF-ALIGNING A GATE ELECTRODE TO A FIELD PLATE
6943038	06-Jan-23	METHOD FOR FABRICATING A FLUX CONCENTRATING SYSTEM

		FOR USE IN A MAGNETOELECTRONICS DEVICE
6943289	18-Dec-23	SLOTTED PLANAR POWER CONDUCTOR
6943650	02-Oct-23	ELECTROMAGNETIC BAND GAP MICROWAVE FILTER
6944052	15-Feb-23	MAGNETORESISTIVE RANDOM ACCESS MEMORY (MRAM) CELL HAVING A DIODE WITH ASYMMETRICAL CHARACTERISTICS
6944755	11-Dec-23	METHOD AND APPARATUS FOR EXTRACTING A PORTION OF DATA IN A SOURCE REGISTER AND ARRANGING IT ON ONE SIDE OF A DESTINATION REGISTER
6944806	14-Aug-23	METHOD AND APPARATUS TO DATA LOG AT-SPEED MARCH C+ MEMORY BIST
6946697	05-Jan-24	SYNTHETIC ANTIFERROMAGNET STRUCTURES FOR USE IN MTJS IN MRAM TECHNOLOGY
6948164	14-Dec-18	METHOD AND SYSTEM FOR MODIFYING EXECUTABLE CODE TO ADD ADDITIONAL FUNCTIONALITY
6949398	16-Jul-23	LOW COSE FABRICATION AND ASSEMBLY OF LID FOR SEMICONDUCTOR DEVICES
6949455	19-Dec-22	METHOD FOR FORMING A SEMICONDUCTOR DEVICE STRUCTURE IN A SEMICONDUCTOR LAYER
6949816	07-May-23	SEMICONDUCTOR COMPONENT HAVING FIRST SURFACE AREA FOR ELECTRICALLY COUPLING TO A SEMICONDUCTOR CHIP AND SECOND SURFACE AREA FOR ELECTRICALLY COUPLING TO A SUBSTRATE, AND METHOD OF MANUFACTURING SAME
6950476	04-Apr-23	APPARATUS AND METHOD FOR PERFORMING SISO DECODING
6950634	13-Nov-23	TRANSCEIVER CIRCUIT ARRANGEMENT AND METHOD
6950910	09-Jan-23	MOBILE WIRELESS COMMUNICATION DEVICE ARCHITECTURES AND METHODS THEREFOR
6951783	09-Jan-24	CONFINED SPACERS FOR DOUBLE GATE TRANSISTOR SEMICONDUCTOR FABRICATION PROCESS
6951801	13-Mar-23	METAL REDUCTION IN WAFER SCRIBE AREA
6952572	23-Jan-24	IMAGE REJECTION MIXER WITH SWITCHABLE HIGH OR LOW SIDE INJECTION
6952812	20-Sep-22	DESIGN ANALYSIS TOOL FOR PATH EXTRACTION AND FALSE PATH IDENTIFICATION AND METHOD THEREOF
6953738	12-Dec-23	METHOD AND APPARATUS FOR FORMING AN SOI BODY-CONTACTED TRANSISTOR
6953985	07-Aug-22	WAFER LEVEL MEMS PACKAGING
6954100	12-Sep-23	LEVEL SHIFTER
6954821	06-Apr-24	CROSSBAR SWITCH THAT SUPPORTS A MULTI-PORT SLAVE DEVICE AND METHOD OF OPERATION
6954826	10-Jul-23	READ ACCESS AND STORAGE CIRCUITRY READ ALLOCATION APPLICABLE TO A CACHE
6955967	17-Nov-23	NON-VOLATILE MEMORY HAVING A REFERENCE TRANSISTOR AND METHOD FOR FORMING
6956281	04-Nov-22	SEMICONDUCTOR DEVICE FOR REDUCING PHOTOVOLTAIC CURRENT
6956763	27-Jun-23	MRAM ELEMENT AND METHODS FOR WRITING THE MRAM ELEMENT
6956764	07-Dec-23	METHOD OF WRITING TO A MULTI-STATE MAGNETIC RANDOM ACCESS MEMORY CELL
6957054	03-Dec-23	RADIO RECEIVER HAVING A VARIABLE BANDWIDTH IF FILTER AND METHOD THEREFOR
6958261	09-Jan-23	OPTICAL SENSOR PACKAGE

6958265	21-Nov-23	SEMICONDUCTOR DEVICE WITH NANOCLOUDS
6958548	19-Nov-23	SEMICONDUCTOR DEVICE WITH MAGNETICALLY PERMEABLE HEAT SINK
6959014	16-Oct-23	METHOD AND APPARATUS FOR OPERATING A COMMUNICATION BUS
6959035	30-Jan-24	POST CORRELATION INTERPOLATION FOR DELAY LOCKED LOOPS
6959309	02-Apr-23	INTERFACE BETWEEN PROGRAMMING LANGUAGES AND METHOD THEREFOR
6960509	30-Jun-24	METHOD OF FABRICATING THREE DIMENSIONAL GATE STRUCTURE USING OXYGEN DIFFUSION
6961011	25-Mar-24	DATA COMPRESSION SYSTEM
6961423	24-Aug-23	METHOD AND APPARATUS FOR PERFORMING ADAPTIVE FILTERING
6961669	14-Nov-23	DE-EMBEDDING DEVICES UNDER TEST
6963090	09-Jan-23	ENHANCEMENT MODE METAL-OXIDE-SEMICONDUCTOR FIELD EFFECT TRANSISTOR
6963963	14-May-23	MEMORY MANAGEMENT IN A DATA PROCESSING SYSTEM
6964902	26-Feb-24	METHOD FOR REMOVING NANOCLOUDS FROM SELECTED REGIONS
6964911	23-Sep-23	METHOD FOR FORMING A SEMICONDUCTOR DEVICE HAVING ISOLATION REGIONS
6965128	27-Aug-23	STRUCTURE AND METHOD FOR FABRICATING SEMICONDUCTOR MICRORESONATOR DEVICES
6965357	16-Mar-24	LIGHT EMITTING ELEMENT DRIVING CIRCUIT
6965630	01-Jan-23	MODE CONTROLLER FOR SIGNAL ACQUISITION AND TRACKING IN AN ULTRA WIDEBAND COMMUNICATION SYSTEM
6965653	03-May-23	CIRCUIT AND METHOD FOR PROCESSING AN AUTOMATIC FREQUENCY CONTROL SIGNAL
6967143	31-Oct-23	SEMICONDUCTOR FABRICATION PROCESS WITH ASYMMETRICAL CONDUCTIVE SPACERS
6967158	07-Mar-23	METHOD OF FORMING A LOW-K DIELECTRIC STRUCTURE ON A SUBSTRATE
6967366	25-Aug-23	MAGNETORESISTIVE RANDOM ACCESS MEMORY WITH REDUCED SWITCHING FIELD VARIATION
6967390	29-Aug-23	ELECTRONIC COMPONENT AND METHOD OF MANUFACTURING SAME
6967611	12-Jun-24	OPTIMIZED REFERENCE VOLTAGE GENERATION USING SWITCHED CAPACITOR SCALING FOR DATA CONVERTERS
6967993	23-Nov-22	ULTRAWIDE BANDWIDTH SYSTEM AND METHOD FOR FAST SYNCHRONIZATION USING SUB-CODE SPINS
6969568	14-Mar-24	METHOD FOR ETCHING A QUARTZ LAYER IN A PHOTORESISTLESS SEMICONDUCTOR MASK
6969656	05-Dec-23	METHOD AND CIRCUIT FOR MULTIPLYING SIGNALS WITH A TRANSISTOR HAVING MORE THAN ONE INDEPENDENT GATE STRUCTURE
6969883	27-Jun-23	NON-VOLATILE MEMORY HAVING A REFERENCE TRANSISTOR
6970336	09-Jan-24	ELECTROSTATIC DISCHARGE PROTECTION CIRCUIT AND METHOD OF OPERATION
6972224	27-Mar-23	METHOD FOR FABRICATING DUAL-METAL GATE DEVICE
6972255	01-Aug-23	SEMICONDUCTOR DEVICE HAVING AN ORGANIC ANTI-REFLECTIVE COATING (ARC) AND METHOD THEREFOR

6972571	22-Mar-24	LOAD BOARD WITH EMBEDDED RELAY TRACKER
6973142	02-Jun-23	TIMING SYNCHRONIZATION FOR M-DPSK CHANNELS
6973417	05-Nov-19	METHOD AND SYSTEM FOR SIMULATING EXECUTION OF A TARGET PROGRAM IN A SIMULATED TARGET SYSTEM
6973471	07-Nov-23	METHOD AND APPARATUS FOR IMPLEMENTING SIGNED MULTIPLICATION OF OPERANDS HAVING DIFFERING BIT WIDTHS WITHOUT SIGN EXTENSION OF THE MULTIPLICAND
6973540	30-Jun-24	METHOD AND APPARATUS FOR SELECTING CACHE WAYS AVAILABLE FOR REPLACEMENT
6974776	01-Jul-23	ACTIVATION PLATE FOR ELECTROLESS AND IMMERSION PLATING OF INTEGRATED CIRCUITS
6975665	26-Mar-23	LOW POWER, HIGH RESOLUTION TIMING GENERATOR FOR ULTRA-WIDE BANDWIDTH COMMUNICATION SYSTEMS
6976110	01-Jun-24	METHOD AND APPARATUS FOR REDUCING INTERRUPT LATENCY BY DYNAMIC BUFFER SIZING
6978392	03-Oct-23	ENABLE PROPAGATION CONTROLLER
6979622	24-Aug-24	SEMICONDUCTOR TRANSISTOR HAVING STRUCTURAL ELEMENTS OF DIFFERING MATERIALS AND METHOD OF FORMATION
6979627	30-Apr-24	ISOLATION TRENCH
6980541	17-Oct-23	MEDIA ACCESS CONTROLLER HAVING PSEUDO-STATIC GUARANTEED TIME SLOTS
6982483	30-May-23	HIGH IMPEDANCE RADIO FREQUENCY POWER PLASTIC PACKAGE
6982605	05-Aug-23	TRANSFORMER COUPLED OSCILLATOR AND METHOD
6982689	08-Jun-24	LIGHT-EMITTING ELEMENT DRIVE APPARATUS
6986971	05-Apr-23	REFLECTIVE MASK USEFUL FOR TRANSFERRING A PATTERN USING EXTREME ULTRAVIOLET (EUV) RADIATION AND METHOD OF MAKING THE SAME
6986974	05-Apr-24	ATTENUATED PHASE SHIFT MASK FOR EXTREME ULTRAVIOLET LITHOGRAPHY AND METHOD THEREFORE
6987063	30-Jun-24	METHOD TO REDUCE IMPURITY ELEMENTS DURING SEMICONDUCTOR FILM DEPOSITION
6987423	10-Dec-23	TWO PORT VOLTAGE CONTROLLED OSCILLATOR FOR USE IN WIRELESS PERSONAL AREA NETWORK SYNTHESIZERS
6989229	28-Nov-23	NON-RESOLVING MASK TILING METHOD FOR FLARE REDUCTION
6990164	27-Apr-22	DUAL STEERED FREQUENCY SYNTHESIZER
6991984	27-Jan-24	METHOD FOR FORMING A MEMORY STRUCTURE USING A MODIFIED SURFACE TOPOGRAPHY AND STRUCTURE THEREOF
6992003	21-Apr-24	INTEGRATION OF ULTRA LOW K DIELECTRIC IN A SEMICONDUCTOR FABRICATION PROCESS
6992371	16-Jul-24	DEVICE INCLUDING AN AMORPHOUS CARBON LAYER FOR IMPROVED ADHESION OF ORGANIC LAYERS AND METHOD OF FABRICATION
6992377	23-Apr-24	SEMICONDUCTOR PACKAGE WITH CROSSING CONDUCTOR ASSEMBLY AND METHOD OF MANUFACTURE
6992568	21-Nov-22	PASSIVE RESPONSE COMMUNICATION SYSTEM
6993311	02-Jul-23	RADIO RECEIVER HAVING AN ADAPTIVE EQUALIZER AND METHOD THEREFOR
6993693	16-Nov-22	ANALOGUE/DIGITAL INTERFACE CIRCUIT

6995482	05-Jan-20	SWITCHING CIRCUIT AND METHOD THEREFOR
6995791	19-Feb-24	AUTOMATIC WHITE BALANCE FOR DIGITAL IMAGING
6996158	14-Feb-23	SIGNAL DETECTION USING A CDMA RECEIVER
6996651	16-Dec-23	ON CHIP NETWORK WITH MEMORY DEVICE ADDRESS DECODING
6996897	10-Oct-23	MOUNTING SURFACES FOR ELECTRONIC DEVICES
6998952	25-Mar-24	INDUCTIVE DEVICE INCLUDING BOND WIRES
6999014	23-Oct-22	INCREMENTAL-DELTA ANALOGUE-TO-DIGITAL CONVERSION
6999627	17-Jan-24	DETERMINISTIC PREDICTION IN AN IMAGE PROCESSING SYSTEM
7000473	05-Aug-24	MEM STRUCTURE HAVING REDUCED SPRING STICTION
7001852	30-Apr-24	METHOD OF MAKING A HIGH QUALITY THIN DIELECTRIC LAYER
7002371	29-Jan-24	LEVEL SHIFTER
7002940	14-Aug-24	MULTIPLE-STAGE FILTERING DEVICE AND METHOD
7003056	02-Sep-24	SYMBOL TIMING TRACKING AND METHOD THEREFOR
7003743	12-Feb-23	METHOD AND SYSTEM OF DATA PROCESSOR DESIGN
7005193	16-Oct-23	METHOD OF ADDING MASS TO MEMS STRUCTURES
7005717	31-May-20	SEMICONDUCTOR DEVICE AND METHOD
7006318	19-Apr-23	REMOVABLE MEDIA STORAGE SYSTEM WITH MEMORY FOR STORING OPERATIONAL DATA
7006439	06-Sep-23	METHOD AND APPARATUS FOR DETERMINING AN UPPER DATA RATE FOR A VARIABLE DATA RATE SIGNAL
7006553	09-Aug-22	ANALOG SIGNAL SEPARATOR FOR UWB VERSUS NARROWBAND SIGNALS
7007154	14-Nov-18	METHOD AND APPARATUS FOR INTERFACING A PROCESSOR TO A COPROCESSOR
7007253	22-Mar-24	METHOD AND APPARATUS FOR DISTORTION ANALYSIS IN NONLINEAR CIRCUITS
7009424	09-Jul-24	SINGLE SUPPLY LEVEL SHIFTER
7010056	13-Feb-23	SYSTEM AND METHOD FOR GENERATING ULTRA WIDEBAND PULSES
7010278	14-Feb-24	SIDEBAND SUPPRESSION METHOD AND APPARATUS FOR QUADRATURE MODULATOR USING MAGNITUDE MEASUREMENTS
7012324	29-Mar-24	LEAD FRAME WITH FLAG SUPPORT STRUCTURE
7012841	02-Sep-24	CIRCUIT AND METHOD FOR CURRENT PULSE COMPENSATION
7013357	28-Apr-24	ARBITER HAVING PROGRAMMABLE ARBITRATION POINTS FOR UNDEFINED LENGTH BURST ACCESSES AND METHOD
7013409	02-Feb-24	METHOD AND APPARATUS FOR DEBUGGING A DATA PROCESSING SYSTEM
7013447	17-Jan-24	METHOD FOR CONVERTING A PLANAR TRANSISTOR DESIGN TO A VERTICAL DOUBLE GATE TRANSISTOR DESIGN
7014888	05-Apr-23	METHOD AND STRUCTURE FOR FABRICATING SENSORS WITH A SACRIFICIAL GEL DOME
7015075	01-Aug-24	DIE ENCAPSULATION USING A POROUS CARRIER
7015153	20-Oct-24	METHOD FOR FORMING A LAYER USING A PURGING GAS IN A SEMICONDUCTOR PROCESS
7015517	02-Oct-23	SEMICONDUCTOR DEVICE INCORPORATING A DEFECT CONTROLLED STRAINED CHANNEL STRUCTURE AND METHOD

		OF MAKING THE SAME
7015585	06-Jan-23	A PACKAGED INTEGRATED CIRCUIT HAVING WIRE BONDS AND METHOD THEREFOR
7015679	28-Jun-24	CIRCUIT AND METHOD FOR SUPPLYING AN ELECTRICAL AC LOAD
7015852	30-Nov-24	CYCLIC ANALOG-TO-DIGITAL CONVERTER
7016398	07-Oct-23	MULTICODE RECEIVER
7016488	07-Feb-23	METHOD AND APPARATUS FOR NON-LINEAR PROCESSING OF AN AUDIO SIGNAL
7018747	24-Feb-23	PHOTOMASK HAVING LINE END PHASE ANCHORS
7018876	18-Jun-24	TRANSISTOR WITH VERTICAL DIELECTRIC STRUCTURE
7018901	29-Sep-24	METHOD FOR FORMING A SEMICONDUCTOR DEVICE HAVING A STRAINED CHANNEL AND A HETEROJUNCTION SOURCE/DRAIN
7018939	04-Oct-23	MICELLAR TECHNOLOGY FOR POST-ETCH RESIDUES
7019332	20-Jul-21	FABRICATION OF A WAVELENGTH LOCKER WITHIN A SEMICONDUCTOR STRUCTURE
7019403	29-Aug-23	ADHESIVE FILM AND TACKING PADS FOR PRINTED WIRING ASSEMBLIES
7020374	25-Apr-23	OPTICAL WAVEGUIDE STRUCTURE AND METHOD FOR FABRICATING THE SAME
7023195	18-May-24	MODULE, SYSTEM AND METHOD FOR TESTING A PHASE LOCKED LOOP
7023981	27-Sep-24	METHOD AND APPARATUS FOR SIGNAL DETECTION
7026076	19-Jul-23	METHOD OF PATTERNING PHOTORESIST ON A WAFER USING A REFLECTIVE MASK WITH A MULTI-LAYER ARC
7026204	17-Jul-24	TRANSISTOR WITH REDUCED GATE-TO-SOURCE CAPACITANCE AND METHOD THEREFOR
7029980	16-Dec-23	METHOD OF MANUFACTURING SOI TEMPLATE LAYER
7030001	19-Apr-24	METHOD FOR FORMING A GATE ELECTRODE HAVING A METAL
7030469	09-Oct-23	METHOD OF FORMING A SEMICONDUCTOR PACKAGE AND STRUCTURE THEREOF
7030663	24-Jun-23	MONOCYCLE GENERATOR
7030849	01-Nov-24	ROBUST LCD CONTROLLER
7031183	01-Jan-24	MRAM DEVICE INTEGRATED WITH OTHER TYPES OF CIRCUITRY
7031258	29-Dec-22	DIGITAL DATA SYSTEM WITH LINK LEVEL MESSAGE FLOW CONTROL
7031680	25-May-24	STOP-ON-STATION METHOD AND APPARATUS
7033866	20-Aug-23	METHOD FOR MAKING DUAL GAUGE LEADFRAME
7034558	01-Aug-23	TEST SYSTEM FOR DEVICE AND METHOD THEREOF
7035319	21-Oct-24	METHOD AND APPARATUS FOR DETERMINING WHETHER A RECEIVED SIGNAL INCLUDES A DESIRED SIGNAL
7037795	15-Oct-24	LOW RC PRODUCT TRANSISTORS IN SOI SEMICONDUCTOR PROCESS
7037857	16-Dec-23	METHOD FOR ELIMINATING OF EXCESSIVE FIELD OXIDE RECESS FOR THIN SI SOI
7038547	25-Nov-23	AMPLIFIER CIRCUIT
7038959	01-Oct-24	MRAM SENSE AMPLIFIER HAVING A PRECHARGE CIRCUIT AND METHOD FOR SENSING

7039392	05-Mar-24	SYSTEM AND METHOD FOR PROVIDING DEVICE AUTHENTICATION IN A WIRELESS NETWORK
7039438	22-Jan-23	MULTI-MODE RADIO COMMUNICATIONS DEVICE USING A COMMON REFERENCE OSCILLATOR
7039883	30-Sep-24	DERIVATION OF CIRCUIT BLOCK CONSTRAINTS
7040154	08-Jul-24	MOTION SENSING FOR TIRE PRESSURE MONITORING
7041562	28-Mar-24	METHOD FOR FORMING MULTIPLE GATE OXIDE THICKNESS UTILIZING ASHING AND CLEANING
7041576	28-May-24	SEPARATELY STRAINED N-CHANNEL AND P-CHANNEL TRANSISTORS
7042025	16-Mar-23	METHOD AND STRUCTURE FOR CONTACTING AN OVERLYING ELECTRODE FOR A MAGNETOELECTRONICS ELEMENT
7042098	07-Jul-23	BONDING PAD FOR A PACKAGED INTEGRATED CIRCUIT
7042103	30-Mar-23	LOW STRESS SEMICONDUCTOR DIE ATTACH
7042377	18-Sep-23	ANALOGUE-TO-DIGITAL SIGMA-DELTA MODULATOR WITH FIR FILTERS
7042765	19-Aug-24	MEMORY BIT LINE SEGMENT ISOLATION
7042868	13-Oct-24	METHOD AND SYSTEM FOR PERFORMING RANGING FUNCTIONS IN AN ULTRAWIDE BANDWIDTH SYSTEM
7042909	21-Dec-23	METHOD AND APPARATUS FOR CONTROLLING THE TIMING OF A COMMUNICATION DEVICE
7042964	26-Oct-23	VITERBI DECODER, METHOD AND UNIT THEREFOR
7043017	04-Feb-24	KEY STREAM CIPHER DEVICE
7045432	19-Jul-24	METHOD FOR FORMING A SEMICONDUCTOR DEVICE WITH LOCAL SEMICONDUCTOR-ON-INSULATOR (SOI)
7045815	24-Jun-21	A SEMICONDUCTOR STRUCTURE EXHIBITING REDUCED LEAKAGE CURRENT AND METHOD OF FABRICATING SAME
7046977	13-Nov-22	APPARATUS FOR GENERATING MULTIPLE CLOCK SIGNALS OF DIFFERENT FREQUENCY CHARACTERISTICS
7047174	24-Jun-23	METHOD FOR PRODUCING TEST PATTERNS FOR TESTING AN INTEGRATED CIRCUIT
7047350	21-Apr-24	DATA PROCESSING SYSTEM HAVING A SERIAL DATA CONTROLLER
7049694	26-Feb-24	SEMICONDUCTOR PACKAGE WITH CROSSING CONDUCTOR ASSEMBLY AND METHOD OF MANUFACTURE
7050354	18-Apr-24	LOW-POWER COMPILER-PROGRAMMABLE MEMORY WITH FAST ACCESS TIMING
7051150	15-Oct-23	SCALABLE ON CHIP NETWORK
7052939	26-Nov-22	STRUCTURE TO REDUCE SIGNAL CROSS-TALK THROUGH SEMICONDUCTOR SUBSTRATE FOR SYSTEM ON CHIP APPLICATIONS
7056766	04-Mar-24	METHOD OF FORMING LAND GRID ARRAY PACKAGED DEVICE
7056778	25-Sep-23	SEMICONDUCTOR LAYER FORMATION
7057427	18-Oct-24	POWER ON RESET CIRCUIT
7057462	25-Jun-24	TEMPERATURE COMPENSATED ON-CHIP BIAS CIRCUIT FOR LINEAR RF HBT POWER AMPLIFIERS
7057564	31-Aug-24	MULTILAYER CAVITY SLOT ANTENNA
7058149	20-Sep-24	SYSTEM FOR PROVIDING A CALIBRATED CLOCK AND METHODS THEREOF
7058414	10-Oct-22	METHOD AND SYSTEM FOR ENABLING DEVICE FUNCTIONS

		BASED ON DISTANCE INFORMATION
7061299	06-Feb-24	BIDIRECTIONAL LEVEL SHIFTER
7063919	19-Jun-23	LITHOGRAPHIC TEMPLATE HAVING A REPAIRED GAP DEFECT METHOD OF REPAIR AND USE
7064030	19-Mar-25	METHOD FOR FORMING A MULTI-BIT NON-VOLATILE MEMORY DEVICE
7064396	28-Jun-24	INTEGRATED CIRCUIT WITH MULTIPLE SPACER INSULATING REGION WIDTHS
7064615	24-Mar-24	METHOD AND APPARATUS FOR DOHERTY AMPLIFIER BIASING
7064700	15-Jun-25	MULTI-CHANNEL ANALOG TO DIGITAL CONVERTER
7065136	18-Feb-22	RECEIVER HAVING AN EQUALIZING DEMODULATOR AND A NON-EQUALIZING DEMODULATOR AND METHOD FOR CONTROLLING THE SAME
7065207	30-Jun-24	CONTROLLING ATTENUATION DURING ECHO SUPPRESSION
7067331	30-Aug-22	AMORPHOUS ALLOYS FOR MAGNETIC DEVICES
7067342	15-Nov-24	METHOD OF INTEGRATING OPTICAL DEVICES AND ELECTRONIC DEVICES ON AN INTEGRATED CIRCUIT
7067856	19-Feb-22	SEMICONDUCTOR STRUCTURE, SEMICONDUCTOR DEVICE, COMMUNICATING DEVICE, INTEGRATED CIRCUIT, AND PROCESS FOR FABRICATING THE SAME
7067868	11-Nov-24	DOUBLE GATE DEVICE HAVING A HETEROJUNCTION SOURCE/DRAIN AND STRAINED CHANNEL
7067907	30-Jul-23	SEMICONDUCTOR PACKAGE HAVING ANGULATED INTERCONNECT SURFACES
7068198	10-Jul-24	DOUBLE-SAMPLED INTEGRATOR SYSTEM AND METHOD THEREOF
7069384	05-Apr-22	SYSTEM AND METHOD FOR CACHE EXTERNAL WRITING AND WRITE SHADOWING
7071038	22-Sep-24	METHOD OF FORMING A SEMICONDUCTOR DEVICE HAVING A DIELECTRIC LAYER WITH HIGH DIELECTRIC CONSTANT
7071518	28-May-24	SCHOTTKY DEVICE
7072635	24-Dec-24	METHOD FOR COMBINING DATA FROM PHASE INDETERMINATE DATA STREAMS FOR RAKING
7074118	01-Nov-25	POLISHING CARRIER HEAD WITH A MODIFIED PRESSURE PROFILE
7074527	01-Jun-24	METHOD FOR FABRICATING A MASK USNG A HARDMASK AND METHOD FOR MAKING A SEMICONDUCTOR DEVICE USING THE SAME
7074627	29-Jun-24	LEAD SOLDER INDICATOR AND METHOD
7074647	07-Jul-23	SEMICONDUCTOR COMPONENT AND METHOD OF MANUFACTURING SAME
7074664	29-Mar-25	DUAL METAL GATE ELECTRODE SEMICONDUCTOR FABRICATION PROCESS AND STRUCTURE THEREOF
7074681	27-Jun-21	SEMICONDUCTOR COMPONENT AND METHOD OF MANUFACTURING
7074687	05-Apr-23	METHOD FOR FORMING AN ESD PROTECTION DEVICE
7074713	30-Sep-24	PLASMA ENHANCED NITRIDE LAYER
7075473	30-Apr-22	SYSTEM AND APPARATUS FOR REDUCING THE EFFECTS OF CIRCUIT MISMATCH IN ANALOG-TO-DIGITAL CONVERTERS
7076584	01-Jul-24	METHOD AND APPARATUS FOR INTERCONNECTING PORTIONS OF CIRCUITRY WITHIN A DATA PROCESSING SYSTEM

7078297	09-Aug-24	MEMORY WITH RECESSED DEVICES
7078785	23-Sep-23	SEMICONDUCTOR DEVICE AND MAKING THEREOF
7078796	01-Jul-23	CORROSION-RESISTANT COPPER BOND PAD AND INTEGRATED DEVICE
7078966	08-Jan-25	POWER AMPLIFIER SATURATION DETECTION AND OPERATION AT MAXIMUM POWER
7079068	21-May-24	ANALOG TO DIGITAL CONVERTER
7079604	14-Apr-23	ULTRAWIDE BANDWIDTH SYSTEM AND METHOD FOR FAST SYNCHRONIZATION USING MULTIPLE DETECTION ARMS
7080191	21-May-22	METHOD AND SYSTEM FOR ACCESSING MEMORY DEVICES
7080373	13-Oct-23	METHOD AND DEVICE FOR CREATING AND USING PRE-INTERNALIZED PROGRAM FILES
7082389	03-Nov-24	METHOD AND APPARATUS FOR SIMULATING A MAGNETORESISTIVE RANDOM ACCESS MEMORY (MRAM)
7082451	11-Jul-24	RECONFIGURABLE VECTOR-FFT/IFFT, VECTOR-MULTIPLIER/DIVIDER
7083880	10-Jul-23	LITHOGRAPHIC TEMPLATE AND METHOD OF FORMATION AND USE
7084485	17-Apr-24	METHOD OF MANUFACTURING A SEMICONDUCTOR COMPONENT, AND SEMICONDUCTOR COMPONENT FORMED THEREBY
7084698	21-Jan-25	BAND-GAP REFERENCE CIRCUIT
7085175	18-Nov-24	WORD LINE DRIVER CIRCUIT FOR A STATIC RANDOM ACCESS MEMORY AND METHOD THEREFOR
7085943	12-Oct-24	METHOD AND CIRCUITRY FOR CONTROLLING SUPPLY VOLTAGE IN A DATA PROCESSING SYSTEM
7086027	27-May-21	METHOD AND APPARATUS FOR CONSTRAINT GRAPH BASED LAYOUT COMPACTION FOR INTEGRATED CIRCUITS
7087972	31-Jan-25	MAGNETOELECTRONIC DEVICES UTILIZING PROTECTIVE CAPPING LAYERS AND METHODS OF FABRICATING THE SAME
7088009	21-Mar-24	WIREBONDED ASSEMBLAGE METHOD AND APPARATUS
7088162	07-Sep-21	CIRCUIT GENERATING CONSTANT NARROW-PULSE-WIDTH BIPOLARITY MONOCYCLES
7088608	25-Jan-24	REDUCING POWER CONSUMPTION DURING MRAM WRITES USING MULTIPLE CURRENT LEVELS
7088632	26-May-24	AUTOMATIC HIDDEN REFRESH IN A DRAM AND METHOD THEREFOR
7088702	14-Jun-24	METHOD FOR CONTROLLING A DATA STREAM IN A WIRELESS NETWORK
7089467	18-Jan-25	ASYNCHRONOUS DEBUG INTERFACE
7091071	03-Jan-25	SEMICONDUCTOR FABRICATION PROCESS INCLUDING RECESSED SOURCE/DRAIN REGIONS IN AN SOI WAFER
7091089	21-Jan-25	METHOD OF FORMING A NANOCUSTER CHARGE STORAGE DEVICE
7091130	08-Dec-24	METHOD OF FORMING A NANOCUSTER CHARGE STORAGE DEVICE
7091568	22-Dec-24	DIELECTRIC LAYER, AN ELECTRONIC DEVICE INCLUDING THE DIELECTRIC LAYER, AND A PROCESS FOR FORMING THE ELECTRONIC DEVICE
7091602	02-Mar-24	MINIATURE MOLDBLOCKS FOR HEATSINK OR FLAG FOR AN OVERMOLDED PLASTIC PACKAGE
7091712	10-Nov-24	CIRCUIT FOR PERFORMING VOLTAGE REGULATION

7092465	23-Dec-24	METHOD AND APPARATUS FOR PROCESSING AN AMPLITUDE MODULATED (AM) SIGNAL
7092890	27-Nov-22	METHOD FOR MANUFACTURING THIN GAAS DIE WITH COPPER-BACK METAL STRUCTURES
7093223	30-Apr-23	NOISE ANALYSIS FOR AN INTEGRATED CIRCUIT MODEL
7094645	17-Sep-24	PROGRAMMING AND ERASING STRUCTURE FOR A FLOATING GATE MEMORY CELL AND METHOD OF MAKING
7095092	29-Jul-24	SEMICONDUCTOR DEVICE AND METHOD OF FORMING THE SAME
7095246	21-Oct-24	VARIABLE IMPEDANCE OUTPUT BUFFER
7095646	19-Feb-23	MULTI-STATE MAGNETORESISTANCE RANDOM ACCESS CELL WITH IMPROVED MEMORY STORAGE DENSITY
7096307	24-Jan-25	SHARED WRITE BUFFER IN A PERIPHERAL INTERFACE AND METHOD OF OPERATING
7096348	27-May-24	METHOD AND APPARATUS FOR ALLOCATING ENTRIES IN A BRANCH TARGET BUFFER
7096378	22-Aug-23	DATA STORAGE SYSTEM HAVING A NON-VOLATILE IC BASED MEMORY FOR STORING USER DATA
7098073	18-Apr-25	METHOD FOR STACKING AN INTEGRATED CIRCUIT ON ANOTHER INTEGRATED CIRCUIT
7098090	18-Feb-25	METHOD OF FORMING A SEMICONDUCTOR DEVICE
7098495	19-Oct-24	MAGNETIC TUNNEL JUNCTION ELEMENT STRUCTURES AND METHODS FOR FABRICATING THE SAME
7098502	04-Jul-24	TRANSISTOR HAVING THREE ELECTRICALLY ISOLATED ELECTRODES AND METHOD OF FORMATION
7098877	29-Feb-24	DRIVER CIRCUIT
7099973	25-Mar-24	METHOD AND SYSTEM OF BUS MASTER ARBITRATION
7100020	07-May-19	DIGITAL COMMUNICATIONS PROCESSOR
7101736	15-Jul-24	METHOD OF ASSEMBLING A SEMICONDUCTOR COMPONENT AND APPARATUS THEREFOR
7102359	11-Oct-25	INTEGRATED FAULT DETECTOR CIRCUIT
7102365	01-Apr-25	APPARATUS FOR CURRENT SENSING
7102410	15-Jul-24	HIGH VOLTAGE LEVEL CONVERTER USING LOW VOLTAGE DEVICES
7102669	06-Dec-22	DIGITAL COLOR IMAGE PRE-PROCESSING
7105363	27-Nov-22	CLADDED CONDUCTOR FOR USE IN A MAGNETOELECTRONICS DEVICE AND METHOD FOR FABRICATING THE SAME
7105383	17-Mar-23	PACKAGED SEMICONDUCTOR WITH COATED LEADS AND METHOD THEREFORE
7105395	31-Aug-24	PROGRAMMING AND ERASING STRUCTURE FOR AN NVM CELL
7105429	03-Mar-25	METHOD OF INHIBITING METAL SILICIDE ENCROACHMENT IN A TRANSISTOR
7105430	30-Jul-24	METHOD FOR FORMING A SEMICONDUCTOR DEVICE HAVING A NOTCHED CONTROL ELECTRODE AND STRUCTURE THEREOF
7105866	24-Jul-20	HETEROJUNCTION TUNNELING DIODES AND PROCESS FOR FABRICATING SAME
7105886	21-Jul-24	HIGH K DIELECTRIC FILM
7105903	18-Nov-24	METHODS AND STRUCTURES FOR ELECTRICAL COMMUNICATION WITH AN OVERLYING ELECTRODE FOR A SEMICONDUCTOR ELEMENT

7107436	17-Oct-24	CONDITIONAL NEXT PORTION TRANSFERRING OF DATA STREAM TO OR FROM REGISTER BASED ON SUBSEQUENT INSTRUCTION ASPECT
7107489	01-Nov-23	METHOD AND APPARATUS FOR DEBUGGING A DATA PROCESSING SYSTEM
7108755	01-Oct-22	SIMPLIFICATION OF BALL ATTACH METHOD USING SUPER-SATURATED FINE CRYSTAL FLUX
7109051	28-Dec-24	METHOD OF INTEGRATING OPTICAL DEVICES AND ELECTRONIC DEVICES ON AN INTEGRATED CIRCUIT
7109055	20-Jan-25	METHODS AND APPARATUS HAVING WAFER LEVEL CHIP SCALE PACKAGE FOR SENSING ELEMENTS
7109079	26-Jan-25	METAL GATE TRANSISTOR CMOS PROCESS AND METHOD FOR MAKING
7109101	23-Aug-23	CAPPING LAYER FOR REDUCING AMORPHOUS CARBON CONTAMINATION OF PHOTORESIST IN SEMICONDUCTOR DEVICE MANUFACTURE; AND PROCESSES FOR MAKING SAME
7109550	06-Jul-23	SEMICONDUCTOR FABRICATION PROCESS WITH ASYMMETRICAL CONDUCTIVE SPACERS
7109782	12-Feb-25	WELL BIAS VOLTAGE GENERATOR
7109816	28-Jan-25	DUAL PORT MODULATOR COMPRISING A FREQUENCY SYNTHESISER
7109906	29-Apr-25	NICAM ENCODER FEATURING SYNCHRONIZATION OF A NICAM PROCESSOR WITH FRONT-END INPUT AND OUTPUT SECTIONS
7110380	01-Oct-24	SYSTEM, METHOD, AND COMPUTER PROGRAM PRODUCT FOR SHARING BANDWIDTH IN A WIRELESS PERSONAL AREA NETWORK OR A WIRELESS LOCAL AREA NETWORK
7111184	17-Jun-24	SYSTEM AND METHOD FOR DETERMINISTIC COMMUNICATION ACROSS CLOCK DOMAINS
7112455	18-Jun-24	SEMICONDUCTOR OPTICAL DEVICES AND METHOD FOR FORMING
7112490	25-Jul-25	HOT CARRIER INJECTION PROGRAMMABLE STRUCTURE INCLUDING DISCONTINUOUS STORAGE ELEMENTS AND SPACER CONTROL GATES IN A TRENCH
7112832	31-Jul-23	TRANSISTOR HAVING MULTIPLE CHANNELS
7112871	08-Jan-24	FLIPCHIP QFN PACKAGE
7113054	28-Jan-22	ARRANGEMENT AND METHOD IMPEDANCE MATCHING
7113430	15-Dec-23	DEVICE FOR REDUCING THE EFFECTS OF LEAKAGE CURRENT WITHIN ELECTRONIC DEVICES
7115949	27-Nov-22	METHOD OF FORMING A SEMICONDUCTOR DEVICE IN A SEMICONDUCTOR LAYER AND STRUCTURE THEREOF
7116147	25-Oct-24	CIRCUIT AND METHOD FOR INTERPOLATIVE DELAY
7116537	14-Dec-25	SURGE CURRENT PREVENTION CIRCUIT AND DC POWER SUPPLY
7117234	11-Aug-23	WAVEFORM GENERATOR FOR USE IN IQ MODULATION
7117346	23-Oct-23	DATA PROCESSING SYSTEM HAVING MULTIPLE REGISTER CONTEXTS AND METHOD THEREFOR
7119381	02-Aug-24	COMPLEMENTARY METAL-OXIDE SEMICONDUCTOR FIELD EFFECT TRANSISTOR STRUCTURE
7120126	01-Jul-24	METHOD FOR IMPROVED MEDIA QUALITY FEEDBACK
7120402	05-Apr-25	METHOD AND APPARATUS FOR A HIGH PERFORMANCE AND HIGH DYNAMIC RANGE BASEBAND POWER CONTROL SYSTEM
7120661	29-Mar-25	BIT EXACTNESS SUPPORT IN DUAL-MAC ARCHITECTURE

7121141	28-Jan-25	Z-AXIS ACCELEROMETER WITH AT LEAST TWO GAP SIZES AND TRAVEL STOPS DISPOSED OUTSIDE AN ACTIVE CAPACITOR AREA
7122395	18-Jan-23	METHOD OF FORMING SEMICONDUCTOR DEVICES THROUGH EPITAXY
7122421	15-Aug-23	SEMICONDUCTOR DEVICE INCLUDING A TRANSISTOR AND A CAPACITOR HAVING AN ALIGNED TRANSISTOR AND CAPATIVE ELEMENT
7123068	14-Jun-25	FLIP-FLOP CIRCUIT HAVING LOW POWER DATA RETENTION
7123647	12-Nov-19	CHIP RATE BASE BAND RECEIVER PROCESSOR WHICH RECEIVES DIGITAL INFORMATION CONTAINING SYMBOL INFORMATION
7123677	04-Jul-24	VARIABLE SAMPLING DATA OUTPUT CIRCUIT
7123892	15-Apr-25	ARCHITECTURE FOR AN AM/FM DIGITAL INTERMEDIATE FREQUENCY RADIO
7124162	15-Sep-24	ADDER TREE STRUCTURE DSP SYSTEM AND METHOD
7124281	17-Mar-22	PROCESSING SYSTEM HAVING SEQUENTIAL ADDRESS INDICATOR SIGNALS
7124385	13-May-24	METHOD FOR AUTOMATED TRANSISTOR FOLDING
7125805	20-Jul-24	METHOD OF SEMICONDUCTOR FABRICATION INCORPORATING DISPOSABLE SPACER INTO ELEVATED SOURCE/DRAIN PROCESSING
7126172	13-Oct-24	INTEGRATION OF MULTIPLE GATE DIELECTRICS BY SURFACE PROTECTION
7126192	24-Mar-24	TRANSISTOR WITH REDUCED GATE-TO-SOURCE CAPACITANCE AND METHOD THEREFOR
7126433	06-Jun-24	SELF-CALIBRATING OSCILLATOR SYSTEM
7127384	05-Jul-24	FAST SIMULATION OF CIRCUITRY HAVING SOI TRANSISTORS
7127563	08-Nov-21	SHARED MEMORY ARCHITECTURE
7129098	09-Mar-25	REDUCED POWER MAGNETORESISTIVE RANDOM ACCESS MEMORY ELEMENTS
7129566	30-Jun-24	SCRIBE STREET STRUCTURE FOR BACKEND INTERCONNECT SEMICONDUCTOR WAFER INTEGRATION
7130346	13-Jul-24	METHOD AND APPARATUS HAVING A DIGITAL PWM SIGNAL GENERATOR WITH INTEGRAL NOISE SHAPING
7130943	30-Sep-24	DATA PROCESSING SYSTEM WITH BUS ACCESS RETRACTION
7132303	18-Dec-23	STACKED SEMICONDUCTOR DEVICE ASSEMBLY AND METHOD FOR FORMING
7132327	25-May-24	DECOUPLED COMPLEMENTARY MASK PATTERNING TRANSFER METHOD
7132329	29-Jun-25	SOURCE SIDE INJECTION STORAGE DEVICE WITH SPACER GATES AND METHOD THEREFOR
7132360	25-Jan-25	METHOD FOR TREATING A SEMICONDUCTOR SURFACE TO FORM A METAL-CONTAINING LAYER
7132372	29-Apr-25	METHOD FOR PREPARING A SEMICONDUCTOR SUBSTRATE SURFACE FOR SEMICONDUCTOR DEVICE FABRICATION
7132704	21-Jul-23	TRANSISTOR SIDEWALL SPACER STRESS MODULATION
7132863	05-May-25	DIGITAL CLOCK FREQUENCY DOUBLER
7135370	07-Jul-23	DIELECTRIC STORAGE MEMORY CELL HAVING HIGH PERMITTIVITY TOP DIELECTRIC AND METHOD THEREFOR
7135379	30-Sep-24	ISOLATION TRENCH PERIMETER IMPLANT FOR THRESHOLD VOLTAGE CONTROL

7135842	18-Apr-25	VOLTAGE REGULATOR HAVING IMPROVED IR DROP
7135934	07-Jun-25	FULLY PROGRAMMABLE PHASE LOCKED LOOP
7136028	27-Aug-24	APPLICATIONS OF A HIGH IMPEDANCE SURFACE
7136029	27-Aug-24	FREQUENCY SELECTIVE HIGH IMPEDANCE SURFACE
D345731	05-Apr-08	SEMICONDUCTOR PACKAGE
RE34227	27-Nov-09	NON-CYANIDE ELECTRODE CLEANING PROCESS
RE35294	13-Nov-10	HIGH-PRESSURE POLYSILICON ENCAPSULATED LOCALIZED OXIDATION OF SILICON
RE36773	18-Oct-13	METHOD FOR PLATING USING NESTED PLATING BUSES AND SEMICONDUCTOR DEVICE HAVING THE SAME
RE36890	31-Jul-10	GRADIENT CHUCK METHOD FOR WAFER BONDING EMPLOYING A CONVEX PRESSURE

U.S. Patents Applications Assigned to Freescale

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Brazil	PCT/US99/10024
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Canada	2207129
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