

PATENT ASSIGNMENT

Electronic Version v1.1

Stylesheet Version v1.1

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| NATURE OF CONVEYANCE: | ASSIGNMENT |
| CONVEYING PARTY DATA | |
| Name | Execution Date |
| SiberCore Technologies, Inc. | 08/18/2005 |
| RECEIVING PARTY DATA | |
| Name: | Core Networks LLC |
| Street Address: | 2215-B Renaissance Drive, Suite 5 |
| City: | Las Vegas |
| State/Country: | NEVADA |
| Postal Code: | 89119 |
| PROPERTY NUMBERS Total: 1 | |
| Property Type | Number |
| Application Number: | 11469248 |
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| NAME OF SUBMITTER: | Julianne Flynn |
| Total Attachments: 6 source=JJF_012P45002C_2#page1.tif source=JJF_012P45002C_2#page2.tif source=JJF_012P45002C_2#page3.tif source=JJF_012P45002C_2#page4.tif source=JJF_012P45002C_2#page5.tif | |

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*Exhibit B***ASSIGNMENT OF PATENT RIGHTS**

For good and valuable consideration, the receipt of which is hereby acknowledged, SiberCore Technologies Inc. having offices at 100-1 Hines Rd., Kanata, ON, Canada, K2K 3C7, ("Assignor"), does hereby sell, assign, transfer and convey unto Core Networks LLC, a Nevada limited liability company, having an office at 2215-B Renaissance Drive, Suite 5, Las Vegas, NV 89119 ("Assignee") or its designees, all of Assignor's right, title and interest in and to the patent applications and patents listed below, any patents, registrations, or certificates of invention issuing on any patent applications listed below, the inventions disclosed in any of the foregoing, any and all counterpart United States, international and foreign patents, applications and certificates of invention based upon or covering any portion of the foregoing, and all reissues, re-examinations, divisionals, renewals, extensions, provisionals, continuations and continuations-in-part of any of the foregoing (collectively "*Patent Rights*"):

| <u>Patent or Application No.</u> | <u>Country</u> | <u>Filing Date</u> | <u>Title and Inventor(s)</u> |
|----------------------------------|----------------|--------------------|--|
| 6,044,005 | US | February 3, 1999 | Content addressable memory storage device Gibson; Garnet Fredrick Randall; Shafai; Farhad; Podaima; Jason Edward |
| 6,275,406 | US | August 29, 2000 | Content address memory circuit with redundant array and method for implementing the same Gibson; G. F. Randall; Shafai; Farhad; Podaima; Jason E. |
| 6,339,539 | US | August 30, 2000 | Content addressable memory having read/write capabilities that do not interrupt continuous search cycles Gibson; G. F. Randall; Avramescu; Radu |
| ① CA2318019 | Canada | September 8, 2000 | Content addressable memory having read/write capabilities |

| | | | |
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| ✓ Abandoned | | | that do not interrupt continuous search cycles Gibson; G. F. Randall; Avramescu; Radu |
| EP1096504 Deemed withdrawn | Europe | September 8, 2000 | Content addressable memory having read/write capabilities that do not interrupt continuous search cycles Gibson; G. F. Randall; Avramescu; Radu |
| JP2001148195 | Japan | September 8, 2000 | Content addressable memory having read/write capabilities that do not interrupt continuous search cycles Gibson; G. F. Randall; Avramescu; Radu |
| 6,362,990 | US | September 1, 2000 | Three port content addressable memory device and methods for implementing the same Gibson; G. F. Randall; Shafai; Farhad; Schultz; Kenneth J. |
| CA2317902 ✓ Abandoned | Canada | September 7, 2000 | Three port content addressable memory device and methods for implementing the same Gibson; G. F. Randall; Shafai; Farhad; Schultz; Kenneth J. |
| ✓ EP1083572 Deemed withdrawn | Europe | September 8, 2000 | Three port content addressable memory device and methods for implementing the same Gibson; G. F. Randall; Shafai; Farhad; Schultz; Kenneth J. |

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| JP2001143479 | Japan | September 8, 2000 | Three port content addressable memory device and methods for implementing the same Gibson; G. F. Randall; Shafai; Farhad; Schultz; Kenneth J |
| 6,392,910 | US | August 17, 2000 | Priority encoder with multiple match function for content addressable memories and methods for implementing the same Podaima; Jason Edward; Schultz; Kenneth J. |
| 6,538,911 | US | August 24, 2001 | Content addressable memory with block select for power management Allan; Graham A.; Gibson; G. F. Randall; Podaima; Jason Edward |
| 6,553,453 | US | September 1, 2000 | Variable width content addressable memory device for searching variable width data Gibson; G. F. Randall; Shafai; Farhad |
| CA2317387 Abandoned | Canada | September 7, 2000 | Variable width content addressable memory device for searching variable width data Gibson; G. F. Randall; Shafai; Farhad |
| EP1083573 Deemed withdrawn | Europe | September 8, 2000 | Variable width content addressable memory device for searching variable width data |

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| | | | Gibson; G. F. Randall; Shafai; Farhad |
| JP2001160292 | Japan | September 8, 2000 | Variable width content addressable memory device for searching variable width data Gibson; G. F. Randall; Shafai; Farhad |
| TW04921015 | Taiwan | September 8, 2000 | Variable width content addressable memory device for searching variable width data Gibson; G. F. Randall; Shafai; Farhad |
| US2004003170 Abandoned — failure to respond to OA | US | April 2, 2003 | Variable width content addressable memory device for searching variable width data Gibson; G. F. Randall; Shafai; Farhad |
| 6,577,519 | US | August 30, 2001 | System and method for low power searching in content addressable memories using sample search words Avramescu; Radu |
| 09/944,251 (Abandoned) | US | August 30, 2001 | System and Method for low power searching in content addressable memories using non-precharged compare lines Jason Podaima; GF Randall Gibson |
| 09/944,256 | US | August 30, 2001 | System and Method for low power searching in content |

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|-----------|----|-------------------|--|
| Abandoned | | | addressable memories using non-precharged compare lines Jason Podaima; GF Randall Gibson" |
| 6,609,222 | US | September 1, 2000 | Methods and circuitry for built-in self-testing of content addressable memories Gupta; Sanjay; Gibson; G. F. Randall |
| 6,775,167 | US | March 11, 2003 | System and method for low power searching in content addressable memories using sample search words to save power in compare lines Avramescu; Radu; Podaima; Jason Edward |
| 6,862,655 | US | October 1, 2002 | Wide word search using serial match line computation in content addressable memory Podaima; Jason Edward; Gupta; Sanjay; Gibson; G. F. Randall; Avramescu; Radu |

Assignor represents, warrants and covenants that: (i) it is the sole owner, assignee and holder of record title to the Patent Rights identified above, (ii) it has obtained and properly recorded previously executed assignments for all patent applications and patents identified above as necessary to fully perfect its rights and title therein in accordance with governing law and regulations in each respective jurisdiction, and (iii) it has full power and authority to make the present assignment. Assignor shall indemnify and hold harmless Assignee for any breach of the foregoing.

Assignor further agrees to and hereby does sell, assign, transfer and convey unto Assignee all rights: (i) in and to causes of action and enforcement rights for the Patent Rights including all rights to pursue damages, injunctive relief and other remedies for past and future infringement of

and all patents or certificates of invention which may be granted upon any of the Patent Rights in the name of Assignee, as the assignee to the entire interest therein.

Assignor will, at the reasonable request of Assignee and without demanding any further consideration therefor, do all things necessary, proper, or advisable, including without limitation the execution, acknowledgment and recordation of specific assignments, oaths, declarations and other documents on a country-by-country basis, to assist Assignee in obtaining, perfecting, sustaining, and/or enforcing the Patent Rights. Such assistance shall include providing, and obtaining from the respective inventors, prompt production of pertinent facts and documents, giving of testimony, execution of petitions, oaths, powers of attorney, specifications, declarations or other papers and other assistance reasonably necessary for filing patent applications, complying with any duty of disclosure, and conducting prosecution, reexamination, reissue, interference or other priority proceedings, opposition proceedings, cancellation proceedings, public use proceedings, infringement or other court actions and the like with respect to the Patent Rights.

The terms and conditions of this Assignment shall inure to the benefit of Assignee, its successors, assigns and other legal representatives, and shall be binding upon Assignor, its successor, assigns and other legal representatives.

IN WITNESS WHEREOF this Assignment of Patent Rights is executed at Ottawa ON
on August 18, 2005

ASSIGNOR

SIBERCORE TECHNOLOGIES INC.

By: [Signature]

Name: BARBARA DARLOW

Title: PRESIDENT

I, Thomas J. Marlay, a notary public in and for the Province of Ontario, duly appointed, commissioned and sworn and practising at 555 Legget Drive, Suite 740, Tower B, Kanata, Ontario K2K 2X3 CERTIFY that I was personally present on the 18th day of August, 2005, and did see Barbara Darlow who is personally known to me to be the person named in this Assignment of Patent Rights between Core Networks LLC and SiberCore Technologies Inc., duly sign and execute same.

IN TESTIMONY OF WHICH I have subscribed my name and affixed my notarial seal at Ottawa this 18th day of August, 2005.

[Signature]
A Notary Public in and for the Province of Ontario
Thomas J. Marlay