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Commissioner for Patents: Please record the attached original document(s) or copy(ies).

FINANCE SECTION

1. Name of conveying party(ies):

Intel Corporation

Additional name(s) attached? ☐ Yes ☒ No

2. Name and address of receiving party(ies):

Analog Devices, Inc.
One Technology Way
P.O. Box 9106
Norwood, MA 02062-9106

Additional names/addresses attached? ☐ Yes ☒ No

3. Nature of conveyance:

- ☒ Assignment
☐ Merger
☐ Security Agreement
☐ Change of Name
☐ Other:

Execution Date: 11/08/2006

4. Application number(s) or patent number(s):

If this document is being filed with a new application, the execution date of the application is:

A. Patent Application No(s):

See Appendix A

B. Patent No(s):

See Appendix A

Additional numbers attached? ☒ Yes ☐ No

5. Name/address of party to whom correspondence concerning document should be mailed:

SCOTT C. HARRIS
Fish & Richardson P.C.
12390 El Camino Real
San Diego, California 92130

6. Total number of applications/patents involved: 1

7. Total fee (37 CFR §3.41): \$40

- ☐ Enclosed
☒ Authorized to charge Deposit Account.

8. Deposit Account No.: 06-1050

Please apply any additional charges, or any credits, to our
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9. Statement and Signature: *To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.*

Scott C. Harris

Reg. No. 32,030

Name of Person Signing

Signature

February 6, 2007

Date

Total number of pages including coversheet, attachments and document: 28

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I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Mail Stop Assignment Recordation Services, Commissioner of Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

February 6, 2007

Date of Deposit

Signature

Jennifer H. Payne

Typed Name of Person Signing Certificate

PATENT
REEL: 018891 FRAME: 0599

APPENDIX A

ATTY DOCKET NO.	SERIAL NO.	FILING DATE
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22144-188002	11/138,825	05-25-2005
22144-271001	09/711,496	11-13-2000
22144-276001	09/731,523	12-06-2000
22144-284002	11/347,097	02-03-2006
22144-286001	09/704,467	10-31-2000
22144-293002	11/231,397	09-20-2005
22144-395002	11/135,862	05-23-2005
22144-789001	10/335,149	12-31-2002

ATTY DOCKET NO.	PATENT NO.	ISSUE DATE
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22144-184001	6,446,181	09-03-2002
22144-185001	6,606,684	08-12-2003
22144-185002	6,898,690	05-24-2005
22144-186001	6,654,502	11-25-2003
22144-186002	7,154,950	12-26-2006
22144-187001	6,700,996	03-02-2004
22144-187002	7,031,498	04-18-2006
22144-188001	6,907,080	06-14-2005
22144-202001	6,725,360	04-20-2004
22144-202002	7,047,271	05-16-2006
22144-203001	6,601,077	07-29-2003
22144-203002	6,820,102	11-16-2004
22144-257001	7,120,781	10-10-2006
22144-270001	6,976,151	12-13-2005
22144-272001	7,168,032	01-23-2007
22144-273001	6,789,187	09-07-2004
22144-274001	7,069,420	06-27-2006

22144-277001	6,986,026	01-10-2006
22144-278001	6,842,812	01-11-2005
22144-279001	6,665,795	12-16-2003
22144-280001	6,754,808	06-22-2004
22144-280002	7,036,000	04-25-2006
22144-281001	6,829,701	12-07-2004
22144-282001	6,760,800	07-06-2004
22144-283001	7,028,165	04-11-2006
22144-284001	7,082,516	07-25-2006
22144-285001	6,728,870	04-27-2004
22144-287001	6,823,448	11-23-2004
22144-288001	6,898,693	05-24-2005
22144-289001	6,748,523	06-08-2004
22144-290001	6,789,184	09-07-2004
22144-291001	6,766,444	07-20-2004
22144-292001	7,155,570	12-26-2006
22144-293001	6,948,056	09-20-2005
22144-330001	6,889,316	05-03-2005
22144-391001	7,065,636	06-20-2006
22144-392001	6,760,830	07-06-2004
22144-393001	7,124,285	10-17-2006
22144-394001	6,898,695	05-24-2005
22144-395001	6,920,515	07-19-2005
22144-396001	6,763,453	07-13-2004
22144-432001	6,920,547	07-19-2005
22144-799001	7,043,582	05-09-2006

PATENT ASSIGNMENT

WHEREAS, Intel Corporation, (hereinafter "ASSIGNOR"), a corporation organized and existing under the laws of the state of Delaware and having a usual place of business at 2200 Mission College Boulevard, Santa Clara, CA 95054, owns and has legal title to one or more inventions described in the patents and patent applications identified in attached **Schedule B-1** (the "Inventions"); and

WHEREAS, Analog Devices, Inc., (hereinafter "ASSIGNEE"), a corporation organized and existing under the laws of the Commonwealth of Massachusetts and having a usual place of business at One Technology Way, Norwood, MA 02062, desires to acquire an interest therein, in accordance with agreements duly entered into with ASSIGNOR;

NOW, THEREFORE, to all whom it may concern be it known that for and in consideration of said agreements and of other good and valuable consideration, the receipt of which is hereby acknowledged, ASSIGNOR HAS sold, assigned and transferred and by these presents do hereby sell, assign and transfer unto said ASSIGNEE, its successors, assigns, and legal representatives, ASSIGNOR'S entire right, title and interest in and throughout the United States of America, its territories and all foreign countries, in and to the Inventions, together with ASSIGNOR'S entire right, title and interest in and to the patents and in and to the patent applications and any patents as may issue thereon or claim priority under United States law or international convention, including continuations, divisionals, renewals, reexaminations and reissues thereof; said Inventions, applications and patents to be held and enjoyed by said ASSIGNEE for its own use and behalf and for its successors, assigns and legal representatives, to the full end of the term for which said patents may be granted as fully and entirely as the same would have been held by ASSIGNOR had this assignment and sale not been made; ASSIGNOR hereby conveys all of ASSIGNOR'S rights arising under or pursuant to any and all United States laws and international agreements, treaties or laws relating to the protection of industrial property by filing any such applications for patents, including but not limited to any cause(s) of action and damages accruing prior to this assignment. ASSIGNOR hereby acknowledges that this assignment, being of ASSIGNOR'S entire right, title and interest in and to said Inventions, carries with it the right in ASSIGNEE to apply for and obtain from competent authorities in all countries of the world any and all patents by attorneys and agents of ASSIGNEE's selection and the right to procure the grant of all patents to ASSIGNEE in its own name as assignee of ASSIGNOR'S entire right, title and interest therein;

AND, ASSIGNOR hereby further agrees for ASSIGNOR and ASSIGNOR'S successors, assigns and legal representatives to execute upon request any other lawful documents and likewise to perform any other lawful acts which may be deemed necessary to secure fully the aforesaid Inventions to said ASSIGNEE, its successors, assigns and legal representatives, but at its or their expense and charges;

AND, ASSIGNOR hereby further agrees to provide statements or testimony in any interference or other proceeding in which said Inventions or any application or patent directed thereto may be involved;

AND, ASSIGNOR hereby authorizes ASSIGNEE or its attorneys or agents to insert the correct serial number and filing date into this assignment, if none is indicated on the date of ASSIGNOR'S execution of this assignment;

AND, ASSIGNOR hereby authorizes and requests the Director of the United States Patent and Trademark Office to issue such patents as shall be granted upon said applications, or applications based thereon, to said ASSIGNEE, its successors or assigns.

[SIGNATURE PAGE FOLLOWS]

IN TESTIMONY WHEREOF, ASSIGNOR has caused this instrument to be duly executed and hereunto hands have been set out and a seal affixed on the date set forth below.

ASSIGNOR:

INTEL CORPORATION

By: 
Name: **RAHEEL SHAH**
Title: **ASSISTANT TREASURER**

[SIGNATURE PAGE TO INTEL/ADI PATENT ASSIGNMENT]

ACKNOWLEDGMENT

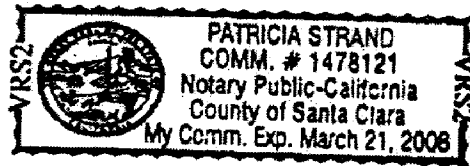
STATE OF CALIFORNIA)
) ss.
COUNTY OF Santa Clara)

On 11/08/06, before me, Patricia Strand,
Notary Public in and for said State, personally appeared Raheel A. Shah,
personally known to me or proved to me on the basis of satisfactory evidence to be the
person whose name is subscribed to within the forgoing instrument and acknowledged to
me that he or she executed the same in his or her authorized capacity, and that by his or
her signature on the instrument the person, or the entity upon behalf of which the person
acted, executed the instrument.

WITNESS my hand and official seal.

Signature Patricia Strand

(Seal)



Schedule B-1

To Patent Assignment By Intel Corporation to Analog Devices, Inc.

APD1637-1-US	P8092	10559-184001	SYSTEM HAVING A CONFIGURABLE CACHE/SRAM MEMORY	Hebbalalu S. Ramagopal; William C. Anderson; Ravi K. Kolagolla; Michael Allen; David B. Witt; Lawrence A. Booth Jr.; Mohini Syed	3/31/2000	3/31/2000	US	09/541,117	Utility	6,346,181	9/3/2002
APD1637-1-CN	P8092CN	10559-184CN1	SYSTEM HAVING A CONFIGURABLE CACHE/SRAM MEMORY	Hebbalalu S. Ramagopal; William C. Anderson; Ravi K. Kolagolla; Michael Allen; David B. Witt; Lawrence A. Booth Jr.; Mohini Syed	3/31/2000	9/30/2002	CN	01807891.2	Designated Country Application	1220150C	9/21/2005
APD1637-1-EPO	P8092EP	10559-184EP1	SYSTEM HAVING A CONFIGURABLE CACHE/SRAM MEMORY	Hebbalalu S. Ramagopal; William C. Anderson; Ravi K. Kolagolla; Michael Allen; David B. Witt; Lawrence A. Booth Jr.; Mohini Syed	3/31/2000	10/3/2002	EP	01922815.2	Designated Country Application		
APD1637-1-HK	P8092HK	10559-184HK1	SYSTEM HAVING A CONFIGURABLE CACHE/SRAM MEMORY	Hebbalalu S. Ramagopal; William C. Anderson; Ravi K. Kolagolla; Michael Allen; David B. Witt; Lawrence A. Booth Jr.; Mohini Syed	3/31/2000	4/7/2003	HK	03102444.5	Country Equivalent		
APD1637-1-SG	P8092SG	10559-184SG1	SYSTEM HAVING A CONFIGURABLE CACHE/SRAM MEMORY	Hebbalalu S. Ramagopal; William C. Anderson; Ravi K. Kolagolla; Michael Allen; David B. Witt; Lawrence A. Booth Jr.; Mohini Syed	3/31/2000	9/29/2002	SG	200205841-0	Designated Country Application	9206\$	2/28/2006
APD1637-1-TW	P8092TW	10559-184TW1	SYSTEM HAVING A CONFIGURABLE CACHE/SRAM MEMORY	Hebbalalu S. Ramagopal; William C. Anderson; Ravi K. Kolagolla; Michael Allen; David B. Witt; Lawrence A. Booth Jr.; Mohini Syed	3/31/2000	3/30/2001	TW	90107589	Country Equivalent	1-233989	11/21/2005
APD1637-1-PCT	P8092PCT	10559-184WO1	SYSTEM HAVING A CONFIGURABLE CACHE/SRAM MEMORY	Hebbalalu S. Ramagopal; William C. Anderson; Ravi K. Kolagolla; Michael Allen; David B. Witt; Lawrence A. Booth Jr.; Mohini Syed	3/31/2000	3/30/2001	WO	PCT/US01/102 99	Country Equivalent	Expired	
APD1639-1-US	P8093	10559-185001	MULTI-TIERED MEMORY BANK HAVING DIFFERENT DATA BUFFER SIZES WITH A PROGRAMMABLE BANK SELECT	Hebbalalu S. Ramagopal; Jose Friedman; Marc Hoffman; Michael Allen	3/31/2000	3/31/2000	US	09/541,114	Utility	6,606,684	8/12/2003
APD1639-2-US	P8093C	10559-185002	MULTI-TIERED MEMORY BANK HAVING DIFFERENT DATA BUFFER SIZES WITH A PROGRAMMABLE BANK SELECT	Hebbalalu S. Ramagopal; Jose Friedman; Marc Hoffman; Michael Allen	3/31/2000	8/11/2003	US	10/639,820	Continuation	6,898,690	5/24/2005

APD1639-1-CN	P8089CN	10559-185CNT	MULTI-TIERED MEMORY BANK HAVING DIFFERENT DATA BUFFER SIZES WITH A PROGRAMMABLE BANK SELECT	Hebbalalu S. Ramagopal; Jose Fridman; Marc Hoffman; Michael Allen	3/31/2000	9/30/2002	CN	01807693.9	Designated Country Application	1201233C	5/1/2006
APD1639-1-EPO	P8089EP	10559-185EP1	MULTI-TIERED MEMORY BANK HAVING DIFFERENT DATA BUFFER SIZES WITH A PROGRAMMABLE BANK SELECT	Hebbalalu S. Ramagopal; Jose Fridman; Marc Hoffman; Michael Allen	3/31/2000	10/3/2002	EP	01926544.6	Designated Country Application		
APD1639-1-SG	P8089SG	10559-185SG1	MULTI-TIERED MEMORY BANK HAVING DIFFERENT DATA BUFFER SIZES WITH A PROGRAMMABLE BANK SELECT	Hebbalalu S. Ramagopal; Jose Fridman; Marc Hoffman; Michael Allen	3/31/2000	9/26/2002	SG	200205840-2	Designated Country Application	97064	11/30/2004
APD1639-1-TW	P8089TW	10559-185TW1	MULTI-TIERED MEMORY BANK HAVING DIFFERENT DATA BUFFER SIZES WITH A PROGRAMMABLE BANK SELECT	Hebbalalu S. Ramagopal; Jose Fridman; Marc Hoffman; Michael Allen	3/31/2000	3/30/2001	TW	90107608	Country Equivalent	NI-178578	9/22/2003
APD1639-1-PCT	P8089PCT	10559-185WO1	MULTI-TIERED MEMORY BANK HAVING DIFFERENT DATA BUFFER SIZES WITH A PROGRAMMABLE BANK SELECT	Hebbalalu S. Ramagopal; Jose Fridman; Marc Hoffman; Michael Allen	3/31/2000	3/30/2001	WO	PCT/US01/105 73	Country Equivalent	Expired	
APD1636-1-US	P8089	10559-186001	ADAPTIVE EARLY EXIT TECHNIQUES IN IMAGE CORRELATION	Bradley C. Aldrich; Jose Fridman	6/7/2000	6/7/2000	US	09/589,630	Utility	6,654,302	11/25/2003
APD1636-2-US	P8089C	10559-186002	ADAPTIVE EARLY EXIT TECHNIQUES IN IMAGE CORRELATION	Bradley C. Aldrich; Jose Fridman	6/7/2000	11/24/2003	US	10/721,593	Continuation		
APD1636-1-CN	P8089CN	10559-186CN1	ADAPTIVE EARLY EXIT TECHNIQUES FOR MINIMUM DISTORTION CALCULATION IN IMAGE CORRELATION	Bradley C. Aldrich; Jose Fridman	6/7/2000	12/5/2002	CN	01810826.8	Designated Country Application		
APD1636-1-JP	P8089JP	10559-186JP1	ADAPTIVE EARLY EXIT TECHNIQUES FOR MINIMUM DISTORTION CALCULATION IN IMAGE CORRELATION	Bradley C. Aldrich; Jose Fridman	6/7/2000	12/6/2002	JP	P2002-503042	Designated Country Application		
APD1636-1-KR	P8089KR	10559-186KR1	ADAPTIVE EARLY EXIT TECHNIQUES FOR MINIMUM DISTORTION CALCULATION IN IMAGE CORRELATION	Bradley C. Aldrich; Jose Fridman	6/7/2000	12/6/2002	KR	7016708	Designated Country Application	604169	7/18/2006

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APD1636-1-SG	P8089SG	10559-186SG1	ADAPTIVE EARLY EXIT TECHNIQUES FOR MINIMUM DISTORTION CALCULATION IN IMAGE CORRELATION	Bradley C. Aldrich, Jose Fridman	6/7/2000	12/2/2002	SG	200207411-0	Designated Country Application	93558	1/31/2005
APD1636-1-TW	P8089TW	10559-186TW1	ADAPTIVE EARLY EXIT TECHNIQUES IN IMAGE CORRELATION	Bradley C. Aldrich, Jose Fridman	6/7/2000	6/7/2001	TW	90113325	Country Equivalent	NI-174198	3/21/2003
APD1636-1-PCT	P8089PCT	10559-186WO1	ADAPTIVE EARLY EXIT TECHNIQUES FOR MINIMUM DISTORTION CALCULATION IN IMAGE CORRELATION	Bradley C. Aldrich, Jose Fridman	6/7/2000	6/6/2001	WQ	PCT/US01/408 71	Country Equivalent	Expired	
APD2798-1-US	P8090	10559-187001	ADAPTIVE EARLY EXIT TECHNIQUES IN IMAGE CORRELATION	Bradley C. Aldrich, Jose Fridman	6/7/2000	6/7/2000	US	09/589/624	Utility	6,700,996	3/2/2004
APD2798-2-US	P8090C	10559-187002	ADAPTIVE EARLY EXIT TECHNIQUES IN IMAGE CORRELATION	Bradley C. Aldrich, Jose Fridman	6/7/2000	2/9/2004	US	10/775,261	Continuation	7,031,498	4/18/2008
APD2798-1-CN	P8090CN	10559-187CN1	ADAPTIVE EARLY EXIT TECHNIQUES FOR MINIMUM DISTORTION CALCULATION IN IMAGE CORRELATION	Bradley C. Aldrich, Jose Fridman	6/7/2000	12/2/2002	CN	01810828.2	Designated Country Application		
APD2798-1-JP	P8090JP	10559-187JP1	ADAPTIVE EARLY EXIT TECHNIQUES FOR MINIMUM DISTORTION CALCULATION IN IMAGE CORRELATION	Bradley C. Aldrich, Jose Fridman	6/7/2000	12/6/2002	JP	P2002-603041	Designated Country Application	3865240	10/13/2008
APD2798-1-KR	P8090KR	10559-187KR1	ADAPTIVE EARLY EXIT TECHNIQUES FOR MINIMUM DISTORTION CALCULATION IN IMAGE CORRELATION	Bradley C. Aldrich, Jose Fridman	6/7/2000	12/6/2002	KR	7016709	Designated Country Application	551543	2/6/2006
APD2798-1-SG	P8090SG	10559-187SG1	ADAPTIVE EARLY EXIT TECHNIQUES FOR MINIMUM DISTORTION CALCULATION IN IMAGE CORRELATION	Bradley C. Aldrich, Jose Fridman	6/7/2000	12/2/2002	SG	200207410-2	Designated Country Application		
APD2798-2-SG	P8090SG-D	10559-187SG2	ADI - ADAPTIVE EARLY EXIT TECHNIQUES FOR MINIMUM DISTORTION CALCULATION IN IMAGE CORRELATION	Bradley C. Aldrich, Jose Fridman	6/7/2000	12/7/2004	SG	200407854-9	Divisional of Designated Country Application		
APD2798-1-TW	P8090TW	10559-187TW1	ADAPTIVE EARLY EXIT TECHNIQUES IN IMAGE CORRELATION	Bradley C. Aldrich, Jose Fridman	6/7/2000	6/7/2001	TW	90113826	Country Equivalent	NI-172568	6/23/2003

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APD2798-1-POI	P8090PCT	10558-187WO1	ADAPTIVE EARLY EXIT TECHNIQUES FOR MINIMUM DISTORTION CALCULATION IN IMAGE CORRELATION	Bradley C. Aldrich, Jose Fridman	6/7/2000	6/6/2001	WFO	PCT/US01/408 63	Country Equivalent	Expired	
APD2799-1-US	P8091	10559-188001	ADAPTIVE EARLY EXIT TECHNIQUES FOR MINIMUM DISTORTION CALCULATION IN IMAGE CORRELATION	Bradley C. Aldrich, Jose Fridman	6/7/2000	6/7/2000	US	09/590,026	Utility	6,907,080	6/14/2005
APD2798-2-US	P8091D	10559-188002	ADAPTIVE EARLY EXIT TECHNIQUES FOR MINIMUM DISTORTION CALCULATION IN IMAGE CORRELATION	Bradley C. Aldrich, Jose Fridman	6/7/2000	5/25/2005	US	11/138,825	Divisional		
APD2798-1-CN	P8091CN	10559-188CN1	ADAPTIVE EARLY EXIT TECHNIQUES FOR MINIMUM DISTORTION CALCULATION IN IMAGE CORRELATION	Bradley C. Aldrich, Jose Fridman	6/7/2000	12/5/2002	CN	01810831.4	Designated Country Application		
APD2798-1-JP	P8091JP	10559-188JP1	ADAPTIVE EARLY EXIT TECHNIQUES FOR MINIMUM DISTORTION CALCULATION IN IMAGE CORRELATION	Bradley C. Aldrich, Jose Fridman	6/7/2000	12/6/2002	JP	P2002-503043	Designated Country Application	386,241	10/13/2006
APD2798-1-KR	P8091KR	10559-188KR1	ADAPTIVE EARLY EXIT TECHNIQUES FOR MINIMUM DISTORTION CALCULATION IN IMAGE CORRELATION	Bradley C. Aldrich, Jose Fridman	6/7/2000	12/6/2002	KR	7016707	Designated Country Application		
APD2798-1-SG	P8091SG	10559-188SG1	ADAPTIVE EARLY EXIT TECHNIQUES FOR MINIMUM DISTORTION CALCULATION IN IMAGE CORRELATION	Bradley C. Aldrich, Jose Fridman	6/7/2000	12/2/2002	SG	200207412-8	Designated Country Application		
APD2799-2-SG	P8091SG-D	10559-188SG2	ADAPTIVE EARLY EXIT TECHNIQUES FOR MINIMUM DISTORTION CALCULATION IN IMAGE CORRELATION	Bradley C. Aldrich, Jose Fridman	6/7/2000	8/3/2004	SG	200403796-2	Divisional of Designated Country Application		
APD2798-1-TW	P8091TW	10559-188TW1	ADAPTIVE EARLY EXIT TECHNIQUES FOR MINIMUM DISTORTION CALCULATION IN IMAGE CORRELATION	Bradley C. Aldrich, Jose Fridman	6/7/2000	6/7/2001	TW	90113B27	Country Equivalent	XI-182,549	8/12/2003

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APD2759-1-PCT	P8081PCT	10559-188WO1	ADAPTIVE EARLY EXIT TECHNIQUES FOR MINIMUM DISTORTION CALCULATION IN IMAGE CORRELATION	Bradley C. Aldrich, Jose Fridman	6/7/2000	6/6/2001	WQ	PCT/US01/408 76	Country Equivalent	Expired
APD1635-1-US	P8467	10559-201WO1	MULTIPLIER ARCHITECTURE IN A GENERAL PURPOSE PROCESSOR OPTIMIZED FOR EFFICIENT MULTI- INPUT ADDITION	Bradley C. Aldrich, Ravi K. Kolagolla, William C. Anderson	3/31/2000	3/31/2000	US	09/539,922	Utility	Abandoned
APD1635-1-CN	P8467CN	10559-201CN1	MULTIPLIER ARCHITECTURE IN A GENERAL PURPOSE PROCESSOR OPTIMIZED FOR EFFICIENT MULTI- INPUT ADDITION	Bradley C. Aldrich, Ravi K. Kolagolla, William C. Anderson	3/31/2000	9/30/2002	CN	01807694.7	Designated Country Application	Abandoned
APD1635-1-EPO	P8467EP	10559-201EP1	MULTIPLIER ARCHITECTURE IN A GENERAL PURPOSE PROCESSOR OPTIMIZED FOR EFFICIENT MULTI- INPUT ADDITION	Bradley C. Aldrich, Ravi K. Kolagolla, William C. Anderson	3/31/2000	10/3/2002	EP	01923026.3	Designated Country Application	Abandoned
APD1635-1-HK	P8467HK	10559-201HK1	MULTIPLIER ARCHITECTURE IN A GENERAL PURPOSE PROCESSOR OPTIMIZED FOR EFFICIENT MULTI- INPUT ADDITION	Bradley C. Aldrich, Ravi K. Kolagolla, William C. Anderson	3/31/2000	4/7/2003	HK	03102443.6	Country Equivalent	Abandoned
APD1635-1-SG	P8467SG	10559-201SG1	MULTIPLIER ARCHITECTURE IN A GENERAL PURPOSE PROCESSOR OPTIMIZED FOR EFFICIENT MULTI- INPUT ADDITION	Bradley C. Aldrich, Ravi K. Kolagolla, William C. Anderson	3/31/2000	9/26/2002	SG	200205939-2	Designated Country Application	Abandoned
APD1635-1-TW	P8467TW	10559-201TW1	MULTIPLIER ARCHITECTURE IN A GENERAL PURPOSE PROCESSOR OPTIMIZED FOR EFFICIENT MULTI- INPUT ADDITION	Bradley C. Aldrich, Ravi K. Kolagolla, William C. Anderson	3/31/2000	3/30/2001	TW	090107607	Country Equivalent	Abandoned
APD1635-1-PCT	P8467PCT	10559-201WO1	MULTIPLIER ARCHITECTURE IN A GENERAL PURPOSE PROCESSOR OPTIMIZED FOR EFFICIENT MULTI- INPUT ADDITION	Bradley C. Aldrich, Ravi K. Kolagolla, William C. Anderson	3/31/2000	4/2/2001	WO	PCT/US01/106 03	Country Equivalent	Expired

2006_11_06_Final_Intel-ADI_Detailed Patent List [ADI_revision].xls - ver. 1.4

APD1632-1-US	P8465	10559-202001	DSP EXECUTION UNIT FOR EFFICIENT ALTERNATE MODES OF OPERATION	Bradley C. Aldrich; Jose Fridman; Paul Meyer; Gang Liang	3/31/2000	3/31/2000	US	09/841,116	Utility	6,725,360	4/20/2004
APD1632-2-US	P8465D	10559-202002	DSP EXECUTION UNIT FOR EFFICIENT ALTERNATE MODES OF OPERATION	Bradley C. Aldrich; Jose Fridman; Paul Meyer; Gang Liang	3/31/2000	4/20/2004	US	10/828,913	Divisional	7,047,221	5/16/2006
APD1632-1-CN	P8465CN	10559-202CN1	DSP EXECUTION UNIT OPTIMIZED FOR EFFICIENT 8/16-BIT ALTERNATE MODES OF OPERATION	Bradley C. Aldrich; Jose Fridman; Paul Meyer; Gang Liang	3/31/2000	9/30/2002	CN	01807692.0	Designated Country Application		
APD1632-1-EP	P8465EP	10559-202EP1	DSP EXECUTION UNIT OPTIMIZED FOR EFFICIENT 8/16-BIT ALTERNATE MODES OF OPERATION	Bradley C. Aldrich; Jose Fridman; Paul Meyer; Gang Liang	3/31/2000	10/3/2002	EP	01924583.0	Designated Country Application		
APD1632-1-HK	P8465HK	10559-202HK1	DSP EXECUTION UNIT OPTIMIZED FOR EFFICIENT 8/16-BIT ALTERNATE MODES OF OPERATION	Bradley C. Aldrich; Jose Fridman; Paul Meyer; Gang Liang	3/31/2000	5/1/2004	HK	04102747.8	Country Equivalent		
APD1632-1-SG	P8465SG	10559-202SG1	DSP EXECUTION UNIT OPTIMIZED FOR EFFICIENT 8/16-BIT ALTERNATE MODES OF OPERATION	Bradley C. Aldrich; Jose Fridman; Paul Meyer; Gang Liang	3/31/2000	9/23/2002	SG	200205938-4	Designated Country Application	92133	3/31/2005
APD1632-1-TW	P8465TW	10559-202TW1	DSP EXECUTION UNIT OPTIMIZED FOR EFFICIENT 8/16-BIT ALTERNATE MODES OF OPERATION	Bradley C. Aldrich; Jose Fridman; Paul Meyer; Gang Liang	3/31/2000	3/30/2001	TW	90107601	Country Equivalent	NI-186812	9/11/2003
APD1632-1-PCT	P8465PCT	10559-202WO1	DSP EXECUTION UNIT OPTIMIZED FOR EFFICIENT 8/16-BIT ALTERNATE MODES OF OPERATION	Bradley C. Aldrich; Jose Fridman; Paul Meyer; Gang Liang	3/31/2000	4/2/2001	WO	PCT/US01/10602	Country Equivalent	Expired	
APD1634-1-US	P8466	10559-203001	DSP UNIT FOR MULTI-LEVEL GLOBAL ACCUMULATION	Bradley C. Aldrich; Ravi K. Kolegolia	3/31/2000	3/31/2000	US	09/544,148	Utility	6,601,073	7/29/2003
APD1634-3-US	P8468G	10559-203002	DSP UNIT FOR MULTI-LEVEL GLOBAL ACCUMULATION	Bradley C. Aldrich; Ravi K. Kolegolia	3/31/2000	7/28/2003	US	10/630,517	Continuation	6,820,162	11/16/2004
APD1785-1-US	P8806	10559-257001	GENERAL PURPOSE REGISTER FILE ARCHITECTURE FOR ALIGNED SIMD	Ravi K. Kolegolia; David B. Witt; Bradley C. Aldrich	6/30/2000	6/30/2000	US	09/608,883	Utility	7,120,781	10/10/2006
APD1785-1-CN	P8806CN	10559-257CN1	GENERAL PURPOSE REGISTER FILE ARCHITECTURE FOR ALIGNED SIMD	Ravi K. Kolegolia; David B. Witt; Bradley C. Aldrich	6/30/2000	6/25/2001	CN	01123388.5	Country Equivalent	1246789C	3/22/2006
APD1785-1-JP	P8806JP	10559-257JP1	GENERAL PURPOSE REGISTER FILE ARCHITECTURE FOR ALIGNED SIMD	Ravi K. Kolegolia; David B. Witt; Bradley C. Aldrich	6/30/2000	7/2/2001	JP	2001-201406	Country Equivalent		

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APD1795-1-KR	P8906KR	10559-257KR1	GENERAL PURPOSE REGISTER FILE ARCHITECTURE FOR ALIGNED SIMD	Ravi K. Kolagolla; David B. Witt; Bradley C. Aldrich	6/30/2000	5/30/2001	KR	2001-0038638	Country Equivalent	0463121	12/14/2004
APD1795-1-SG	P8906SG	10559-257SG1	GENERAL PURPOSE REGISTER FILE ARCHITECTURE FOR ALIGNED SIMD	Ravi K. Kolagolla; David B. Witt; Bradley C. Aldrich	6/30/2000	6/25/2001	SG	200103878-5	Country Equivalent	105500	4/29/2005
APD1795-1-TW	P8906TW	10559-257TW1	GENERAL PURPOSE REGISTER FILE ARCHITECTURE FOR ALIGNED SIMD	Ravi K. Kolagolla; David B. Witt; Bradley C. Aldrich	8/30/2000	6/29/2001	TW	90115068	Country Equivalent	NL189124	11/12/2003
APD1796-1-US	P9277	10559-270001	PARTIAL INSTRUCTION DECODING AND RE-ENCODING	Gregory A. Overkamp; Ravi P. Singh; Charles P. Roth	9/28/2000	9/28/2000	US	091875,816	Utility	6,976,151	12/13/2005
APD1865-1-US	P9278	10559-271001	PROCESSOR IDLE STATE	Charles P. Roth; Ravi P. Singh; Thomas Tomazin; David B. Witt; Ravi K. Kolagolla; Juan G. Revilla	11/13/2000	11/13/2000	US	09771,496	Utility		
APD1865-1-CN	P9278CN	10559-271CN1	PROCESSOR IDLE STATE	Charles P. Roth; Ravi P. Singh; Thomas Tomazin; David B. Witt; Ravi K. Kolagolla; Juan G. Revilla	11/13/2000	5/9/2003	CN	01818788.6	Designated Country Application		
APD1865-1-JP	P9278JP	10559-271JP1	PROCESSOR IDLE STATE	Charles P. Roth; Ravi P. Singh; Thomas Tomazin; David B. Witt; Ravi K. Kolagolla; Juan G. Revilla	11/13/2000	5/9/2003	JP	P2002-548560	Designated Country Application		
APD1865-1-KR	P9278KR	10559-271KR1	PROCESSOR IDLE STATE	Charles P. Roth; Ravi P. Singh; Thomas Tomazin; David B. Witt; Ravi K. Kolagolla; Juan G. Revilla	11/13/2000	5/7/2003	KR	10-2003-7009267	Designated Country Application	50027	6/30/2005
APD1865-1-SG	P9278SG	10559-271SG1	PROCESSOR IDLE STATE	Charles P. Roth; Ravi P. Singh; Thomas Tomazin; David B. Witt; Ravi K. Kolagolla; Juan G. Revilla	11/13/2000	4/30/2003	SG	200302465-6	Designated Country Application	96783	1/31/2005
APD1865-1-TW	P9278TW	10559-271TW1	PROCESSOR IDLE STATE	Charles P. Roth; Ravi P. Singh; Thomas Tomazin; David B. Witt; Ravi K. Kolagolla; Juan G. Revilla	11/13/2000	11/12/2001	TW	90128004	Country Equivalent		
APD1865-1-PCT	P9278PCT	10559-271WO1	PROCESSOR IDLE STATE	Charles P. Roth; Ravi P. Singh; Thomas Tomazin; David B. Witt; Ravi K. Kolagolla; Juan G. Revilla	11/13/2000	11/13/2001	WO	PCT/US01/43412	Country Equivalent	Expired	
APD1866-1-US	P9279	10559-272001	DATA SYNCHRONIZATION FOR A TEST ACCESS PORT	Charles P. Roth; Ravi P. Singh; Ravi Kolagolla; Tien Dinh	12/15/2000	12/15/2000	US	08738,405	Utility		

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APD1868-1-CN	P8279CN	10559-272CN1	DATA SYNCHRONIZATION FOR A TEST ACCESS PORT	Charles P. Roth; Ravi P. Singh; Ravi Kolagotla; Tien Dinh	12/15/2000	6/10/2003	CN	01820708.X	Designated Country Application	12/05/77C	6/21/2006
APD1868-1-JP	P8279JP	10559-272JP1	DATA SYNCHRONIZATION FOR A TEST ACCESS PORT	Charles P. Roth; Ravi P. Singh; Ravi Kolagotla; Tien Dinh	12/15/2000	9/4/2003	JP	P2002-549976	Designated Country Application		
APD1868-1-KR	P8279KR	10559-272KR1	DATA SYNCHRONIZATION FOR A TEST ACCESS PORT	Charles P. Roth; Ravi P. Singh; Ravi Kolagotla; Tien Dinh	12/15/2000	6/12/2003	KR	2003-7007846	Designated Country Application	551546	2/6/2008
APD1868-1-SG	P8279SG	10559-272SG1	DATA SYNCHRONIZATION FOR A TEST ACCESS PORT	Charles P. Roth; Ravi P. Singh; Ravi Kolagotla; Tien Dinh	12/15/2000	5/16/2003	SG	200302817-2	Designated Country Application	96904	10/31/2005
APD1868-1-TW	P8279TW	10559-272TW1	DATA SYNCHRONIZATION FOR A TEST ACCESS PORT	Charles P. Roth; Ravi P. Singh; Ravi Kolagotla; Tien Dinh	12/15/2000	12/14/2001	TW	90131094	Country Equivalent	NI-224976	10/11/2004
APD1868-1-PCT	P8279PCT	10559-272WO1	DATA SYNCHRONIZATION FOR A TEST ACCESS PORT	Charles P. Roth; Ravi P. Singh; Ravi Kolagotla; Tien Dinh	12/15/2000	12/10/2001	WO	PCT/US01/476 25	Country Equivalent	Expired	
APD1868-1-US	P8280	10559-273001	EXTENDED PROCESSOR RESET	Ravi P. Singh; Charles P. Roth; Ravi K. Kolagotla; Juan G. Revilla	12/15/2000	12/16/2000	US	08/738,082	Utility	6,789,187	9/7/2004
APD1868-1-CN	P8280CN	10559-273CN1	EXTENDED PROCESSOR RESET	Ravi P. Singh; Charles P. Roth; Ravi K. Kolagotla; Juan G. Revilla	12/15/2000	6/10/2003	CN	01820701.4	Designated Country Application		
APD1868-1-JP	P8280JP	10559-273JP1	EXTENDED PROCESSOR RESET	Ravi P. Singh; Charles P. Roth; Ravi K. Kolagotla; Juan G. Revilla	12/15/2000	6/4/2003	JP	P2002-650516	Designated Country Application		
APD1868-1-KR	P8280KR	10559-273KR1	EXTENDED PROCESSOR RESET	Ravi P. Singh; Charles P. Roth; Ravi K. Kolagotla; Juan G. Revilla	12/15/2000	6/12/2003	KR	2003-7007847	Designated Country Application	571324	4/10/2008
APD1868-1-SG	P8280SG	10559-273SG1	EXTENDED PROCESSOR RESET	Ravi P. Singh; Charles P. Roth; Ravi K. Kolagotla; Juan G. Revilla	12/15/2000	5/22/2003	SG	200302880-0	Designated Country Application	96976	7/31/2008
APD1868-1-TW	P8280TW	10559-273TW1	EXTENDED PROCESSOR RESET	Ravi P. Singh; Charles P. Roth; Ravi K. Kolagotla; Juan G. Revilla	12/15/2000	12/14/2001	TW	90131101	Country Equivalent	NI-180431	10/22/2003
APD1868-1-PCT	P8280PCT	10559-273WO1	Instruction Loading After Processor Reset	Ravi P. Singh; Charles P. Roth; Ravi K. Kolagotla; Juan G. Revilla	12/15/2000	12/10/2001	WO	PCT/US01/476 23	Country Equivalent	Expired	
APD1797-1-US	P8281	10559-274001	DECODE AND DISPATCH OF MULTITHREAD AND MULTIPLE WIDTH INSTRUCTIONS	Gregory A. Overkamp; Ravi P. Singh; Charles P. Roth	9/28/2000	9/28/2000	US	08/675,815	Utility	7,069,420	6/27/2006
APD1798-1-US	P8282	10559-275001	MULTIPLE SOURCES FOR INSTRUCTION DECODE	Gregory A. Overkamp; Ravi P. Singh; Charles P. Roth	9/28/2000	9/28/2000	US	09/670,907	Utility	Abandoned	
APD1798-1-CN	P8282CN	10559-275CN1	MULTIPLE SOURCES FOR INSTRUCTION DECODE	Gregory A. Overkamp; Ravi P. Singh; Charles P. Roth	9/28/2000	3/28/2003	CN	01818463.3	Designated Country Application	1261864C	6/28/2008

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APD1798-1-JP	P9282JP	10559-275JP1	MULTIPLE SOURCES FOR INSTRUCTION DECODE	Gregory A. Overkamp; Ravi P. Singh; Charles P. Roth	9/28/2000	3/27/2003	JP	2002-530993	Designated Country Application	3704319	10/12/2006
APD1798-1-KR	P9282KR	10559-275KR1	MULTIPLE SOURCES FOR INSTRUCTION DECODE	Gregory A. Overkamp; Ravi P. Singh; Charles P. Roth	9/28/2000	3/27/2003	KR	1020037004440	Designated Country Application		
APD1798-1-SG	P9282SG	10559-275SG1	MULTIPLE SOURCES FOR INSTRUCTION DECODE	Gregory A. Overkamp; Ravi P. Singh; Charles P. Roth	9/28/2000	3/27/2003	SG	200301844-7	Designated Country Application	93961	8/31/2006
APD1798-1-TW	P9282TW	10559-275TW1	MULTIPLE SOURCES FOR INSTRUCTION DECODE	Gregory A. Overkamp; Ravi P. Singh; Charles P. Roth	9/28/2000	9/28/2001	TW	90124152	Country Equivalent	1-237787	8/1/2005
APD1798-1-PCT	P9282PCT	10559-275WO1	MULTIPLE SOURCES FOR INSTRUCTION DECODE	Gregory A. Overkamp; Ravi P. Singh; Charles P. Roth	9/28/2000	9/25/2001	WO	PCT/US01/30261	Country Equivalent	Expired	
APD1867-1-US	P9283	10559-276001	MULTI-CYCLE INSTRUCTIONS	Gregory A. Overkamp; Ravi P. Singh; Charles P. Roth	12/6/2000	12/6/2000	US	09/731,523	Utility		
APD1867-1-CN	P9283CN	10559-276CN1	MULTI-CYCLE INSTRUCTIONS	Gregory A. Overkamp; Ravi P. Singh; Charles P. Roth	12/6/2000	6/6/2003	CN	01820175.X	Designated Country Application		
APD1867-1-JP	P9283JP	10559-276JP1	MULTI-CYCLE INSTRUCTIONS	Gregory A. Overkamp; Ravi P. Singh; Charles P. Roth	12/6/2000	5/29/2003	JP	P2002-548581	Designated Country Application	3737083	11/4/2006
APD1867-1-KR	P9283KR	10559-276KR1	MULTI-CYCLE INSTRUCTIONS	Gregory A. Overkamp; Ravi P. Singh; Charles P. Roth	12/6/2000	6/5/2003	KR	2003-7007429	Designated Country Application	371328	4/10/2006
APD1867-1-SG	P9283SG	10559-276SG1	MULTI-CYCLE INSTRUCTIONS	Gregory A. Overkamp; Ravi P. Singh; Charles P. Roth	12/6/2000	5/16/2003	SG	200302890-3	Designated Country Application	97311	11/30/2006
APD1867-1-TW	P9283TW	10559-276TW1	MULTI-CYCLE INSTRUCTIONS	Gregory A. Overkamp; Ravi P. Singh; Charles P. Roth	12/6/2000	12/5/2001	TW	90130085	Country Equivalent	NI-200629	5/1/2004
APD1867-1-PCT	P9283PCT	10559-276WO1	MULTI-CYCLE INSTRUCTIONS	Gregory A. Overkamp; Ravi P. Singh; Charles P. Roth	12/6/2000	12/6/2001	WO	PCT/US01/46706	Country Equivalent	Expired	
APD1869-1-US	P9284	10559-277001	SINGLE-STEP PROCESSING	Charles P. Roth; Ravi P. Singh; Tien Dinh; Ravi Kolagolla; Marc Hoffman; Russell Rivin	12/15/2000	12/15/2000	US	09/738,649	Utility	6,986,026	1/10/2006
APD1869-1-CN	P9284CN	10559-277CN1	SINGLE-STEP PROCESSING	Charles P. Roth; Ravi P. Singh; Tien Dinh; Ravi Kolagolla; Marc Hoffman; Russell Rivin	12/15/2000	6/16/2003	CN	01820700.6	Designated Country Application		
APD1869-1-JP	P9284JP	10559-277JP1	SINGLE-STEP PROCESSING	Charles P. Roth; Ravi P. Singh; Tien Dinh; Ravi Kolagolla; Marc Hoffman; Russell Rivin	12/15/2000	6/13/2003	JP	P2002-550631	Designated Country Application	3837332	9/22/2006
APD1869-1-KR	P9284KR	10559-277KR1	SINGLE-STEP PROCESSING	Charles P. Roth; Ravi P. Singh; Tien Dinh; Ravi Kolagolla; Marc Hoffman; Russell Rivin	12/15/2000	6/12/2003	KR	2003-7607848	Designated Country Application		

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APD1869-1-SG	P9284SG	10559-277SG1	SINGLE-STEP PROCESSING	Charles P. Roth; Ravi P. Singh; Tien Dinh; Ravi Kolagolla; Marc Hoffman; Russell Rivin	12/15/2000	5/16/2003	SG	200302848-7	Designated Country Application	96916	10/31/2005
APD1869-1-TW	P9284TW	10559-277TW1	SINGLE-STEP PROCESSING	Charles P. Roth; Ravi P. Singh; Tien Dinh; Ravi Kolagolla; Marc Hoffman; Russell Rivin	12/15/2000	12/14/2001	TW	90131098	Country Equivalent	1-240017	12/21/2005
APD1869-1-PCT	P9284PCT	10559-277WO1	SINGLE-STEP PROCESSING	Charles P. Roth; Ravi P. Singh; Tien Dinh; Ravi Kolagolla; Marc Hoffman; Russell Rivin	12/15/2000	12/10/2001	WO	PCT/US01/476 24	Country Equivalent	Expired	
APD1799-1-US	P9285	10559-278001	EVENT HANDLING	Charles P. Roth; Ravi P. Singh	11/2/2000	11/2/2000	US	09705.207	Utility	6,842,812	11/11/2005
APD1799-2-US	P9285C	10559-278002	EVENT HANDLING	Charles P. Roth; Ravi P. Singh	11/2/2000	11/11/2005	US	11/033.961	Continuation		
APD1799-1-CN	P9285CN	10559-278CN1	EVENT HANDLING	Charles P. Roth; Ravi P. Singh	11/2/2000	4/24/2003	CN	01818105.8	Designated Country Application		
APD1799-1-JP	P9285JP	10559-278JP1	EVENT HANDLING	Charles P. Roth; Ravi P. Singh	11/2/2000	5/1/2003	JP	82002-561888	Designated Country Application	3823568	6/30/2008
APD1799-1-KR	P9285KR	10559-278KR1	EVENT HANDLING	Charles P. Roth; Ravi P. Singh	11/2/2000	4/30/2003	KR	2003-7005998	Designated Country Application	521110	10/6/2005
APD1799-1-SG	P9285SG	10559-278SG1	EVENT HANDLING	Charles P. Roth; Ravi P. Singh	11/2/2000	3/10/2003	SG	200301131-9	Designated Country Application	98301	10/31/2005
APD1799-1-TW	P9285TW	10559-278TW1	EVENT HANDLING	Charles P. Roth; Ravi P. Singh	11/2/2000	11/1/2001	TW	90127163	Country Equivalent	NI-185818	9/1/2003
APD1799-1-PCT	P9285PCT	10559-278WO1	EVENT HANDLING	Charles P. Roth; Ravi P. Singh	11/2/2000	10/30/2001	WO	PCT/US01/452 48	Country Equivalent	Expired	
APD1800-1-US	P9286	10559-278001	RESETTING A PROGRAMMABLE PROCESSOR	Gregory A. Overkamp; Ravi P. Singh; Charles P. Roth	10/6/2000	10/6/2000	US	09/684,113	Utility	6,663,995	12/18/2003
APD1800-1-CN	P9286CN	10559-278CN1	RESETTING A PROGRAMMABLE PROCESSOR	Gregory A. Overkamp; Ravi P. Singh; Charles P. Roth	10/6/2000	4/1/2003	CN	01817010.2	Designated Country Application		
APD1800-1-JP	P9286JP	10559-278JP1	RESETTING A PROGRAMMABLE PROCESSOR	Gregory A. Overkamp; Ravi P. Singh; Charles P. Roth	10/6/2000	3/17/2003	JP	P2002-533063	Designated Country Application		
APD1800-1-KR	P9286KR	10559-278KR1	RESETTING A PROGRAMMABLE PROCESSOR	Gregory A. Overkamp; Ravi P. Singh; Charles P. Roth	10/6/2000	3/18/2003	KR	2003-7003893	Designated Country Application	571332	4/10/2008
APD1800-1-SG	P9286SG	10559-278SG1	RESETTING A PROGRAMMABLE PROCESSOR	Gregory A. Overkamp; Ravi P. Singh; Charles P. Roth	10/6/2000	2/28/2003	SG	200301030-3	Designated Country Application	96289	6/30/2005

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APD1800-1-TW	P8286TW	10559-279TW1	RESETTING A PROGRAMMABLE PROCESSOR	Gregory A. Overkamp; Ravi P. Singh; Charles P. Roth	10/6/2000	10/5/2001	TW	90124676	Country Equivalent	NH-181812	11/12/2003
APD1800-1-PCT	P8286PCT	10559-279WO1	RESETTING A PROGRAMMABLE PROCESSOR	Gregory A. Overkamp; Ravi P. Singh; Charles P. Roth	10/6/2000	10/5/2001	WO	PCT/US01/42533	Country Equivalent	Expired	
APD1801-1-US	P9287	10559-280001	VALID BIT GENERATION AND TRACKING IN A PIPELINED PROCESSOR	Charles P. Roth; Gregory A. Overkamp; Ravi P. Singh; Thomas Tomazh	9/29/2000	9/29/2000	US	09/675,712	Utility	6,734,808	6/22/2004
APD1801-2-US	P9287C	10559-280002	ADI VALID BIT GENERATION AND TRACKING IN A PIPELINED PROCESSOR	Charles P. Roth; Gregory A. Overkamp; Ravi P. Singh; Thomas Tomazh	9/29/2000	5/17/2004	US	10/847,837	Continuation	7,036,090	4/25/2006
APD1870-1-US	P9288	10559-281001	WATCHPOINT ENGINE FOR A PIPELINED PROCESSOR	Charles P. Roth; Gregory A. Overkamp; Ravi P. Singh	12/16/2000	12/16/2000	US	09/739,092	Utility	6,839,101	12/7/2004
APD1870-1-CN	P9288CN	10559-281CN1	WATCHPOINT ENGINE FOR A PIPELINED PROCESSOR	Charles P. Roth; Gregory A. Overkamp; Ravi P. Singh	12/15/2000	6/10/2003	CN	01820706.5	Designated Country Application		
APD1870-1-JP	P9288JP	10559-281JP1	WATCHPOINT ENGINE FOR A PIPELINED PROCESSOR	Charles P. Roth; Gregory A. Overkamp; Ravi P. Singh	12/15/2000	6/13/2003	JP	P2002-550532	Designated Country Application	3,708,521	8/12/2005
APD1870-1-KR	P9288KR	10559-281KR1	WATCHPOINT ENGINE FOR A PIPELINED PROCESSOR	Charles P. Roth; Gregory A. Overkamp; Ravi P. Singh	12/15/2000	6/13/2003	KR	2003-7007896	Designated Country Application	5,717,32	4/11/2006
APD1870-1-SG	P9288SG	10559-281SG1	WATCHPOINT ENGINE FOR A PIPELINED PROCESSOR	Charles P. Roth; Gregory A. Overkamp; Ravi P. Singh	12/15/2000	5/22/2003	SG	200302978-2	Designated Country Application	96,981	6/30/2005
APD1870-1-TW	P9288TW	10559-281TW1	WATCHPOINT ENGINE FOR A PIPELINED PROCESSOR	Charles P. Roth; Gregory A. Overkamp; Ravi P. Singh	12/15/2000	12/14/2001	TW	90131069	Country Equivalent	1,235,949	7/11/2005
APD1870-1-PCT	P9288PCT	10559-281WO1	WATCHPOINT ENGINE FOR A PIPELINED PROCESSOR	Charles P. Roth; Gregory A. Overkamp; Ravi P. Singh	12/15/2000	12/10/2001	WO	PCT/US01/47521	Country Equivalent	Expired	
APD1802-1-US	P9289	10559-282001	EVENT VECTOR TABLE OVERRIDE	Charles P. Roth; Jose Fridman; Ravi K. Kolagolla	9/29/2000	9/28/2000	US	09/672,288	Utility	6,760,800	7/6/2004
APD1802-1-CN	P9289CN	10559-282CN1	EVENT VECTOR TABLE OVERRIDE	Charles P. Roth; Jose Fridman; Ravi K. Kolagolla	9/29/2000	3/28/2003	CN	01816466.8	Designated Country Application		
APD1802-1-JP	P9289JP	10559-282JP1	EVENT VECTOR TABLE OVERRIDE	Charles P. Roth; Jose Fridman; Ravi K. Kolagolla	9/29/2000	3/27/2003	JP	P2002-531001	Designated Country Application	3,844,465	8/25/2006
APD1802-1-KR	P9289KR	10559-282KR1	EVENT VECTOR TABLE OVERRIDE	Charles P. Roth; Jose Fridman; Ravi K. Kolagolla	9/29/2000	3/27/2003	KR	1020037004439	Designated Country Application	5,090,06	8/10/2006
APD1802-1-SG	P9289SG	10559-282SG1	EVENT VECTOR TABLE OVERRIDE	Charles P. Roth; Jose Fridman; Ravi K. Kolagolla	9/29/2000	3/27/2003	SG	200301693-8	Designated Country Application	9,385,2	6/30/2005

APD1802-1-TW	P8289TW	10559-282TW1	EVENT VECTOR TABLE OVERRIDE	Charles P. Roth; Jose Fridman; Ravi K. Kolagolla	9/28/2000	9/28/2001	TW	90124146	Country Equivalent	NI-191887	3/26/2004
APD1802-1-PCT	P8289PCT	10559-282WO1	EVENT VECTOR TABLE OVERRIDE	Charles P. Roth; Jose Fridman; Ravi K. Kolagolla	9/28/2000	9/25/2001	WO	PCT/US01/302 42	Country Equivalent	Expired	
APD1871-1-US	P8280	10559-283001	PROCESSOR STALLING	Charles P. Roth; Gregory A. Overkamp; Ravi P. Singh	12/6/2000	12/6/2000	US	09/731,198	Utility	7,028,165	4/11/2006
APD1871-1-CN	P8290CN	10559-283CN1	PROCESSOR STALLING	Charles P. Roth; Gregory A. Overkamp; Ravi P. Singh	12/6/2000	6/3/2003	CN	01819987.9	Designated Country Application		
APD1871-1-JP	P8280JP	10559-283JP1	PROCESSOR STALLING	Charles P. Roth; Gregory A. Overkamp; Ravi P. Singh	12/6/2000	5/29/2003	JP	P2002-559125	Designated Country Application	3088270	6/17/2005
APD1871-1-KR	P8290KR	10559-283KR1	PROCESSOR STALLING	Charles P. Roth; Gregory A. Overkamp; Ravi P. Singh	12/6/2000	6/3/2003	KR	2003-7007428	Designated Country Application	571323	4/10/2006
APD1871-1-SG	P8280SG	10559-283SG1	METHOD AND APPARATUS TO STALL THE PIPELINE OF A PROCESSOR	Charles P. Roth; Gregory A. Overkamp; Ravi P. Singh	12/6/2000	6/16/2003	SG	2003022809.9	Designated Country Application	96902	9/30/2005
APD1871-1-TW	P8290TW	10559-283TW1	PROCESSOR STALLING	Charles P. Roth; Gregory A. Overkamp; Ravi P. Singh	12/6/2000	12/5/2001	TW	90130098	Country Equivalent	NI-204359	6/21/2004
APD1871-1-PCT	P8280PCT	10559-283WO1	PROCESSOR STALLING	Charles P. Roth; Gregory A. Overkamp; Ravi P. Singh	12/6/2000	12/6/2001	WO	PCT/US01/468 99	Country Equivalent	Expired	
APD1803-1-US	P8290J	10559-284001	VARIABLE WIDTH INSTRUCTION ALIGNMENT ENGINE	Thomas Tomazin; William C. Anderson; Charles P. Roth; Kayla Chalmers; Juan Revilla; Ravi P. Singh	9/28/2000	9/28/2000	US	09/675,817	Utility	7,082,516	7/25/2006
APD1802-2-US	P8291C	10559-284002	VARIABLE WIDTH INSTRUCTION ALIGNMENT ENGINE	Thomas Tomazin; William C. Anderson; Charles P. Roth; Kayla Chalmers; Juan Revilla; Ravi P. Singh	9/28/2000	2/3/2006	US	11/347,697	Continuation		
APD1803-1-CN	P8291CN	10559-284CN1	METHOD AND APPARATUS SUPPORTING VARIABLE LENGTH INSTRUCTION EXECUTION	Thomas Tomazin; William C. Anderson; Charles P. Roth; Kayla Chalmers; Juan Revilla; Ravi P. Singh	9/28/2000	3/28/2003	CN	01819468.4	Designated Country Application	1219252C	9/14/2005
APD1803-1-JP	P8291JP	10559-284JP1	METHOD AND APPARATUS SUPPORTING VARIABLE LENGTH INSTRUCTION EXECUTION	Thomas Tomazin; William C. Anderson; Charles P. Roth; Kayla Chalmers; Juan Revilla; Ravi P. Singh	9/28/2000	3/27/2003	JP	P2002-530984	Designated Country Application	3701653	10/5/2004
APD1803-1-KR	P8291KR	10559-284KR1	METHOD AND APPARATUS SUPPORTING VARIABLE LENGTH INSTRUCTION EXECUTION	Thomas Tomazin; William C. Anderson; Charles P. Roth; Kayla Chalmers; Juan Revilla; Ravi P. Singh	9/28/2000	3/28/2003	KR	10-2803- 7004527	Designated Country Application	545499	1/17/2006

APD1803-1-SG	P9291SG	10559-284SG1	METHOD AND APPARATUS SUPPORTING VARIABLE LENGTH INSTRUCTION EXECUTION	Thomas Tomazin; William C. Anderson; Charles P. Roth; Kayla Chalmers; Juan Revilla; Ravi P. Singh	9/28/2000	3/27/2003	SG	200301893-4	Designated Country Application	96007	8/31/2005
APD1803-1-TW	P9291TW	10559-284TW1	VARIABLE WIDTH INSTRUCTION ALIGNMENT ENGINE	Thomas Tomazin; William C. Anderson; Charles P. Roth; Kayla Chalmers; Juan Revilla; Ravi P. Singh	9/28/2000	9/28/2001	TW	90124154	Country Equivalent	NI-193074	4/15/2004
APD1803-1-PCT	P9291PCT	10559-284WO1	METHOD AND APPARATUS SUPPORTING VARIABLE LENGTH INSTRUCTION EXECUTION	Thomas Tomazin; William C. Anderson; Charles P. Roth; Kayla Chalmers; Juan Revilla; Ravi P. Singh	9/28/2000	9/28/2001	WO	PCT/US01/303 18	Country Equivalent	Expired	
APD1804-1-US	P9292	10559-285001	REGISTER MOVE OPERATIONS	Charles P. Roth; Gregory A. Overkamp; Ravi P. Singh; Ryo Inoue	10/6/2000	10/6/2000	US	09/680,894	Utility	6,728,870	4/27/2004
APD1804-1-CN	P9292CN	10559-285CN1	REGISTER MOVE OPERATIONS	Charles P. Roth; Gregory A. Overkamp; Ravi P. Singh; Ryo Inoue	10/6/2000	4/7/2003	CN	01617011.0	Designated Country Application	12690266	8/9/2006
APD1804-1-JP	P9292JP	10559-285JP1	REGISTER MOVE OPERATIONS	Charles P. Roth; Gregory A. Overkamp; Ravi P. Singh; Ryo Inoue	10/6/2000	3/17/2003	JP	P2002-533060	Designated Country Application	1640646	4/22/2005
APD1804-1-KR	P9292KR	10559-285KR1	REGISTER MOVE OPERATIONS	Charles P. Roth; Gregory A. Overkamp; Ravi P. Singh; Ryo Inoue	10/6/2000	4/2/2003	KR	2003-7004681	Designated Country Application	10-0512655	8/30/2005
APD1804-1-SG	P9292SG	10559-285SG1	REGISTER MOVE OPERATIONS	Charles P. Roth; Gregory A. Overkamp; Ravi P. Singh; Ryo Inoue	10/6/2000	3/10/2003	SG	200301137-6	Designated Country Application	95318	6/30/2005
APD1804-1-TW	P9292TW	10559-285TW1	REGISTER MOVE OPERATIONS	Charles P. Roth; Gregory A. Overkamp; Ravi P. Singh; Ryo Inoue	10/6/2000	10/5/2001	TW	90124689	Country Equivalent	1-238352	8/21/2005
APD1804-1-PCT	P9292PCT	10559-285WO1	REGISTER MOVE OPERATIONS	Charles P. Roth; Gregory A. Overkamp; Ravi P. Singh; Ryo Inoue	10/6/2000	10/5/2001	WO	PCT/US01/314 08	Country Equivalent	Expired	
APD1872-1-US	P9293	10559-286001	EFFICIENT EMULATION DISPATCH BASED ON INSTRUCTION WIDTH	Charles P. Roth; Gregory A. Overkamp; Ravi P. Singh; Tien Dinh	10/31/2000	10/31/2000	US	08/704,467	Utility		
APD1872-1-CN	P9293CN	10559-286CN1	EFFICIENT EMULATION DISPATCH BASED ON INSTRUCTION WIDTH	Charles P. Roth; Gregory A. Overkamp; Ravi P. Singh; Tien Dinh	10/31/2000	3/30/2003	CN	01818338.7	Designated Country Application		
APD1872-1-JP	P9293JP	10559-286JP1	EFFICIENT EMULATION DISPATCH BASED ON INSTRUCTION WIDTH	Charles P. Roth; Gregory A. Overkamp; Ravi P. Singh; Tien Dinh	10/31/2000	4/22/2003	JP	P2002-539859	Designated Country Application	3692115	8/7/2005
APD1872-1-KR	P9293KR	10559-286KR1	EFFICIENT EMULATION DISPATCH BASED ON INSTRUCTION WIDTH	Charles P. Roth; Gregory A. Overkamp; Ravi P. Singh; Tien Dinh	10/31/2000	4/28/2003	KR	2003-7005888	Designated Country Application	601815	7/10/2006

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APD1872-1-SG	P9293SG	10559-286SG1	EFFICIENT EMULATION DISPATCH BASED ON INSTRUCTION WIDTH	Charles P. Roth; Gregory A. Overkamp; Ravi P. Singh; Tien Dinh	10/31/2000	4/21/2003	SG	200302244-9	Designated Country Application	96379	8/31/2005
APD1872-1-TW	P9293TW	10559-286TW1	EFFICIENT EMULATION DISPATCH BASED ON INSTRUCTION WIDTH	Charles P. Roth; Gregory A. Overkamp; Ravi P. Singh; Tien Dinh	10/31/2000	10/30/2001	TW	90128863	Country Equivalent	N/A189150	11/1/2003
APD1872-1-PCT	P9293PCT	10559-286WO1	INSTRUCTION DISPATCH FROM THE INSTRUCTION REGISTER OF AN IN-CIRCUIT-EMULATOR, BASED ON INSTRUCTION WIDTH	Charles P. Roth; Gregory A. Overkamp; Ravi P. Singh; Tien Dinh	10/31/2000	10/31/2001	WO	PCT/US01/46336	Country Equivalent	Expired	
APD1873-1-US	P9294	10559-287US1	EXCEPTION HANDLING IN A PIPELINED PROCESSOR	Charles P. Roth; Gregory A. Overkamp; Ravi P. Singh	12/15/2000	12/15/2000	US	09738781	Utility	6,823,448	11/23/2004
APD1873-1-CN	P9294CN	10559-287CN1	EXCEPTION HANDLING IN A PIPELINED PROCESSOR	Charles P. Roth; Gregory A. Overkamp; Ravi P. Singh	12/15/2000	6/18/2003	CN	01820708.4	Designated Country Application	1269020C	8/9/2006
APD1873-1-JP	P9294JP	10559-287JP1	EXCEPTION HANDLING IN A PIPELINED PROCESSOR	Charles P. Roth; Gregory A. Overkamp; Ravi P. Singh	12/15/2000	6/13/2003	JP	P2002-550517	Designated Country Application	3781439	3/17/2006
APD1873-1-KR	P9294KR	10559-287KR1	EXCEPTION HANDLING IN A PIPELINED PROCESSOR	Charles P. Roth; Gregory A. Overkamp; Ravi P. Singh	12/15/2000	6/12/2003	KR	2003-7007848	Designated Country Application	371322	4/10/2006
APD1873-1-SG	P9294SG	10559-287SG1	EXCEPTION HANDLING IN A PIPELINED PROCESSOR	Charles P. Roth; Gregory A. Overkamp; Ravi P. Singh	12/15/2000	5/26/2003	SG	200302986-5	Designated Country Application	96983	8/31/2006
APD1873-1-TW	P9294TW	10559-287TW1	METHOD, APPARATUS, AND SYSTEM FOR EXCEPTION HANDLING IN A PIPELINED PROCESSOR	Charles P. Roth; Gregory A. Overkamp; Ravi P. Singh	12/15/2000	12/14/2001	TW	90131095	Country Equivalent	1223196	11/1/2004
APD1873-1-PCT	P9294PCT	10559-287WO1	EXCEPTION HANDLING IN A PIPELINED PROCESSOR	Charles P. Roth; Gregory A. Overkamp; Ravi P. Singh	12/15/2000	12/10/2001	WO	PCT/US01/47628	Country Equivalent	Expired	
APD1805-1-US	P9295	10559-288US1	HARDWARE LOOPS	Ravi P. Singh; Charles P. Roth; Gregory A. Overkamp	11/2/2000	11/2/2000	US	08705088	Utility	6,898,693	5/24/2005
APD1805-1-CN	P9295CN	10559-288CN1	HARDWARE LOOPS	Ravi P. Singh; Charles P. Roth; Gregory A. Overkamp	11/2/2000	5/6/2003	CN	01818428.6	Designated Country Application	1275142C	9/13/2006
APD1805-1-JP	P9295JP	10559-288JP1	HARDWARE LOOPS	Ravi P. Singh; Charles P. Roth; Gregory A. Overkamp	11/2/2000	5/1/2003	JP	P2002-539955	Designated Country Application	3787329	3/31/2006
APD1805-1-KR	P9295KR	10559-288KR1	HARDWARE LOOPS	Ravi P. Singh; Charles P. Roth; Gregory A. Overkamp	11/2/2000	4/29/2003	KR	2003-7005947	Designated Country Application	580851	5/10/2006
APD1805-1-SG	P9295SG	10559-288SG1	HARDWARE LOOPS	Ravi P. Singh; Charles P. Roth; Gregory A. Overkamp	11/2/2000	4/30/2003	SG	2003025530-1	Designated Country Application	96790	8/31/2006
APD1805-1-TW	P9295TW	10559-288TW1	HARDWARE LOOPS	Ravi P. Singh; Charles P. Roth; Gregory A. Overkamp	11/2/2000	11/1/2001	TW	90127150	Country Equivalent	1-236623	7/21/2005

APD1803-1-PCT	P8295PCT	10559-288WO1	HARDWARE LOOPS	Ravi P. Singh; Charles P. Roth; Gregory A. Overkamp	11/2/2000	10/30/2001	WO	PCT/US01/453 57	Country Equivalent	Expired	
APD1806-1-US	P8296	10559-289001	HARDWARE LOOPS	Ravi P. Singh; Charles P. Roth; Gregory A. Overkamp	11/2/2000	11/2/2000	US	08/705,217	Utility	6,748,523	6/8/2004
APD1806-1-CN	P8296CN	10559-289CN1	HARDWARE LOOPS	Ravi P. Singh; Charles P. Roth; Gregory A. Overkamp	11/2/2000	4/28/2003	CN	01818089.2	Designated Country Application		
APD1806-1-JP	P8296JP	10559-289JP1	HARDWARE LOOPS	Ravi P. Singh; Charles P. Roth; Gregory A. Overkamp	11/2/2000	5/1/2003	JP	P2002-545362	Designated Country Application	373,7802	11/4/2005
APD1806-1-KR	P8296KR	10559-289KR1	HARDWARE LOOPS	Ravi P. Singh; Charles P. Roth; Gregory A. Overkamp	11/2/2000	4/29/2003	KR	2003-7005948	Designated Country Application	551,544	2/8/2008
APD1806-1-SG	P8298SG	10559-289SG1	METHOD AND APPARATUS FOR PROCESSING PROGRAM LOOPS	Ravi P. Singh; Charles P. Roth; Gregory A. Overkamp	11/2/2000	4/30/2003	SG	200302537-6	Designated Country Application	96469	6/30/2005
APD1806-1-TW	P8296TW	10559-289TW1	HARDWARE LOOPS	Ravi P. Singh; Charles P. Roth; Gregory A. Overkamp	11/2/2000	1/11/2001	TW	90127144	Country Equivalent	NI-200624	5/1/2004
APD1806-1-PCT	P8286PCT	10559-289WO1	HARDWARE LOOPS	Ravi P. Singh; Charles P. Roth; Gregory A. Overkamp	11/2/2000	10/31/2001	WO	PCT/US01/463 37	Country Equivalent	Expired	
APD1807-1-US	P8297	10559-290001	INSTRUCTION ADDRESS GENERATION AND TRACKING IN A PIPELINED PROCESSOR	Ravi P. Singh; Charles P. Roth; Gregory A. Overkamp	9/29/2000	9/29/2000	US	09/576,058	Utility	6,789,184	9/7/2004
APD1807-1-CN	P8297CN	10559-290CN1	INSTRUCTION ADDRESS GENERATION AND TRACKING IN A PIPELINED PROCESSOR	Ravi P. Singh; Charles P. Roth; Gregory A. Overkamp	9/29/2000	3/28/2003	CN	01816470.8	Designated Country Application		
APD1807-1-JP	P8297JP	10559-290JP1	INSTRUCTION ADDRESS GENERATION AND TRACKING IN A PIPELINED PROCESSOR	Ravi P. Singh; Charles P. Roth; Gregory A. Overkamp	9/29/2000	3/27/2003	JP	2002-530888	Designated Country Application		
APD1807-1-KR	P8297KR	10559-290KR1	INSTRUCTION ADDRESS GENERATION AND TRACKING IN A PIPELINED PROCESSOR	Ravi P. Singh; Charles P. Roth; Gregory A. Overkamp	9/29/2000	3/28/2003	KR	102003-7004534	Designated Country Application	623,956	9/7/2006
APD1807-1-SG	P8297SG	10559-290SG1	INSTRUCTION ADDRESS GENERATION AND TRACKING IN A PIPELINED PROCESSOR	Ravi P. Singh; Charles P. Roth; Gregory A. Overkamp	9/29/2000	3/27/2003	SG	200301522-9	Designated Country Application	95721	11/30/2004
APD1807-1-TW	P8297TW	10559-290TW1	INSTRUCTION ADDRESS GENERATION AND TRACKING IN A PIPELINED PROCESSOR	Ravi P. Singh; Charles P. Roth; Gregory A. Overkamp	9/29/2000	9/28/2001	TW	90124155	Country Equivalent	1-242122	10/21/2005

APD1807-1-PCT	P929/PCT	10559-291WD1	INSTRUCTION ADDRESS GENERATION AND TRACKING IN A PIPELINED PROCESSOR	Ravi P. Singh; Charles P. Roth; Gregory A. Overkamp	9/29/2000	9/26/2001	WD	PCT/US01/303 30	Country Equivalent	Expired	
APD1808-1-US	P9288	10559-291001	HARDWARE LOOPS	Ravi P. Singh; Charles P. Roth; Gregory A. Overkamp	11/2/2000	11/2/2000	US	09/705,070	Utility	6,766,444	7/20/2004
APD1808-1-CN	P9288CN	10559-291CN1	HARDWARE LOOPS	Ravi P. Singh; Charles P. Roth; Gregory A. Overkamp	11/2/2000	5/6/2003	CN	01818398.9	Designated Country Application		
APD1808-1-JP	P9288JP	10559-291JP1	HARDWARE LOOPS	Ravi P. Singh; Charles P. Roth; Gregory A. Overkamp	11/2/2000	5/1/2003	JP	P2002-539854	Designated Country Application	3,739,553	11/11/2005
APD1808-1-KR	P9288KR	10559-291KR1	HARDWARE LOOPS	Ravi P. Singh; Charles P. Roth; Gregory A. Overkamp	11/2/2000	4/29/2003	KR	2003-7005950	Designated Country Application	3,360,18	12/6/2005
APD1808-1-SG	P9288SG	10559-291SG1	HARDWARE LOOPS	Ravi P. Singh; Charles P. Roth; Gregory A. Overkamp	11/2/2000	4/30/2003	SG	200302523-6	Designated Country Application	9,0718	9/30/2005
APD1808-1-TW	P9288TW	10559-291TW1	HARDWARE LOOPS	Ravi P. Singh; Charles P. Roth; Gregory A. Overkamp	11/2/2000	11/1/2001	TW	90127157	Country Equivalent	1-227854	2/11/2005
APD1808-1-PCT	P9288PCT	10559-291WD1	HARDWARE LOOPS	Ravi P. Singh; Charles P. Roth; Gregory A. Overkamp	11/2/2000	10/31/2001	WO	PCT/US01/460 63	Country Equivalent	Expired	
APD1809-1-US	P9299	10559-292001	A FIFO WRITE/LIFO READ TRACE BUFFER WITH SOFTWARE AND HARDWARE LOOP COMPRESSION	Ravi P. Singh; Charles P. Roth; Gregory A. Overkamp	9/29/2000	9/29/2000	US	09/575,569	Utility		
APD1809-1-CN	P9299CN	10559-292CN1	Trace Buffer For Loop Compression	Ravi P. Singh; Charles P. Roth; Gregory A. Overkamp	9/29/2000	3/28/2003	CN	01816465.X	Designated Country Application		
APD1809-1-JP	P9299JP	10559-292JP1	Trace Buffer For Loop Compression	Ravi P. Singh; Charles P. Roth; Gregory A. Overkamp	9/29/2000	3/27/2003	JP	2002-530984	Designated Country Application	3,818,965	6/23/2006
APD1809-1-KR	P9299KR	10559-292KR1	Trace Buffer For Loop Compression	Ravi P. Singh; Charles P. Roth; Gregory A. Overkamp	9/29/2000	3/28/2003	KR	102003700452 8	Designated Country Application	5,090,009	8/10/2005
APD1809-1-SG	P9299SG	10559-292SG1	Trace Buffer For Loop Compression	Ravi P. Singh; Charles P. Roth; Gregory A. Overkamp	9/29/2000	3/27/2003	SG	200301527-8	Designated Country Application	9,5726	10/29/2004
APD1809-1-TW	P9299TW	10559-292TW1	A FIFO WRITE/LIFO READ TRACE BUFFER WITH SOFTWARE AND HARDWARE LOOP COMPRESSION	Ravi P. Singh; Charles P. Roth; Gregory A. Overkamp	9/29/2000	9/28/2001	TW	90124153	Country Equivalent	1-349129	2/11/2006
APD1809-1-PCT	P9299PCT	10559-292WO1	Trace Buffer For Loop Compression	Ravi P. Singh; Charles P. Roth; Gregory A. Overkamp	9/29/2000	9/26/2001	WO	PCT/US01/423 67	Country Equivalent	Expired	
APD1810-1-US	P9300	10559-293001	ARRAY SEARCHING OPERATIONS	Ravi P. Singh; Charles P. Roth; Gregory A. Overkamp Charles P. Roth; Ravi K. Singh; Gregory A. Overkamp	9/28/2000	9/28/2000	US	09/675,066	Utility	6,948,056	9/20/2005

APD1810-2-US	P8300-D	10559-293002	ARRAY SEARCHING OPERATIONS	Charles P. Roth; Ravi K. Kolegalla; Jose Fridman	9/28/2000	9/20/2005	US	11/231,397	Divisional		
APD1810-1-CN	P8300-CN	10559-293001	ARRAY PROCESSING OPERATIONS (AS AMENDED)	Charles P. Roth; Ravi K. Kolegalla; Jose Fridman	9/28/2000	3/28/2003	CN	01816469.2	Designated Country Application	1230741C	12/7/2005
APD1810-2-CN	P8300-CN-D	10559-293002	ARRAY PROCESSING OPERATIONS (AS AMENDED)	Charles P. Roth; Ravi K. Kolegalla; Jose Fridman	9/28/2000	11/7/2005	CN	200510125001 X	Designated Country Application		
APD1810-1-JP	P8300-JP	10559-293001	ARRAY PROCESSING OPERATIONS (AS AMENDED)	Charles P. Roth; Ravi K. Kolegalla; Jose Fridman	9/28/2000	3/27/2003	JP	2002-530886	Designated Country Application		
APD1810-1-KR	P8300-KR	10559-293001	ARRAY PROCESSING OPERATIONS	Charles P. Roth; Ravi K. Kolegalla; Jose Fridman	9/28/2000	3/28/2003	KR	20037004631	Designated Country Application	571325	4/10/2008
APD1810-1-SG	P8300-SG	10559-293001	ARRAY PROCESSING OPERATIONS	Charles P. Roth; Ravi K. Kolegalla; Jose Fridman	9/28/2000	3/27/2003	SG	200301690-4	Designated Country Application	96313	8/31/2005
APD1810-1-TW	P8300-TW	10559-293001	ARRAY SEARCHING OPERATIONS	Charles P. Roth; Ravi K. Kolegalla; Jose Fridman	9/28/2000	9/28/2001	TW	90124147	Country Equivalent	NI-179954	10/18/2003
APD1810-1-PCT	P8300-PCT	10559-293001	ARRAY PROCESSING OPERATIONS (AS AMENDED)	Charles P. Roth; Ravi K. Kolegalla; Jose Fridman	9/28/2000	9/28/2001	WO	PCT/US01/303 08	Country Equivalent	Expired	
APD2005-1-US	P8842	10559-330001	METHOD AND APPARATUS FOR RESTORING REGISTERS AFTER CANCELLING A MULTI-CYCLE INSTRUCTION	Gregory A. Overkamp; Ryo Inoue	3/28/2001	3/28/2001	US	09/620,670	Utility	6,889,316	5/3/2005
APD2005-1-TW	P8842-TW	10559-330001	METHOD AND APPARATUS FOR RESTORING REGISTERS AFTER CANCELLING A MULTI-CYCLE INSTRUCTION	Gregory A. Overkamp; Ryo Inoue	3/28/2001	3/25/2002	TW	091105737	Country Equivalent	223014	10/11/2004
APD1812-1-US	P10256	10559-391001	RESOURCE EFFICIENT HARDWARE LOOPS	Ryo Inoue; Charles P. Roth; Gregory A. Overkamp; Ravi P. Singh	12/20/2000	12/20/2000	US	09/745,104	Utility	7,065,636	6/20/2006
APD1812-1-CN	P10256-CN	10559-391001	RESOURCE EFFICIENT HARDWARE LOOPS	Ryo Inoue; Charles P. Roth; Gregory A. Overkamp; Ravi P. Singh	12/20/2000	6/13/2003	CN	01820774.X	Designated Country Application	123740C	5/24/2006
APD1812-1-JP	P10256-JP	10559-391001	RESOURCE EFFICIENT HARDWARE LOOPS	Ryo Inoue; Charles P. Roth; Gregory A. Overkamp; Ravi P. Singh	12/20/2000	6/19/2003	JP	P2002-551696	Designated Country Application	3738233	11/4/2005
APD1812-1-KR	P10256-KR	10559-391001	RESOURCE EFFICIENT HARDWARE LOOPS	Ryo Inoue; Charles P. Roth; Gregory A. Overkamp; Ravi P. Singh	12/20/2000	6/20/2003	KR	102003700845	Designated Country Application	571326	4/10/2008

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APD1812-1-SG	P10256SG	10559-391SG1	RESOURCE EFFICIENT HARDWARE LOOPS	Ryo Inoue; Charles P. Roth; Gregory A. Overkamp; Ravi P. Singh	12/20/2000	5/22/2003	SG	200301981-6	Designated Country Application	97343	9/30/2005
APD1812-1-TW	P10256TW	10559-391TW1	RESOURCE EFFICIENT HARDWARE LOOPS	Ryo Inoue; Charles P. Roth; Gregory A. Overkamp; Ravi P. Singh	12/20/2000	12/20/2001	TW	90131673	Country Equivalent	1-233330	7/1/2005
APD1812-1-PCT	P10256PC	10559-391WO1	RESOURCE EFFICIENT HARDWARE LOOPS	Ryo Inoue; Charles P. Roth; Gregory A. Overkamp; Ravi P. Singh	12/20/2000	12/20/2001	WO	PCT/US01/505 06	Country Equivalent	Expired	
APD18174-1-US	P10257	10559-392001	MODULO ADDRESSING	Ryo Inoue; Raghavan Suchakar; Ravi K. Kolagolla	12/29/2000	12/29/2000	US	09/751,507	Utility	6,760,830	7/6/2004
APD18174-1-CN	P10257CN	10559-392CN1	MODULO ADDRESSING	Ryo Inoue; Raghavan Suchakar; Ravi K. Kolagolla	12/29/2000	6/27/2003	CN	01821422.3	Designated Country Application	1269028C	8/9/2008
APD18174-1-JP	P10257JP	10559-392JP1	MODULO ADDRESSING	Ryo Inoue; Raghavan Suchakar; Ravi K. Kolagolla	12/29/2000	6/29/2003	JP	P2002-554857	Designated Country Application		
APD18174-1-KR	P10257KR	10559-392KR1	MODULO ADDRESSING	Ryo Inoue; Raghavan Suchakar; Ravi K. Kolagolla	12/29/2000	6/16/2003	KR	2003-7008023	Designated Country Application	571327	4/10/2006
APD18174-1-SG	P10257SG	10559-392SG1	MODULO ADDRESSING	Ryo Inoue; Raghavan Suchakar; Ravi K. Kolagolla	12/29/2000	5/22/2003	SG	200303076-4	Designated Country Application	97016	8/31/2005
APD18174-1-TW	P10257TW	10559-392TW1	MODULO ADDRESSING	Ryo Inoue; Raghavan Suchakar; Ravi K. Kolagolla	12/29/2000	12/29/2001	TW	90132773	Country Equivalent	NI-182678	8/1/2003
APD18174-1-PCT	P10257PC	10559-392WO1	MODULO ADDRESSING	Ryo Inoue; Raghavan Suchakar; Ravi K. Kolagolla	12/29/2000	12/29/2001	WO	PCT/US01/507 65	Country Equivalent	Expired	
APD18176-1-US	P10268	10559-393001	PEAK POWER REDUCTION WHEN UPDATING FUTURE FILE	Ryo Inoue; Juan G. Revilla	3/29/2001	3/29/2001	US	09/823,276	Utility	7,224,285	10/17/2006
APD18176-1-TW	P10268TW	10559-393TW1	PEAK POWER REDUCTION WHEN UPDATING FUTURE FILE	Ryo Inoue; Juan G. Revilla	3/29/2001	3/25/2002	TW	091105736	Country Equivalent	1-244029	11/21/2005
APD18176-1-US	P10269	10559-394001	USE OF A FUTURE FILE FOR DATA ADDRESS CALCULATIONS IN A PIPELINED PROCESSOR	William C. Anderson; Ryo Inoue	3/28/2001	3/28/2001	US	09/820,514	Utility	6,898,695	5/24/2005
APD18176-1-TW	P10269TW	10559-394TW1	USE OF A FUTURE FILE FOR DATA ADDRESS CALCULATIONS IN A PIPELINED PROCESSOR	William C. Anderson; Ryo Inoue	3/28/2001	3/28/2002	TW	091105730	Country Equivalent	NI-202374	6/1/2004
APD18111-1-US	P10260	10559-395001	EARLY EXCEPTION DETECTION	Juan G. Revilla; Charles P. Roth; Ravi P. Singh	3/29/2001	3/29/2001	US	09/823,095	Utility	6,920,515	7/19/2005
APD18111-2-US	P10260C	10559-395002	EARLY EXCEPTION DETECTION	Juan G. Revilla; Charles P. Roth; Ravi P. Singh	3/29/2001	5/23/2003	US	11/135,862	Continuation		
APD18111-1-TW	P10260TW	10559-395TW1	EARLY EXCEPTION DETECTION	Juan G. Revilla; Charles P. Roth; Ravi P. Singh	3/29/2001	3/25/2002	TW	081105735	Country Equivalent	221961	10/17/2004

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APD1877-1-US	P10261	10559-396001	SECURITY ON HARDWARE LOOPS	Ravi P. Singh; Charles P. Roth; William C. Anderson; Thomas Tomazin	12/28/2000	12/28/2000	US	09/753,081	Utility	6,763,453	7/13/2004
APD1877-1-CN	P10261CN	10559-396CN1	SECURITY ON HARDWARE LOOPS	Ravi P. Singh; Charles P. Roth; William C. Anderson; Thomas Tomazin	12/28/2000	6/20/2003	CN	01821238.0	Designated Country Application	3720807	11/30/2005
APD1877-1-JP	P10261JP	10559-396JP1	SECURITY ON HARDWARE LOOPS	Ravi P. Singh; Charles P. Roth; William C. Anderson; Thomas Tomazin	12/28/2000	6/25/2003	JP	P2002-554856	Designated Country Application	3720807	9/16/2005
APD1877-1-KR	P10261KR	10559-396KR1	SECURITY ON HARDWARE LOOPS	Ravi P. Singh; Charles P. Roth; William C. Anderson; Thomas Tomazin	12/28/2000	6/27/2003	KR	2003-7008769	Designated Country Application	536019	12/6/2005
APD1877-1-SG	P10261SG	10559-396SG1	SECURITY OF HARDWARE LOOPS	Ravi P. Singh; Charles P. Roth; William C. Anderson; Thomas Tomazin	12/28/2000	5/27/2003	SG	200303116-8	Designated Country Application	97028	9/30/2005
APD1877-1-TW	P10261TW	10559-396TW1	SECURITY ON HARDWARE LOOPS	Ravi P. Singh; Charles P. Roth; William C. Anderson; Thomas Tomazin	12/28/2000	12/27/2001	TW	90132526	Country Equivalent	1-212401	5/11/2005
APD1877-1-PCT	P10261PCT	10559-396W01	SECURITY ON HARDWARE LOOPS	Ravi P. Singh; Charles P. Roth; William C. Anderson; Thomas Tomazin	12/28/2000	12/20/2001	WO	PCT/US01/50508	Country Equivalent	Expired	
APD1878-1-US	P10502	10559-432001	SPECULATIVE REGISTER ADJUSTMENT	Charles P. Roth; Ravi P. Singh; Gregory A. Overkamp	12/20/2000	12/20/2000	US	08742,745	Utility	6,920,547	7/19/2005
APD1878-1-CN	P10502CN	10559-432CN1	SPECULATIVE REGISTER ADJUSTMENT	Charles P. Roth; Ravi P. Singh; Gregory A. Overkamp	12/20/2000	6/16/2003	CN	01821032.5	Designated Country Application	1244049C	3/1/2006
APD1878-1-JP	P10502JP	10559-432JP1	SPECULATIVE REGISTER ADJUSTMENT	Charles P. Roth; Ravi P. Singh; Gregory A. Overkamp	12/20/2000	6/16/2003	JP	P2002-551696	Designated Country Application	3759729	1/13/2006
APD1878-1-KR	P10502KR	10559-432KR1	SPECULATIVE REGISTER ADJUSTMENT	Charles P. Roth; Ravi P. Singh; Gregory A. Overkamp	12/20/2000	6/16/2003	KR	2003-7008024	Designated Country Application	576560	4/27/2006
APD1878-1-SG	P10502SG	10559-432SG1	SPECULATIVE REGISTER ADJUSTMENT	Charles P. Roth; Ravi P. Singh; Gregory A. Overkamp	12/20/2000	5/22/2003	SG	200302976-6	Designated Country Application	96979	7/29/2005
APD1878-1-TW	P10502TW	10559-432TW1	SPECULATIVE REGISTER ADJUSTMENT	Charles P. Roth; Ravi P. Singh; Gregory A. Overkamp	12/20/2000	11/1/2001	TW	90127145	Country Equivalent		
APD1878-1-PCT	P10502PCT	10559-432W01	SPECULATIVE REGISTER ADJUSTMENT	Charles P. Roth; Ravi P. Singh; Gregory A. Overkamp	12/20/2000	10/31/2001	WO	PCT/US01/46334	Country Equivalent	Expired	
APD2303-1-US	P14126	10559-789001	METHOD AND APPARATUS FOR TESTING EMBEDDED CORES	Luis A. Basto; Sankaran M. Menon; Tien Dinh; Thomas Tomazin; Juan G. Revilla	12/31/2002	12/31/2002	US	10/335,149	Utility		
APD2303-1-TW	P14126TW	10559-789TW1	METHOD AND APPARATUS FOR TESTING EMBEDDED CORES	Luis A. Basto; Sankaran M. Menon; Tien Dinh; Thomas Tomazin; Juan G. Revilla	12/31/2002	12/25/2003	TW	92156868	Country Equivalent	1-239359	8/1/2006

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APD2303-1-PCT	P14126PCT	10559-789WO1	METHOD AND APPARATUS FOR TESTING EMBEDDED CORES.	Luis A. Basio; Sankaran M. Menon; Tien Dinh; Thomas Tomazini; Juan G. Revilla	12/31/2002	12/18/2003	WO	PCT/US03/408 48	Country Equivalent	Expired
APD2665-1-US	P14223	10559-789WO1	SELF-NESTING INTERRUPTS	Ravi P. Singh; Thomas Tomazini; Charles P. Roth; Jose Fridman; Michael Allen	9/6/2002	9/6/2002	US	10236,856	Utility	7,043,583 5/9/2006
APD2665-1-CN	P14223CN	10559-789CN1	SELF-NESTING INTERRUPTS	Ravi P. Singh; Thomas Tomazini; Charles P. Roth; Jose Fridman; Michael Allen	9/6/2002	9/4/2003	CN	03159326.7	Country Equivalent	
APD2665-1-TW	P14223TW	10559-789TW1	SELF-NESTING INTERRUPTS	Ravi P. Singh; Thomas Tomazini; Charles P. Roth; Jose Fridman; Michael Allen	9/6/2002	9/5/2003	TW	092124636	Country Equivalent	
APD2665-1-PCT	P14223PCT	10559-789WO1	SELF-NESTING INTERRUPTS	Ravi P. Singh; Thomas Tomazini; Charles P. Roth; Jose Fridman; Michael Allen	9/6/2002	9/5/2003	WO	PCT/US03/277 43	Country Equivalent	Expired

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