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Form PTO-1595	02-28-2007		Attorney Docket No.: SUZU1510 3HEET
To the Honorable Commissioner for F thereof.	103376259		e attached original documents or copy
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Toshiba America Electronic Components, Inc. Additional name(s) of conveying party(ies) Attached? ☐ Yes ☒ No		Kabushiki Kaisi 1-1 Shibaura 1-4 Minato-ku, Toky Japan Additional name(s) □ Yes ⊠ N	chome /o) & address(es) attached?
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1			

PATENT

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ATTORNEY DOCKET NO.: SUZU1510 TOSHIBA DOCKET NO.: STI-1421

ASSIGNMENT

We, Toshiba America Electronic Components, Inc., a corporation organized under the laws of California, located at 19900 MacArthur Blvd., Suite 400, Irvine, California 92612, U.S.A., the assignee of the following U.S. Patent Application:

SERIAL NO. DATE FILED **INVENTOR** TITLE OF INVENTION 11/137,296 05/25/2005 Satoru System and Method for Configuring Takase Conductors within an Integrated Circuit to Reduce Impedance Variation Caused by Connection **Bumps**

hereby assign the entirety of our right, title and interest in said U.S. Patent Application, including all divisions, reissues, continuations, substitutions and extensions thereof and all rights arising under or pursuant to any and all international agreements, treaties or laws relating to the protection of industrial property, including rights of priority, resulting from the filing of any said application to KABUSHIKI KAISHA TOSHIBA, a Japanese corporation, located at 1-1 Shibaura 1-chome, Minato-ku, Tokyo, Japan, their successors, assigns, or other legal representatives.

The undersigned is authorized to sign this document on behalf of the corporation.

APPROVED TAEC LEGAL DEP

TOSHIBA AMERICA ELECTRONIC COMPONENTS, INC.

RECORDED: 02/27/2007

Name: Hideya Sakaida

Title: President

Date:

PATENT REEL: 018973 FRAME: 0710

2/14/2007