

PATENT ASSIGNMENT

Electronic Version v1.1

Stylesheet Version v1.1

SUBMISSION TYPE:

NEW ASSIGNMENT

NATURE OF CONVEYANCE:

ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
Advanced Micro Devices, Inc.	01/31/2007

RECEIVING PARTY DATA

Name:	Spansion Inc.
Street Address:	915 DeGuigne Drive, P.O. Box 3453
Internal Address:	Mail Stop 250
City:	Sunnyvale
State/Country:	CALIFORNIA
Postal Code:	94088-3453

PROPERTY NUMBERS Total: 35

Property Type	Number
Patent Number:	6221768
Patent Number:	6063666
Patent Number:	5981339
Patent Number:	6140246
Patent Number:	5994239
Patent Number:	6114230
Patent Number:	6281078
Patent Number:	6157572
Patent Number:	5875130
Patent Number:	6052310
Patent Number:	6205059
Patent Number:	6011721
Patent Number:	5901090
Patent Number:	6275415

PATENT

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REEL: 019028 FRAME: 0644

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Patent Number:	6172914
Patent Number:	6122198
Patent Number:	6046932
Patent Number:	6103602
Patent Number:	5949718
Patent Number:	6329257
Patent Number:	6188101
Patent Number:	5888867
Patent Number:	5940709
Patent Number:	6127222
Patent Number:	6387755
Patent Number:	6015736
Patent Number:	6153487
Patent Number:	6025240
Patent Number:	6410956
Patent Number:	6137153
Patent Number:	6262469
Patent Number:	6005804
Patent Number:	6081455
Patent Number:	5991202
Patent Number:	6177322

CORRESPONDENCE DATA

Fax Number: (408)616-3762
Correspondence will be sent via US Mail when the fax attempt is unsuccessful.
Phone: 408-616-3722
Email: sally.nieto@spansion.com
Correspondent Name: Raymond E. Fritz
Address Line 1: 915 DeGuigne Drive, P.O. Box 3453
Address Line 2: Mail Stop 250
Address Line 4: Sunnyvale, CALIFORNIA 94088-3453

NAME OF SUBMITTER:	Raymond E. Fritz
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Total Attachments: 4
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ASSIGNMENT AGREEMENT

This Assignment Agreement by and between **ADVANCED MICRO DEVICES, INC.**, a corporation of Delaware having a place of business at One AMD Place, P. O. Box 3453, Sunnyvale, California 94088-3453, U.S.A., its affiliates and subsidiaries, (hereinafter referred to as "ASSIGNOR") and **SPANSION, INC.**, a corporation of Delaware, having a place of business at 915 DeGuigne Drive, P. O. Box 3453, Sunnyvale, California, 94088-3453, U.S.A. (hereinafter "ASSIGNEE") is effective on the 21st day of December, 2005.

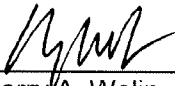
In consideration of good and valuable consideration, the receipt of which is hereby acknowledged, and pursuant to paragraph 2.2 (Patent Rights) of the Amended and Restated Intellectual Property Contribution and Ancillary Matters Agreement executed between the ASSIGNOR and the ASSIGNEE on December 21, 2005, ASSIGNOR hereby assigns, transfers and conveys all of their right, title and interest in the United States and throughout the world in and to all invention disclosures, United States patent applications, United States patents, foreign patents, and foreign patent applications (including any Patent Cooperation Treaty (PCT) applications) identified on Schedule A as initialed on page 1 thereof by the undersigned and attached hereto and made a part hereof, and the entire right, title and interest in the United States and throughout the world in and to the inventions to which said invention disclosures, patents and patent applications pertain, in and to all worldwide rights of priority, and in and to any corresponding United States and foreign patents, extensions and renewals of patents, results of reexamination, substitutions, reissues, patent applications, divisionals, continuations, continuations-in-part, utility models, petty patents, patents of importation, invention registrations, and inventor's certificates, together with all the rights and privileges granted and secured by said patents, extensions and renewals of patents, results of reexamination, substitutions, reissues, patent applications, divisionals, continuations, continuations-in-part, utility models, petty patents, patents of importation, invention registrations and inventor's certificates, including without limitation all claims, demands, rights and causes of action that ASSIGNOR may have against others on account of any past, present and future infringement of any of said patents, patent applications, utility models, petty patents, patents of importation, invention registrations, and inventor's certificates with the right in ASSIGNEE to sue for and obtain all relief to which ASSIGNOR may have been entitled by reason of any such infringement. ASSIGNOR further agrees that ASSIGNEE may apply for and receive Letters Patent, utility models, petty patents, patents of importation, invention registrations, and inventor's certificates for said inventions in its own name.

Said entire right, title, and interest is to be held and enjoyed by ASSIGNEE for its own use and benefit and for the use and benefit of its successors and assigns, to the full end of the term for which said patents, registrations or certificates may be granted, as fully and entirely as the same would have been held and enjoyed by ASSIGNOR had this assignment and sale not been made.

ASSIGNOR's assignment, transference and conveyance of all of its right, title and interest in the United States and throughout the world hereunder is made subject to any rights, licenses or immunities relating to such rights, titles and interests that have been granted to any Persons (as that term is defined in the Amended and Restated Intellectual Property Contribution and Ancillary Matters Agreement (hereinafter "IPCAAMA") executed by one or more of the parties hereto (i.e., either or both of the ASSIGNOR or the ASSIGNEE) on December 21st, 2005) prior to or on December 21st, 2005.

IN WITNESS WHEREOF, the ASSIGNOR and ASSIGNEE have caused this Assignment Agreement to be executed by their respective duly authorized representatives as of December 21, 2005.

ASSIGNOR
ADVANCED MICRO DEVICES, INC.

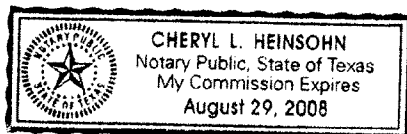
By: 
Name: Harry A. Wolin
Title: Senior Vice President and Assistant General Counsel

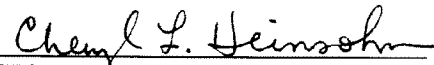
Date: 12-8-06

State of **TEXAS**)
County of **TRAVIS**)

On December 8, 2006, before me, Cheryl L. Heinsohn, a Notary Public, personally appeared Harry A. Wolin, personally known to me or proved to me on the basis of satisfactory evidence to be the person whose name is subscribed to the within instrument and acknowledged to me that he/she executed the same in his/her authorized capacity, and that by his/her signature on the instrument the person, or the entity upon behalf of which the person acted, executed the instrument.

Witness my hand and official seal.




SIGNATURE OF NOTARY

ASSIGNEE
SPANSION, INC.

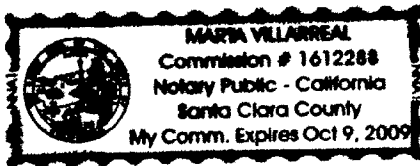
By: [Signature]
Name: Robert C. Melendres
Title: Executive Vice President, Corporate Development,
General Counsel and Corporate Secretary

Date: January 31, 2007

State of CALIFORNIA)
County of SANTA CLARA)

On January 31, 2007, before me, Marta Villarreal, a Notary Public, personally appeared Robert C. Melendres, personally known to me or proved to me on the basis of satisfactory evidence to be the person whose name is subscribed to the within instrument and acknowledged to me that he/she executed the same in his/her authorized capacity, and that by his/her signature on the instrument the person, or the entity upon behalf of which the person acted, executed the instrument.

Witness my hand and official seal.



[Signature]
SIGNATURE OF NOTARY

Docket No.	Country	Status	Application	File Date	Grant Date	Owner	Patent No.	Title
C704497	USA	Granted	09/257,733	2/25/1999	4/24/2001	A2S	6,221,768	USE OF IMPLANTED IONS TO REDUCE OXIDE-
C705497	USA	Granted	09/098,292	6/12/1998	5/16/2000	A2S	6,063,666	RTCDV OXIDE AND N2O ANNEAL FOR TOP OXIDE OF
C706497	USA	Granted	09/045,013	3/20/1998	11/9/1999	A2S	5,981,339	NARROWER ERASE DISTRIBUTION FOR FLASH
C707497	USA	Granted	08/993,444	11/31/1999	10/31/2000	A2S	6,140,246	IN SITU P DOPED AMORPHOUS SILICON BY NH3 TO
C708497	USA	Granted	08/993,343	12/18/1997	11/30/1999	A2S	5,994,239	MANUFACTURING PROCESS TO ELIMINATE
C709497	USA	Granted	08/993,443	12/18/1997	9/5/2000	A2S	6,114,230	NITROGEN ION IMPLANTED AMORPHOUS SILICON
C710497	USA	Granted	08/993,344	5/8/2000	8/28/2001	A2S	6,281,078	MANUFACTURING PROCESS TO ELIMINATE ONO
C712497	USA	Granted	09/085,680	5/27/1998	12/5/2000	A2S	6,157,572	METHOD FOR ERASING FLASH ELECTRICALLY
C713497	USA	Granted	09/085,705	5/27/1998	2/23/1999	A2S	5,875,130	METHOD FOR PROGRAMMING FLASH ELECTRICALLY
C714497	USA	Granted	09/132,981	8/12/1998	4/18/2000	A2S	6,052,310	METHOD FOR TIGHTENING ERASE THRESHOLD
C718497	USA	Granted	09/166,384	10/5/1998	3/20/2001	A2S	6,205,059	METHOD FOR ERASING FLASH ELECTRICALLY
C719497	USA	Granted	09/132,347	8/12/1998	1/4/2000	A2S	6,011,721	METHOD FOR SENSING STATE OF ERASURE OF
C720497	USA	Granted	09/085,552	5/27/1998	5/4/1999	A2S	5,901,090	METHOD FOR ERASING FLASH ELECTRICALLY
C721497	USA	Granted	09/416,563	10/11/1999	8/14/2001	A2S	6,275,415	MULTIPLE BYTE CHANNEL HOT ELECTRON
C722497	USA	Granted	09/404,078	9/23/1999	1/9/2001	A2S	6,172,914	CONCURRENT ERASE VERIFY SCHEME FOR FLASH
C723497	USA	Granted	09/413,182	10/5/1999	9/19/2000	A2S	6,122,198	BIT BY BIT APDE VERIFY FOR FLASH MEMORY
C725497	USA	Granted	09/417,273	10/13/1999	4/4/2000	A2S	6,046,932	CIRCUIT IMPLEMENTATION TO QUENCH BIT LINE
C730497	USA	Granted	08/992,618	12/17/1997	8/15/2000	A2S	6,103,602	METHOD AND SYSTEM FOR PROVIDING A DRAIN
C731497	USA	Granted	08/992,622	12/17/1997	9/7/1999	A2S	5,949,718	METHOD AND SYSTEM FOR SELECTED SOURCE
C732497	USA	Granted	08/994,140	6/1/2000	12/11/2001	A2S	6,329,257	METHOD FOR LATERALLY PEAKED SOURCE DURING
C734497	USA	Granted	09/006,757	1/14/1998	2/13/2001	A2S	6,188,101	FLASH EPROM CELL WITH REDUCED SHORT
C735497	USA	Granted	09/023,241	2/13/1998	3/30/1999	A2S	5,888,867	NON-UNIFORM THRESHOLD VOLTAGE ADJUSTMENT
C737497	USA	Granted	08/993,599	12/18/1997	8/17/1999	A2S	5,940,709	METHOD AND SYSTEM FOR SOURCE-ONLY
C738497	USA	Granted	08/991,687	12/16/1997	10/3/2000	A2S	6,127,222	NON-SELF-ALIGNED SIDE CHANNEL IMPLANTS FOR
C740497	USA	Granted	08/992,616	11/9/2000	5/14/2002	A2S	6,387,755	METHOD AND SYSTEM FOR PROVIDING LOCALIZED
C742497	USA	Granted	08/993,787	12/19/1997	1/18/2000	A2S	6,015,736	METHOD AND SYSTEM FOR GATE STACK
C743497	USA	Granted	09/040,107	3/17/1998	11/28/2000	A2S	6,153,487	NEW APPROACH FOR THE FORMATION OF
C744497	USA	Granted	08/993,600	12/18/1997	2/15/2000	A2S	6,025,240	METHOD AND SYSTEM FOR USING A SPACER TO
C744497	USA	Granted	09/478,864	1/7/2000	6/25/2002	A2S	6,410,956	METHOD AND SYSTEM FOR USING A SPACER TO
C748497	USA	Granted	09/023,497	2/13/1998	10/24/2000	A2S	6,137,153	FLOATING GATE CAPACITOR FOR USE IN VOLTAGE
C749497	USA	Granted	09/047,237	3/25/1998	7/17/2001	A2S	6,262,469	CAPACITOR FOR USE IN A CAPACITOR DIVIDER
C752497	USA	Granted	08/993,634	12/18/1997	12/21/1999	A2S	6,005,804	SPLIT VOLTAGE FOR NAND FLASH
C753497	USA	Granted	09/232,023	1/14/1999	6/27/2000	A2S	6,081,455	EEPROM DECODER BLOCK HAVING A P-WELL
C756497	USA	Granted	09/161,423	9/24/1998	11/23/1999	A2S	5,991,202	METHOD FOR REDUCING PROGRAM DISTURB DURING
C758497	USA	Granted	09/177,817	3/17/2000	1/23/2001	A2S	6,177,322	HIGH VOLTAGE TRANSISTOR WITH HIGH GATED