

PATENT ASSIGNMENT

Electronic Version v1.1
 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
-------------------------	----------------

NATURE OF CONVEYANCE:	ASSIGNMENT
------------------------------	------------

CONVEYING PARTY DATA	
Name	Execution Date
Candescent Intellectual Property Services, Inc.	08/01/2006

RECEIVING PARTY DATA	
Name:	Canon Kabushiki Kaisha
Street Address:	30-2, Shimomaruko 3-chome, Ohta-ku
City:	Tokyo
State/Country:	JAPAN
Postal Code:	146-8501

PROPERTY NUMBERS Total: 34

Property Type	Number
Patent Number:	6489718
Patent Number:	5543683
Patent Number:	6500885
Patent Number:	6022652
Patent Number:	6278066
Patent Number:	6571464
Patent Number:	6288483
Patent Number:	6307382
Patent Number:	6323653
Patent Number:	6406346
Patent Number:	6741027
Patent Number:	6853129
Application Number:	11326266
Patent Number:	6462484
Patent Number:	6512335

CH \$1360.00 6489718

Patent Number:	6624592
Patent Number:	6853135
Patent Number:	7071907
Patent Number:	6731061
Patent Number:	6555025
Patent Number:	6630786
Patent Number:	6727048
Patent Number:	6879097
Patent Number:	6771027
Patent Number:	6632117
Patent Number:	6822628
Application Number:	10969494
Patent Number:	6873097
Patent Number:	6620013
Application Number:	10001084
Application Number:	09998021
Patent Number:	6742257
Patent Number:	6834431
Application Number:	11023995

CORRESPONDENCE DATA

Fax Number: (323)934-0202
Correspondence will be sent via US Mail when the fax attempt is unsuccessful.
Phone: 3239342300
Email: dperez@la.ladas.com
Correspondent Name: Ladas & Parry LLP - Robert Popa
Address Line 1: 5670 Wilshire Blvd., Suite 2100
Address Line 4: Los Angeles, CALIFORNIA 90036

ATTORNEY DOCKET NUMBER:	CIPS TO CKK 1
-------------------------	---------------

NAME OF SUBMITTER:	Robert Popa
--------------------	-------------

Total Attachments: 3
source=CIPS to CKK 1#page1.tif
source=CIPS to CKK 1#page2.tif
source=CIPS to CKK 1#page3.tif

US ASSIGNMENT

This Assignment is effective as of August 1, 2006.

WHEREAS, **Candescent Intellectual Property Services, Inc.**, a corporation organized under the laws of the State of Delaware, United States of America, located at One Canon Plaza, Lake Success, NY, 11042, United States of America, (hereinafter called the ASSIGNOR) was the proprietor of the following Patents[s], and Patent Applications[s] in the United States,

[Set forth on the attached schedule]

AND WHEREAS, **Canon Kabushiki Kaisha**, a corporation organized and existing under the laws of Japan, located at 30-2, Shimomaruko 3-chome, Ohta-ku, Tokyo 146-8501, Japan, (hereinafter called the ASSIGNEE) was desirous of acquiring the entire right, title and interest of the ASSIGNOR in and to the aforesaid Patents[s], and Patent Applications[s],

NOW, THEREFORE, in consideration of at least U.S. \$1.00 and other good and valuable consideration, the ASSIGNOR assigned to the ASSIGNEE, its successors and assigns, all of its right, title and interest in and to the aforesaid Patent[s] and Patent Application[s] including but not limited to all emoluments, advantages, profits and benefits accruing or belonging thereto, and in and to all Letters Patent to be obtained for the aforesaid applications and any continuations, continuations-in-part, divisions, renewals, substitutes therefor, continuing prosecution applications, international applications, certificates of invention, utility models and other governmental grants and in and to the aforesaid Letters Patent and any reissues, re-examinations, renewals and extensions thereof,

The ASSIGNOR consents to the ASSIGNEE'S proceeding with the aforesaid Patents and Patent Application[s], if any, in its own name in substitution of the ASSIGNOR and the said ASSIGNOR agrees to do all that may be required to put the foregoing into effect.

IN WITNESS WHEREOF, the said ASSIGNOR has executed this document on the 20th
day of December, 2006.

Candescent Intellectual Property Services, Inc.

By: *Seymour Lieberman*
Printed Name: SEYMOUR LIEBERMAN
Title: Vice President

IN WITNESS WHEREOF, the said ASSIGNEE has executed this document on the 12th
day of January, ~~2006~~ 2007

Canon Kabushiki Kaisha

By: *Nobuyoshi Tanaka*
Printed Name: Nobuyoshi Tanaka, Ph.D.
Title: Senior Managing Director, Group Executive
Corporate Intellectual Property
and Legal Headquarters

Count	CTC Docket No.	Original Atty Docket No.	Serial Number	Title	File Date	Patent No.
1	S003-14D	M-2325-14D US	09/617,591	"Spacer Structures for Use in Flat Panel Displays and Methods for Forming Same"	7/18/00	6489718
1	F016	CDST-F016	08/343,803	"Faceplate for FED including wall gripper structures"	11/21/94	5543683
1	C024-1P	M-3628-1P US	09/430,799	"Polycarbonate-containing liquid chemical formulation and method for making polycarbonate film"	10/29/99	6500885
1	F037	CDST-F037	08/607,278	"High resolution flat panel phosphor screen with tall barriers"	2/23/96	6022652
1	S039	M-4462 US	08/771,453	"Self standing spacer wall structures and methods of fabricating and installing same"	12/20/96	6278066
1	S039-1D	M-4462-1D US	09/844,738	"Method of Fabricating Self-Standing Spacers"	4/26/01	6571464
1	F040-1D	M-4199-1D US	09/240,351	"Use of sacrificial masking layer and backside exposure in forming openings that typically receive light-emissive material, and associated light-emitting structure"	1/29/99	6288483
1	M045-1D	M-4461-1D US	09/539,727	"Device and Method for Magnetically Sensing Current in Plate Structure"	3/30/00	6307382
1	M071-1D	CT-M071-1D US	09/539,192	Magnetic Detection of Short Circuit Defects in Plate Structure	3/30/00	6323653
1	S099-1D	CDST-S099-1D	09/566,697	"Fabrication of Flat-Panel Display Having Spacer With Laterally Segmented Face Electrode"	5/8/00	6406346
1	F102-3P	CDST-F102.3P	09/895,699	"Protected Substrate Structure for a Field Emission Display Device"	6/29/01	6741027
1	F102-4C	CDST-F102-3P.CON	10/412,196	"Protected Substrate Structure for a Field Emission Display Device"	4/11/03	6853129
1	F102-5D	623114-6	11/326,266	"Protected Substrate Structure for a Field Emission Display Device"	12/30/05	
1	E114-3P	CDST-E114-3P	09/796,868	"Procedures and Apparatus for Turning-On and Turning-Off Elements Within a Field Emission Display Device"	2/28/01	6462484
1	E114-4P	CDST-E114-4P	09/896,402	"Cathode Burn-in Procedures for a Field Emission Display That Avoid Display Non-Uniformities:	6/28/01	6512335
1	E114-6P	CDST-E114-6P	10/025,084	"Procedures and Apparatus for Turning-On and Turning-Off Elements Within a Field Emission Display Device"	12/18/01	6624592
1	A115-3D	CDST-A115-3D	10/152,589	"Auxiliary Chamber & Display Device With Improved Contaminant Removal"	5/20/02	6853135
1	M117	CT-M117 US	09/307,044	"Display With Active Contrast Enhancement"	5/7/99	7071907
1	C122-2P	CDST-C122.2P	09/773,983	"Dual Layer Electroplated Structure for a Flat-Panel Display Device "	1/31/01	6731061
1	A133	CDST-A133	09/495,404	"Tuned Sealing Material for Sealing of a Flat Panle Display"	1/31/00	6555025
1	F137	CT-F137 US	09/823,872	"Structure and Fabrication of Light-Emitting Device Having Light-Reflective Layer Formed With or/and Adjacent to, Material That Enhances Device Performance"	3/30/01	6630786
1	F138	CDST-F138	10/028,043	"A Method for Photo-Imageable Lacquer Deposition for a Display Device"	12/20/01	6727048
1	C140	CT-C140 US	09/967,728	"Flat-Panel Display Containing Electron-Emissive Regions of Non-Uniform Spacing or/and Multi-Part Lateral Configuration"	9/28/01	6879097

Count	CTC Docket No.	Original Atty Docket No.	Serial Number	Title	File Date	Patent No.
1	E142	CDST-E142	10/302,063	"A System and Method for Adjusting Field Emission Display Illumination"	11/21/02	6771027
1	C143-1P	CDST-C143-1P	09/922,578	"Frit Protection in Sealing Process for Flat Panel Displays"	8/3/01	6632117
1	E144	CDST-E144	09/895,985	"Methods and Systems for Compensating Row-to-Row Brightness Variations of a Flat Panel Device"	6/28/01	6822628
1	E144-1D	CDST-E144.DIV1	10/969,494	"Methods and Systems for Compensating Row-to-Row Brightness Variations of a Flat Panel Device"	10/19/04	
1	M158	CT-M158 US	09/896,249	"Cleaning of Cathode-Ray Tube Display"	6/28/01	6873097
1	C159	CDST-C159	09/968,052	"Method for Implementing a 5-Mask Cathode Process"	9/28/01	6620013
1	F164	CDST-F164	10/001,084	"A Method for Screen Printed Lacquer Deposition for a Display Device"	10/31/01	
1	F165	CDST-F165	09/998,021	"A Method for Producing a Lacquer Layer for a Display Device"	11/29/01	
1	C169	CDST-C169	09/970,051	"A Method of Forming Powder Metal Phosphor Matrix and Gripper Structures in Wall Support"	10/2/01	6742257
1	C169-1P	CDST-C169.1P	09/993,740	"A Method of Patterning Wall and Phosphor Well Matrix Utilizing Glass"	11/21/01	6834431
1	C169-3D	622394-9	11/023,995	"Method of Patterning Wall and Phosphor Well Matrix Utilizing Glass"	12/27/04	
34						