

PATENT ASSIGNMENT

Electronic Version v1.1
 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
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NATURE OF CONVEYANCE:	ASSIGNMENT
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CONVEYING PARTY DATA	
Name	Execution Date
Spanion Inc.	01/31/2007

RECEIVING PARTY DATA	
Name:	Spanion LLC
Street Address:	915 DeGuigne Drive, P.O. Box 3453
Internal Address:	Mail Stop 250
City:	Sunnyvale
State/Country:	CALIFORNIA
Postal Code:	94088-3453

PROPERTY NUMBERS Total: 35

Property Type	Number
Patent Number:	6620717
Patent Number:	6773988
Patent Number:	7053446
Patent Number:	6994939
Patent Number:	6901010
Patent Number:	6700815
Patent Number:	6639844
Patent Number:	6690602
Patent Number:	7076703
Patent Number:	6813752
Patent Number:	7049188
Patent Number:	6743677
Patent Number:	6643177
Patent Number:	6897110

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Patent Number:	6906959
Patent Number:	6654283
Patent Number:	6646914
Patent Number:	6768683
Patent Number:	6859748
Patent Number:	6643185
Patent Number:	6861696
Patent Number:	6716710
Patent Number:	6689666
Patent Number:	6642106
Patent Number:	6784682
Patent Number:	6734028
Patent Number:	6576487
Patent Number:	6461905
Patent Number:	6897533
Patent Number:	6836432
Patent Number:	6606273
Patent Number:	6570787
Patent Number:	6646462
Patent Number:	6754106
Patent Number:	6828623

CORRESPONDENCE DATA

Fax Number: (408)616-3762
Correspondence will be sent via US Mail when the fax attempt is unsuccessful.
Phone: 408-616-3722
Email: sally.nieto@spansion.com
Correspondent Name: Raymond E. Fritz
Address Line 1: 915 DeGuigne Drive, P.O. Box 3453
Address Line 2: Mail Stop 250
Address Line 4: Sunnyvale, CALIFORNIA 94088-3453

NAME OF SUBMITTER:	Raymond E. Fritz
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Total Attachments: 4
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ASSIGNMENT AGREEMENT

This Assignment Agreement by and between **SPANSION INC.**, a corporation of Delaware having a place of business at 915 DeGuigne Drive, P. O. Box 3453, Sunnyvale, California 94088-3453, U.S.A., its affiliates and subsidiaries, (hereinafter referred to as "ASSIGNOR") and **SPANSION LLC**, a limited liability company of Delaware, having a place of business at 915 DeGuigne Drive, P. O. Box 3453, Sunnyvale, California, 94088-3453, U.S.A. (hereinafter "ASSIGNEE") is effective on the 21st day of December, 2005.

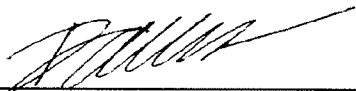
In consideration of good and valuable consideration, the receipt of which is hereby acknowledged, ASSIGNOR hereby assigns, transfers and conveys all of their right, title and interest in the United States and throughout the world in and to all invention disclosures, United States patent applications, United States patents, foreign patents, and foreign patent applications (including any Patent Cooperation Treaty (PCT) applications) identified on Schedule A attached hereto and made a part hereof, and the entire right, title and interest in the United States and throughout the world in and to the inventions to which said invention disclosures, patents and patent applications pertain, in and to all worldwide rights of priority, and in and to any corresponding United States and foreign patents, extensions and renewals of patents, results of reexamination, substitutions, reissues, patent applications, divisionals, continuations, continuations-in-part, utility models, petty patents, patents of importation, invention registrations, and inventor's certificates, together with all the rights and privileges granted and secured by said patents, extensions and renewals of patents, results of reexamination, substitutions, reissues, patent applications, divisionals, continuations, continuations-in-part, utility models, petty patents, patents of importation, invention registrations and inventor's certificates, including without limitation all claims, demands, rights and causes of action that ASSIGNOR may have against others on account of any past, present and future infringement of any of said patents, patent applications, utility models, petty patents, patents of importation, invention registrations, and inventor's certificates with the right in ASSIGNEE to sue for and obtain all relief to which ASSIGNOR may have been entitled by reason of any such infringement. ASSIGNOR further agrees that ASSIGNEE may apply for and receive Letters Patent, utility models, petty patents, patents of importation, invention registrations, and inventor's certificates for said inventions in its own name.

Said entire right, title, and interest is to be held and enjoyed by ASSIGNEE for its own use and benefit and for the use and benefit of its successors and assigns, to the full end of the term for which said patents, registrations or certificates may be granted, as fully and entirely as the same would have been held and enjoyed by ASSIGNOR had this assignment and sale not been made.

ASSIGNOR's assignment, transference and conveyance of all of its right, title and interest in the United States and throughout the world hereunder is made subject to any rights, licenses or immunities relating to such rights, titles and interests that have been granted to any persons by ASSIGNOR or its assignors.

IN WITNESS WHEREOF, the ASSIGNOR and ASSIGNEE have caused this Assignment Agreement to be executed by their respective duly authorized representatives as of December 21, 2005.

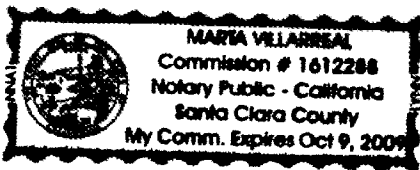
**ASSIGNOR
SPANSION INC.**

By: 
Name: Robert C. Melendres
Title: Executive Vice President, Corporate Development,
General Counsel and Corporate Secretary


Date: January 31, 2007

State of CALIFORNIA)
County of SANTA CLARA)

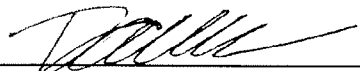
On January 31, 2007, before me, Marta Villarreal, a Notary Public, personally appeared Robert C. Melendres, personally known to me or proved to me on the basis of satisfactory evidence to be the person whose name is subscribed to the within instrument and acknowledged to me that he/she executed the same in his/her authorized capacity, and that by his/her signature on the instrument the person, or the entity upon behalf of which the person acted, executed the instrument.



Witness my hand and official seal.


SIGNATURE OF NOTARY

**ASSIGNEE
SPANSION LLC**

By: 
Name: Robert C. Melendres
Title: Executive Vice President, Corporate Development,
General Counsel and Corporate Secretary

Date: January 31, 2007

State of CALIFORNIA)
County of SANTA CLARA)

On January 31, 2007, before me, Marta Villarreal, a Notary Public, personally appeared Rodriguez C. Melendez, personally known to me or proved to me on the basis of satisfactory evidence to be the person whose name is subscribed to the within instrument and acknowledged to me that he/she executed the same in his/her authorized capacity, and that by his/her signature on the instrument the person, or the entity upon behalf of which the person acted, executed the instrument.

Witness my hand and official seal.



Marta Villarreal
SIGNATURE OF NOTARY

Docket No.	Country	Status	Application	File Date	Grant Date	Owner	Patent No.	Title
G0811	USA	Granted	10/100,487	3/14/2002	9/16/2003	A2S	6,620,717	MEMORY WITH DISPOSABLE ARC FOR WORDLINE
G0812	USA	Granted	10/243,108	9/13/2002	8/10/2004	A2S	6,773,988	MEMORY WORDLINE SPACER
G0812	USA	Granted	10/864,142	6/8/2004	5/30/2006	A2S	7,053,446	MEMORY WORDLINE SPACER
G0814	USA	Granted	10/283,685	10/29/2002	2/7/2006	A2S	6,994,939	SEMICONDUCTOR MANUFACTURING RESOLUTION
G0861	USA	Granted	10/119,366	4/8/2002	5/31/2005	A2S	6,901,010	IMPROVED ERASE METHOD FOR SINGLE SIDED
G0862	USA	Granted	10/119,273	4/8/2002	3/2/2004	A2S	6,700,815	REFRESH SCHEME FOR DYNAMIC PAGE
G0863	USA	Granted	10/099,499	3/13/2002	10/28/2003	A2S	6,639,844	OVERERASE CORRECTION METHOD
G0864	USA	Granted	10/119,391	4/8/2002	2/10/2004	A2S	6,690,602	ALGORITHM DYNAMIC REFERENCE PROGRAMMING
G0865	USA	Granted	10/305,700	11/26/2002	7/11/2006	A2S	7,076,703	METHOD AND SYSTEM FOR DEFINING A
G0867	USA	Granted	10/306,667	11/26/2002	11/2/2004	A2S	6,813,752	METHOD OF DETERMINING CHARGE LOSS
G0871	USA	Granted	10/305,724	11/26/2002	5/23/2006	A2S	7,049,188	LATERAL DOPED CHANNEL
G0872	USA	Granted	10/306,529	11/27/2002	6/1/2004	A2S	6,743,677	METHOD FOR FABRICATING NITRIDE MEMORY
G0875	USA	Granted	10/349,293	1/21/2003	11/4/2003	A2S	6,643,177	METHOD FOR IMPROVING READ MARGIN IN A FLASH
G0877	USA	Granted	10/305,750	11/26/2002	5/24/2005	A2S	6,897,110	METHOD OF PROTECTING A MEMORY ARRAY FROM
G0878	USA	Granted	10/306,252	11/27/2002	6/14/2005	A2S	6,906,959	A METHOD AND SYSTEM FOR ERASING A NITRIDE
G0929	USA	Granted	10/013,993	12/11/2001	11/25/2003	A2S	6,654,283	FLASH MEMORY ARRAY ARCHITECTURE AND
G0930	USA	Granted	10/096,313	3/12/2002	11/11/2003	A2S	6,646,914	FLASH MEMORY ARRAY ARCHITECTURE HAVING
G0931	USA	Granted	10/095,739	3/12/2002	7/27/2004	A2S	6,768,683	LOW COLUMN LEAKAGE FLASH MEMORY ARRAY
G1126	USA	Granted	10/190,420	7/3/2002	2/22/2005	A2S	6,859,748	TEST STRUCTURE FOR MEASURING EFFECT OF
G1127	USA	Granted	10/215,140	8/7/2002	11/4/2003	A2S	6,643,185	METHOD FOR REPAIRING OVER-ERASURE OF FAST
G1135	USA	Granted	10/429,140	5/3/2003	3/1/2005	A2S	6,861,696	STRUCTURE AND METHOD FOR A TWO-BIT MEMORY
G1142	USA	Granted	10/126,207	4/19/2002	4/6/2004	A2S	6,716,710	USING A FIRST LINER LAYER AS A SPACER IN A
G1144	USA	Granted	10/126,841	4/19/2002	2/10/2004	A2S	6,689,666	REPLACING A FIRST LINER LAYER WITH A THICKER
G1145	USA	Granted	10/159,323	5/31/2002	11/4/2003	A2S	6,642,106	METHOD FOR INCREASING CORE GAIN IN FLASH
G1148	USA	Granted	10/113,259	3/28/2002	8/31/2004	A2S	6,784,682	METHOD OF DETECTING SHALLOW TRENCH
G1150	USA	Granted	10/113,152	3/28/2002	5/11/2004	A2S	6,734,028	METHOD OF DETECTING SHALLOW TRENCH
G1244	USA	Granted	10/126,363	4/19/2002	6/10/2003	A2S	6,576,487	METHOD TO DISTINGUISH AN STI OUTER EDGE
G1247	USA	Granted	10/081,246	2/22/2002	10/8/2002	A2S	6,461,905	DUMMY GATE PROCESS TO REDUCE THE VSS
G1251	USA	Granted	10/247,641	9/18/2002	5/24/2005	A2S	6,897,533	METHOD FOR FORMING MULTI-BIT SILICON NITRIDE
G1255	USA	Granted	10/074,495	2/11/2002	12/28/2004	A2S	6,836,432	PARTIAL PAGE PROGRAMMING OF MULTI LEVEL
H0053	USA	Granted	10/121,140	4/11/2002	8/12/2003	A2S	6,606,273	METHODS AND SYSTEMS FOR FLASH MEMORY
H0054	USA	Granted	10/126,330	4/19/2002	5/27/2003	A2S	6,570,787	PROGRAMMING WITH FLOATING SOURCE FOR LOW
H0055	USA	Granted	10/178,144	6/24/2002	11/11/2003	A2S	6,646,462	EXTRACTION OF DRAIN JUNCTION OVERLAP WITH
H0062	USA	Granted	10/245,146	9/16/2002	6/22/2004	A2S	6,754,106	REFERENCE CELL WITH VARIOUS LOAD CIRCUITS
H0068	USA	Granted	10/232,487	8/30/2002	12/7/2004	A2S	6,828,623	FLOATING GATE MEMORY DEVICE WITH

PATENT