

U.S. PTO
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PLEASE RECORD THE ATTACHED

1. Name of conveying parties:

LEE, Jae-Kyeong

LIM, Kwan-Tack

Additional name(s) of conveying party(ies) attached?

☐ Yes☒ No

2. Name and address of receiving party:

Name: SAMSUNG ELECTRONICS CO., LTD.

Address: 416, Maetan-dong, Yeongtong-gu, Suwon-si,
Gyeonggi-do, Korea

Additional name(s) of receiving party(ies) attached?

☐ Yes☒ No

3. Nature of Conveyance:

☒ Assignment☐ Merger☐ Security Agreement☐ Change of Name☐ Other

Execution Date: March 13, 2007

4. Application number(s) or patent number(s):

If this document is being filed together with a new application, the execution date of the application is: March 13, 2007

A. Patent Application No.(s)

Title: THIN FILM TRANSISTOR PANEL AND
MANUFACTURING METHOD THEREOF

B. Patent No.(s)

11/724982

Additional numbers attached?

☐

Yes

☒

No

5. Name and address of party to whom correspondence concerning document should be mailed:

Name:

Internal Address: MACPHERSON, KWOK CHEN & HEID LLP

Street Address: 2033 Gateway Place, Suite 400

City San Jose State CA Zip 95110

6. Total number of applications and patents involved: One (1)

7. Total fee (37 CFR 3.41): \$40.00

☒

Authorized to be charged to Deposit Account No. 50-2257.

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To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

Kieun "Jenny" Sung, Reg. No. 48,639

Name of Person Signing

Signature

March 15, 2007

Total number of pages comprising cover sheet: 1

EXPRESS MAIL LABEL NO.: EV 946 544 405 US

PATENT
REEL: 019107 FRAME: 0084

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ASSIGNMENT

For good and valuable consideration, receipt of which is hereby acknowledged, we

LEE, Jae-Kyeong	of	308-403 Jugong apt., Sanggal-dong, Giheung-gu, Yongin-si, Gyeonggi-do, Korea
LIM, Kwan-Tack	of	644-304 Sinnamusil 6-danji Sinwon apt., Yeongtong-dong, Yeongtong-gu, Suwon-si, Gyeonggi-do, Korea

hereby sell, assign and transfer to SAMSUNG ELECTRONICS CO., LTD., a corporation, having a place of business at 416, Maetan-dong, Yeongtong-gu, Suwon-si, Gyeonggi-do, Korea, its successors and assigns, the entire right, title and interest throughout the world in our invention in

THIN FILM TRANSISTOR PANEL AND MANUFACTURING METHOD THEREOF

for which we have executed a United States patent application on or about the date of this assignment, and all patent applications and patents of every country for said invention, including divisions, reissues, continuations and extensions thereof, and all rights of priority resulting from the filing of said applications; we authorize the above-named assignee to apply for patents of foreign countries for said invention, and to claim all rights of priority without further authorization from us; we agree to execute all papers useful in connection with said United States and foreign applications, and generally to do everything possible to aid said assignee, their successors, assigns and nominees, at their request and expense, in obtaining and enforcing patents for said invention in all countries; and we request that the United States Patent and Trademark Office issue all patents granted for said invention to the above-named assignee, its successors and assigns.

Executed this 13th day of March, 2007



LEE, Jae-Kyeong

WITNESSED:

Signature

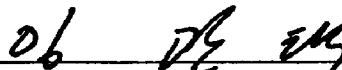


Date:

March 13, 2007

KIM, Yu-Mi

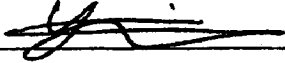
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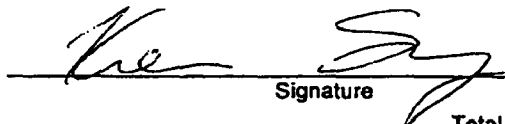
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