

PATENT ASSIGNMENT

Electronic Version v1.1

Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
Intersil Communications, Inc.	04/18/2007
RECEIVING PARTY DATA	
Name:	Intersil Americas, Inc.
Street Address:	1001 Murphy Ranch Road
City:	Milpitas
State/Country:	CALIFORNIA
Postal Code:	95035
PROPERTY NUMBERS Total: 3	
Property Type	Number
Patent Number:	5668397
Patent Number:	5807780
Patent Number:	5892264
CORRESPONDENCE DATA	
Fax Number:	(703)931-6037
<i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i>	
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Email:	wgwalter@aol.com
Correspondent Name:	Wallace G. Walter
Address Line 1:	5726 Clarence Ave.
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ATTORNEY DOCKET NUMBER:	SE-758
NAME OF SUBMITTER:	Wallace G. Walter
Total Attachments: 6	
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AFFIRMATION OF PATENT ASSIGNMENT

WHEREAS, Intersil Communications, Inc., a Delaware corporation, and all its subsidiaries, specifically including Intersil Americas, Inc., located at 1001 Murphy Ranch Road, Milpitas, California USA 95035, owns the entire right, title, and interest in and to the U.S. patents identified in Table A below, including each and every invention contained and expressed therein and including each and every continuation, continuation-in-part, reissue, reexamination, extension, substitute, or division of any of the U.S. patents there listed;

WHEREAS, said ownership of the U.S. patents listed in Table A below arose from a "Contribution Agreement" between Intersil Communications, Inc. and Intersil Americas Inc. executed Dec. 21, 2001 and having an effective date of December 31, 2001, the pertinent portions thereof attached hereto as Attachment 1.

TABLE A	
Patent Number	Issue Date
5668397	09/16/1997
5807780	09/15/1998
5892264	04/06/1999

WHEREAS, it is the purpose of this document to memorialize the assignment of rights affecting the patents in Table A by this Affirmation of Patent Assignment in a form suitable for recording in the United States Patent and Trademark Office,

NOW THEREFORE, in consideration of the sum of Ten Dollars (\$10.00) and other good and valuable consideration as set forth in said Contribution Agreement, the receipt and sufficiency of which is hereby acknowledged, Intersil Communications, Inc. affirms the assignment and transfer to Intersil Americas Inc. of Intersil Communications Inc.'s entire right, title and interest in and to the U.S. patents listed in Table A including all rights to sue or recover and retain damages and costs and attorneys fees for past, present, and future infringement of the U.S. patents listed in Table A; Intersil Communications, Inc. further acknowledges and confirms that Intersil Americas Inc. may, solely in Intersil Americas Inc.'s name, conduct all necessary procedures for recording and registering this Affirmation of Patent Assignment with any patent office, intellectual property office, or other governmental agency throughout the world;

Intersil Americas Inc. formally declares acceptance of all right, title and interest in and to the U.S. patents listed in Table A.

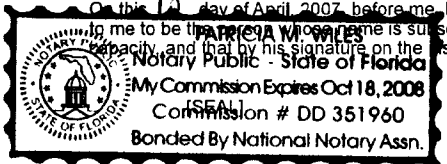
INTERSIL COMMUNICATIONS, INC.

BY: Douglas A. Balog Dated: 18 APRIL 2007
Douglas A. Balog, Associate General Counsel and Assistant Corporate Secretary,
On behalf of Intersil Communications, Inc. and all of its subsidiaries, specifically including Intersil Americas, Inc.

State of FLORIDA)
County of BREVARD)

On this 18th day of April, 2007, before me, Patricia M. Wiles, Notary Public, personally appeared Douglas A. Balog who is personally known to me to be the person whose name is subscribed to the within instrument and acknowledged to me he executed the same in his authorized capacity, and that by his signature on the instrument in person, or the entity upon his behalf of which the person acted, executed the instrument.

Patricia M. Wiles
Notary Public
My Commission Expires: 10/18/2008



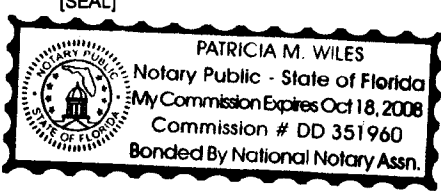
INTERSIL AMERICAS, INC.

BY: Douglas A. Balog Dated: 18 APRIL 2007
Douglas A. Balog, Associate General Counsel and Assistant Corporate Secretary,
On behalf of Intersil Americas, Inc.

State of FLORIDA)
County of BREVARD)

On this 18th day of April, 2007, before me, Patricia M. Wiles, Notary Public, personally appeared Douglas A. Balog, who is personally known to me to be the person whose name is subscribed to the within instrument and acknowledged to me he executed the same in his authorized capacity, and that by his signature on the instrument in person, or the entity upon his behalf of which the person acted, executed the instrument.

Patricia M. Wiles
Notary Public
My Commission Expires: 10/18/2008



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ATTACHMENT 1

CONTRIBUTION AGREEMENT

This Contribution Agreement (the "Agreement") is effective as of December 31, 2001 (the "Effective Date") at 11:59 PM United States of America EST (the "Effective Time") by and between Intersil Communications, Inc., a Delaware corporation ("Intersil"), and Intersil Americas Inc., a Delaware corporation ("Intersil Americas").

Background

To promote greater efficiency and economy in the management of the businesses carried on by the parties to this Agreement, Intersil desires to make a capital contribution to Intersil Americas of certain intellectual property and other assets used in the business of Intersil and its affiliates.

Terms

Intending to be legally bound, the parties to this Agreement agree as follows:

1. Effective as of the date first written above, Intersil shall contribute the following to Intersil Americas, in deemed exchange for stock of Intersil Americas in a non-recognition transaction as described in Section 351 of the Internal Revenue Code of 1986, as amended:

(i) all right, title and interest in and to the trademarks shown in Schedule A and all slogans, logotypes, designs, and trade dress associated therewith (the "Trademarks"), together with the U.S. federal registrations and applications for registration of the Trademarks, in and to all income, royalties, damages and payments now or hereafter due or payable with respect thereto and in and to all rights of action arising from the Trademarks, to be held and enjoyed by Intersil Americas for its own use and benefit and for its successors and assigns as the same would have been held by Intersil had this contribution not been made, and the goodwill of the business symbolized by the Trademarks;

(ii) all right, title and interest in and to the U.S. patents and patent applications shown in Schedule A (the "Patents"), including all reissues, divisions, continuations, continuations-in-part, and extensions thereof, to be held and enjoyed by Intersil Americas as fully and entirely as they would have been held and enjoyed by Intersil if this contribution had not been made, including all licenses of and proceeds from the Patents, all claims, demands and rights to recovery that Intersil has or may have in profits and damages for past and future infringements, if any, and all rights to compromise, sue for, and collect such profits and damages;

(iii) all right, title, and interest in and to all inventions shown in Schedule A (the "Inventions"), and all intellectual property rights relating thereto, including, without limitation, trade secret rights in the Inventions, and all rights of action arising from the Inventions, to be held and enjoyed by Intersil Americas for its own use and benefit and for its successors and assigns as the same would have been held by Intersil had this contribution not been made; and

(iv) all right, title, and interest in and to all copyrights owned by Intersil as of the date first written above that are not otherwise excluded from contribution under the terms of this Agreement, whether or not such copyrights have been registered (collectively, the "Copyrights")

including, without limitation, all U.S. registrations and applications for registration of the Copyrights, all licenses of and proceeds from the Copyrights, all publishing, electronic publishing, and other proprietary rights arising from or related to the Copyrights, all causes of action relating to the Copyrights that may have arisen prior to this contribution, and any recovery resulting from such causes of action.

Notwithstanding the foregoing or any specific item listed on Schedule A, the intellectual property used in connection with providing products or services that are regulated by the United States Department of State shall not be contributed to Intersil Americas.

2. Intersil and Intersil Americas shall each take any and all additional actions as may be necessary or appropriate to effect the transactions contemplated by this Agreement. Such actions may include, without limitation, the execution of additional documents to record the contribution made in this Agreement and the filing of such documents with the appropriate governmental authorities.

[Signatures commence on the following page]

IN WITNESS WHEREOF, the parties have caused this Agreement to be executed as of the date below to be effective as of the Effective Date and Effective Time.

INTERSIL COMMUNICATIONS, INC.
a Delaware corporation

By: Paul A. Bernkopf Date: 12/21/01
Name: PAUL A. BERNKOPF
Title: ASST. SECRETARY

INTERSIL AMERICAS INC.
a Delaware Corporation

By: Paul A. Bernkopf Date: 12/21/01
Name: PAUL A. BERNKOPF
Title: ASST. SECRETARY

SCHEDULE A
(i) PATENTS AND PATENT APPLICATIONS

ClientRef	SubCase	Status	ApI #	FileDate	Pat#	IssDate	Title
SE-745/746		Granted	07/523020	14-May-1990	5119321	02-Jun-1992	ADAPTIVE THRESHOLD SUPPRESSION OF IMPUL
SE-751		Granted	07/739133	30-Jul-1991	5241213	31-Aug-1993	BURIED ZENER DIODE HAVING AUXILIARY ZENER JUNCTION ACCESS PATH
SE-753		Granted	07/769209	01-Oct-1991	5200733	06-Apr-1993	RESISTOR STRUCTURE AND METHOD OF FABRICATION
SE-758	A	Granted	08/131369	04-Oct-1993	5668397	16-Sep-1997	HIGH FREQUENCY ANALOG TRANSISTORS, METHOD OF FABRICATION AND CIRCUIT IMPLEMENTATION
SE-758	B	Granted	08/462851	05-Jun-1995	5807780	15-Sep-1998	HIGH FREQUENCY ANALOG TRANSISTORS, METHOD OF FABRICATION AND CIRCUIT IMPLEMENTATION
SE-759		Granted	07/674147	25-Mar-1991	5311054	0-May-1994	IMPROVED GRADED COLLECTOR FOR INDUCTIVE LOADS
SE-759	A	Granted	08/191963	03-Feb-1994	5397714	14-Mar-1995	IMPROVED GRADED COLLECTOR FOR INDUCTIVE LOADS
SE-76		Granted	07/647346	29-Jan-1991	5140326	18-Aug-1992	CONVERTER COMPARATOR CELL WITH IMPROVED RESOLUTION
SE-768		Granted	07/774620	10-Oct-1991	5450339	12-Sep-1995	NONCANONIC FULLY SYSTOLIC LMS ADAPTIVE ARCHITECTURE
SE-770		Granted	07/774628	01-Jul-1991	5258939	02-Nov-1993	FOLD AND DECIMATE FILTER ARCHITECTURE
SE-766		Granted	07/604883	29-Oct-1990	5117282	26-May-1992	STACKED CONFIGURATION FOR INTEGRATED CIRCUITS DEVICES
SE-766		Granted	07/825133	24-Jan-1992	5228192	20-Jul-1993	STACKED CONFIGURATION FOR INTEGRATED CIRCUITS DEVICES
SE-772	A	Granted	07/816321	30-Dec-1991	5194867	16-Mar-1993	FLASH ANALOG-TO-DIGITAL CONVERTER EMPLOY LEAST SIGNIFICANT BIT REPRESENTATIVE COMPARATIVE REFERENCE VOLTAGE
		Granted	07/689645	23-Apr-1991	5233289	03-Aug-1993	VOLTAGE DIVIDER AND USE AS BIAS NETWORK FOR STACKED TRANSISTORS

SCHEDULE A
(i) PATENTS AND PATENT APPLICATIONS

ClientRef	SubCase	Status	Apl #	FileDate	Pat#	IssDate	Title
SE-1032-TD		Granted	08/726659	07-Oct-1996	5807783	15-Sep-1998	SURFACE MOUNT DIE BY HANDLE REPLACEMENT
SE-1254-AP		Granted	08/738393	24-Oct-1996	5719326	17-Feb-1998	RECONFIGURABLE FILTER SYSTEM
SE-921-TD	B	Granted	08/733368	17-Oct-1996	5872044	16-Feb-1999	LATE PROCESS METHOD AND APPARATUS FOR TRENCH ISOLATION
SE-1026-TD	A	Granted	08/754596	20-Nov-1996	5833758	10-Nov-1998	
SE-992	A	Granted	08/741639	28-Oct-1996	5841182	24-Nov-1998	CAPACITOR STRUCTURE IN A BONDED WAFER AND METHOD OF FABRICATION
SE-1234-TL		Granted	08/725924	04-Oct-1996	5812029	22-Sep-1998	GAIN CONTROL CIRCUIT AND METHOD
SE-921-TD	C	Granted	08/745104	07-Nov-1996	5920108	06-Jul-1999	LATE PROCESS METHOD AND APPARATUS FOR TRENCH ISOLATION
SE-967	B	Granted	08/754597	20-Nov-1996	5882423	16-Mar-1999	PLASMA CLEANING METHOD FOR IMPROVED INK BRAND PERMANENCY ON IC PACKAGES WITH METALLIC PARTS
SE-1265-WR		Granted	08/781736	10-Jan-1997	5949695	07-Sep-1999	INTERPOLATOR USING A PLURALITY OF POLYNOMIAL EQUATIONS AND ASSOCIATED METHODS
SE-1266-WR		Granted	08/781334	10-Jan-1997	5764113	09-Jun-1998	RE-SAMPLING CIRCUIT AND ASSOCIATED METHOD
SE-758	C	Granted	08/786569	21-Jan-1997	5892264	06-Apr-1999	HIGH FREQUENCY ANALOG TRANSISTORS, METHOD OF FABRICATION AND CIRCUIT IMPLEMENTATION
SE-1010-TD	A	Granted	08/775360	03-Jan-1997	5776814	07-Jul-1998	PROCESS FOR DOPING TWO LEVELS OF A DOUBLE POLY BIPOLAR TRANSISTOR AFTER FORMATION OF SECOND POLY LAYER
SE-1010-TD	B	Granted	08/775361	03-Jan-1997	5686322	11-Nov-1997	PROCESS FOR DOPING TWO LEVELS OF A DOUBLE POLY BIPOLAR TRANSISTOR AFTER FORMATION OF SECOND POLY LAYER
SE-1245-TD		Granted	08/771944	23-Dec-1996	5914280	22-Jun-1999	DEEP TRENCH ETCH ON BONDED SILICON WAFER