

05-04-2007



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PATENTS ONLY

FORM PTO-1595

(Rev. 3/01)

OMD NO. 0651-0011 exp. 5/31/2002)

U.S. DEPARTMENT OF COMMERCE

Patent and Trademark Office

To the Honorable Commissioner of Patents and Trademarks. Please record the attached original documents or copy thereof.

1. Name of conveying party(ies):

BTR, Inc.

Additional name(s) of conveying party(ies) attached?

No X Yes

3. Nature of Conveyance

X Assignment

Merger

Security Agreement

Change of Name

Other:

Execution Date(s): 3/16/07

2. Name and address of receiving party(ies):

Name: Actel Corporation

Internal Address:

Street Address: 2061 Stierlin Court

City: Mountain View State/Province: CA Zip: 94043

Country: U.S.A.

Additional name(s) & address(es) attached? Yes No X

4. Application Number(s) or patent number(s):

If this document is being filed together with a new application, the execution date of the application is:

A. Patent Application No.(s)

B. Patent No.(s)

5,850,564

Additional numbers attached? Yes No X

34. Name and address of party to whom correspondence concerning document should be mailed:

Name: Blakely, Sokoloff, Taylor & Zafman LLP

Internal Address:

Street Address: 12400 Wilshire Boulevard, 7th Floor

City: Los Angeles State: California Zip: 90025

6. Total number of applications and patents involved: 1

7. Total Fee (37 CFR 3.41).....\$1,040

X Enclosed

Authorized to be charged to deposit account

8. Deposit Account Number:

02-2666

(Attach duplicate copy of this page if paying by deposit account)

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9. Statement and signature.

To the best of my knowledge and believe, the foregoing is true and correct and any attached copy is a true copy of the original document

Daniel E. Ovanezian

Reg. No. 41,236

Name of Person Signing

Signature

5/1/07

Date

Total number of pages including cover sheet, attachments, and document: 7

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Atty Docket No. 008211.P006

05/03/2007 MJAMA1 00000071 5850564

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PATENT

REEL: 019246 FRAME: 0906

ASSIGNMENT OF PATENT RIGHTS BY BTR

This ASSIGNMENT OF PATENT RIGHTS, dated March 16, 2007 (this "Agreement"), is entered into by BTR, Inc., a Nevada corporation with a place of business at 20380 Town Center Lane, Suite 250, Cupertino, CA 95014 ("BTR" or "Assignor"), with and for the benefit of Actel Corporation, a California corporation with a place of business at 2061 Stierlin Court, Mountain View, CA 94043 ("Actel" or "Assignee").

WHEREAS, Assignor has agreed to sell and assign, and Assignee has agreed to buy and acquire all of Assignor's rights, title and interests in and to the patents and patent applications set forth in Exhibit A attached hereto (the "Assigned Patents").

NOW, THEREFORE, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, Assignor hereby assigns and transfers to Assignee any and all worldwide rights, title and interests Assignor holds, or may hold, in and to the Assigned Patents together with all rights derived therefrom, including but not limited to the right to sue for and collect damages for past, present and future infringement.

Assignor further agrees that, should additional or further documentation of the assignment be required for whatever reason, Assignor will, without further consideration, provide or execute such other information or documents as may be necessary upon Assignee's reasonable request.

This Agreement shall be binding on and shall inure to the benefit of, the Parties hereto and their respective successors and assigns. This Agreement will be governed by, and construed in accordance with, the internal laws of the State of California applicable to contracts executed and performed entirely therein, without regard to the principles of choice of law or conflicts or law of any jurisdiction. If any term or other provision of this Agreement is invalid, illegal or incapable of being enforced by any rule of law or public policy, all other conditions and provisions of this Agreement will nevertheless remain in full force and effect so long as the economic or legal substance of the transactions contemplated hereby is not affected in any manner materially adverse to any Party. Upon such determination that any term or other provision is invalid, illegal or incapable of being enforced, the Parties hereto will negotiate in good faith to modify this Agreement so as to effect the original intent of the Parties as closely as possible in a mutually acceptable manner in order that the transactions contemplated hereby be consummated as originally contemplated to the greatest extent possible.

EXHIBIT A
TO
ASSIGNMENT OF PATENT RIGHTS BY BTR

ASSIGNED PATENTS

Appl. No.	Issue Number	Filing Date	Title	Inventors
08/101,197	5,457,410 INVALIDATED/ ABANDONED	08-03-1993	ARCHITECTURE AND INTERCONNECT SCHEME FOR PROGRAMMABLE LOGIC CIRCUITS	Benjamin S. Ting Saratoga, CA
08/484,922	ABANDONED	06-07-1995		
09/034,769	6,433,580	03-02-1998		
09/955,589	6,507,217	09-13-2001		
10/269,364	6,703,861	10-11-2002		
10/692,880	7,017,136	10-23-2003		
11/299,248	PENDING	12-09-2005		
94922455.4	EU: 0712548	06-24-1994		
94922455.4	UK: 0712548	06-24-1994		
97111287.5	EU: 0806836	07-04-1997		
97111287.5	France: 0806836	07-04-1997		
97111287.5	Germany: 0806836	07-04-1997		
97111287.5	UK: 0806836	07-04-1997		
505821/95	Japan: PENDING	06-24-1994		
96-700569	Korea (South): 413881	02-03-1996		
US94/07187	Entered National Phase	06-24-1994		
94192983.3	China: 49812	06-24-1994		
9603530-8	Singapore: 44600	02-22-1996		
08/186,770	ABANDONED	01-25-1994	APPARATUS AND METHOD FOR PARTITIONING RESOURCES FOR INTERCONNECTIONS	Benjamin S. Ting Saratoga, CA
08/559,122	5,640,327 ABANDONED	02-09-1996		
95907365.1	ABANDONED	01-10-1995		
US95/00313	Entered National Phase	01-10-1995		
9605244-4	ABANDONED	02-03-1996		

Appl. No.	Issue Number	Filing Date	Title	Inventors
08/229,923	ABANDONED	04-14-1994	ARCHITECTURE AND INTERCONNECT SCHEME FOR PROGRAMMABLE LOGIC CIRCUITS	Benjamin S. Ting Saratoga, CA
08/534,500	ABANDONED	09-27-1995		
08/909,928	6,051,991 ABANDONED	08-12-1997		
09/482,149	6,462,578	01-12-2000		
10/117,875	6,597,196	04-05-2002		
10/428,724	6,747,482	05-01-2003		
10/829,527	6,989,688	04-21-2004		
11/233,290	7,078,933	09-21-2005		
11/432,425	7,142,012	05-10-2006		
95916402.1	EU: 00755588	04-14-1995		
95916402.1	Austria: 00755588	04-14-1995		
95916402.1	Belgium: 00755588	04-14-1995		
95916402.1	Denmark: 00755588	04-14-1995		
95916402.1	France: 00755588	04-14-1995		
95916402.1	Germany: 00755588	04-14-1995		
95916402.1	Greece: 00755588	04-14-1995		
95916402.1	Ireland: 00755588	04-14-1995		
95916402.1	Italy: 00755588	04-14-1995		
95916402.1	Luxembourg: 00755588	04-14-1995		
95916402.1	Monaco: 00755588	04-14-1995		
95916402.1	Netherlands: 00755588	04-14-1995		
95916402.1	Portugal: 00755588	04-14-1995		
95916402.1	Spain: 00755588	04-14-1995		
95916402.1	Sweden: 00755588	04-14-1995		
95916402.1	Switzerland: 00755588	04-14-1995		
95916402.1	UK: 00755588	04-14-1995		
95193431.7	China: ZL95193431.7	04-14-1995		
9611526-6	Singapore: 34648	04-14-1995		
527117/95	Japan: 3581152	10-14-1996		
96-705754	Korea (South): ABANDONED	10-14-1996		

Appl. No.	Issue Number	Filing Date	Title	Inventors
0119441.2	EU: 1162746	08-13-2001		
0119441.2	France: 1162746	08-13-2001		
0119441.2	Germany: 1162746	08-13-2001		
0119441.2	UK: 1162746	08-13-2001		
01118849.7	EU: Abandoned	08-13-2001		
05015294.1	EU: Abandoned	07-14-2005		
US95/04639	Entered National Phase	04-14-1995		
08/433,041	Abandoned	05-03-1995	SCALABLE MULTIPLE LEVEL TAB ORIENTED INTERCONNECT ARCHITECTURE	Benjamin S. Ting Saratoga, CA Peter M. Pani Mountain View, CA
08/951,814	6,088,526 ABANDONED	10-14-1997		
09/377,304	6,300,793	08-18-1999		
96915392.3	EU: 0824791	04-30-1996		
96915392.3	UK: 0824791	04-30-1996		
02024873.8	EU: PENDING	11-08-2002		
96194985.6	China: ZL96194985.6	04-30-1996		
533406/96	Japan: 3727065	11-04-1997		
97-707796	Abandoned	11-04-1997		
US96/05964	Entered National Phase	04-30-1996		
8510228	Taiwan: NI-082978	05-01-1996		
9705151-0	Singapore: 46840	04-30-1996		
08/434,980	5,850,564 Abandoned	05-03-1995	FLOOR PLAN FOR SCALABLE MULTIPLE LEVEL TAB ORIENTED INTERCONNECT ARCHITECTURE	Benjamin S. Ting Saratoga, CA Peter M. Pani Mountain View, CA
09/089,298	6,417,690	06-01-1998		
10/021,744	7,009,422	12-05-2001		
11/326,543	7,126,375	01-04-2006		
96920113.6	EU: 0824792	04-30-1996		
96920113.6	UK: 0824792	04-30-1996		
533412/96	Japan: 3684241	04-30-1996		
97-707797	Abandoned	11-03-1997		
96/05982	Entered National Phase	04-30-1996		
96194984.8	China: ZL 96194984.8	04-30-1996		

Appl. No.	Issue Number	Filing Date	Title	Inventors
85105227	Taiwan: NI-081900	05-01-1996	ARCHITECTURE AND INTERCONNECT FOR PROGRAMMABLE LOGIC CIRCUITS	Benjamin S. Ting Saratoga, CA Peter M. Pani Mountain View, CA
9706178-2	Singapore: 53402	04-30-1996		
09/467,736	6,320,412	12-20-1999		
US00/35019	Abandoned	12-20-2000		