# PATENT ASSIGNMENT

Electronic Version v1.1
Stylesheet Version v1.1

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| SUBMISSION TYPE: | NEW ASSIGNMENT |
| NATURE OF CONVEYANCE: | ASSIGNMENT |

CONVEYING PARTY DATA

| Name | Execution Date |
|---|---|
| Silicon Laboratories, Inc. | 03/23/2007 |

RECEIVING PARTY DATA

| | |
|---|---|
| Name: | NXP, B.V. |
| Street Address: | High Tech Campus 60, 5656 |
| City: | AG Eindhoven |
| State/Country: | NETHERLANDS |

PROPERTY NUMBERS Total: 3

| Property Type | Number |
|---|---|
| Application Number: | 11255701 |
| Application Number: | 11362982 |
| Application Number: | 11477014 |

CH $120.00 11255701

CORRESPONDENCE DATA

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Address Line 4: San Jose, CALIFORNIA 95131

| | |
|---|---|
| ATTORNEY DOCKET NUMBER: | 5797-02700,02800,03300 |
| NAME OF SUBMITTER: | Peter Zawilski |

Total Attachments: 18
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# INTELLECTUAL PROPERTY ASSIGNMENT AGREEMENT

This INTELLECTUAL PROPERTY ASSIGNMENT AGREEMENT (this "Assignment"), is effective as of March 23, 2007, among NXP B.V., a limited liability company organized under the laws of The Netherlands ("Parent Buyer"), Silicon Laboratories Inc., a Delaware corporation ("Parent Seller") and Silicon Laboratories International Pte. Ltd., a private limited company organized under the laws of Singapore ("Subsidiary Seller"). Parent Buyer is sometimes referred to in this Assignment as "Assignee" and each of Parent Seller and Subsidiary Seller are sometimes referred to in this Assignment collectively as "Assignor." Unless otherwise defined herein, capitalized terms shall have the meaning set forth in the Purchase Agreement (as defined below).

## RECITALS

WHEREAS, Assignor and Assignee have entered into a Sale and Purchase Agreement, dated as of February 8, 2007 (the "Purchase Agreement"), pursuant to which, among other things, Assignee is acquiring certain Intellectual Property Rights on the terms and subject to the conditions set forth therein; and

WHEREAS, this Assignment is required to be executed and delivered by Assignor on or prior to the Closing Date, pursuant to Section 7.3(b) of the Purchase Agreement.

## ASSIGNMENT

NOW, THEREFORE, for good and valuable consideration, including that recited in the Purchase Agreement, the receipt and sufficiency of which is hereby acknowledged, the parties hereto agree as follows:

1. Assignment of Intellectual Property Rights. Assignor does hereby sell, transfer, and assign to Assignee all of the right, title, and interest of Assignor and its Subsidiaries in and to the Transferred Business IPR, including without limitation the specific items set forth below:

(a) all issued patents and pending patent applications listed in Attachment I hereto;

(b) all trademark registrations and applications listed in Attachment II hereto and the goodwill associated therewith;

(c) all Copyrights included in the Transferred Business IPR, including the software listed in Attachment III hereto; and

(d) all Mask Works included in the Transferred Business IPR, including the mask work registrations listed in Attachment IV hereto.

2. Allocation. The consideration allocable to the transactions contemplated by this Agreement shall be determined in accordance with Section 9.5(d) of the Purchase Agreement.

3. Assistance and Cooperation. This Assignment is effective between the parties on the date hereof. Assignor further agrees, subject to Section 4 (Perfection and Recordation), to perform (or cause to be performed) all such lawful acts and to execute (or cause to be executed) all such further assignments and other lawful documents as may reasonably be necessary to effectuate the Assignment and to perfect and record the Assignment in the various jurisdictions.

4. Perfection and Recordation. Assignee shall prepare all documents that are necessary to perfect and record the assignments of the Transferred Business IPR in the various jurisdictions, and Assignee shall be responsible for all of its own expenses, including recordation expenses, associated therewith.

5. Entire Agreement. This Assignment may be modified only in a written instrument executed by the parties.

6. Binding Assignment. This Assignment shall be binding upon and inure to the benefit of each of the parties hereto, their successors and permitted assigns.

7. Governing Law. This Assignment shall be governed by and construed in accordance with the internal laws of the State of New York.

8. Severability. In the event that any one or more of the provisions contained in this Assignment shall, for any reason, be held to be invalid, illegal or unenforceable in any respect, then to the maximum extent permitted by law, such invalidity, illegality or unenforceability shall not affect any other provision of this Assignment.

9. Counterparts. This Assignment may be executed in one or more counterparts, each of which shall be deemed an original, but all of which together shall constitute one and the same instrument.

10. Headings. The section headings contained in this Assignment are inserted for reference purposes only and are not intended to be a part, nor should they affect the meaning or interpretation, of this Assignment.

11. Transfer Taxes. With respect to this Assignment and to the extent permitted by applicable Law, the parties intend that this Assignment constitutes a transaction that is not subject to Transfer Taxes. Buyer shall use its best efforts to comply with all of the conditions required for the transaction to qualify as an excluded transaction for such purposes, and both Buyer and Seller shall retain the books and records of the transferred business for at least five (5) years from the Closing.

*(Signature Page Follows.)*

IN WITNESS WHEREOF, the undersigned parties have each caused this Assignment to be executed by a duly authorized officer as of the date first above written.

NXP B.V.

By: _____
Name: _____
Its: _____

Silicon Laboratories Inc., on behalf of itself and its Subsidiaries

By: _____
Necip Sayiner
President and Chief Executive Officer

Silicon Laboratories International Pte. Ltd., on behalf of itself and its Subsidiaries

By: _____
Name: _____
Its: _____

IN WITNESS WHEREOF, the undersigned parties have each caused this Assignment to be executed by a duly authorized officer as of the date first above written.

NXP B.V.

By: _____
Name: _____
Its: _____

Silicon Laboratories Inc., on behalf of itself and its Subsidiaries

By: _____
Necip Sayiner
President and Chief Executive Officer

Silicon Laboratories International Pte. Ltd., on behalf of itself and its Subsidiaries

By: _____
Name: _____
Its: _____

STATE OF )
) SS.
COUNTY OF )

I, a notary public, in and for the county and state aforesaid, do hereby certify that NECIP SAYINER personally known to me to be the PRESIDENT & CEO of Silicon Laboratories, Inc., a Delaware corporation, appeared before me this day in person and acknowledged that such person signed the above and foregoing instrument as such person's free and voluntary act and as the free and voluntary act of the corporation pursuant to authority granted to such person by the board of directors of the corporation for the uses and purposes therein set forth.

IN WITNESS WHEREOF, I have hereunto set my hand and notarial seal this 23rd day of MARCH, 2007.

Wendy E. Byrum
Notary Public

WENDY E. BYRUM
NOTARY PUBLIC
STATE OF TEXAS
EXPIRES
9-27-2008

My commission expires: 9-27-2008

AU W195996.5

IN WITNESS WHEREOF, the undersigned parties have each caused this Assignment to be executed by a duly authorized officer as of the date first above written.

NXP B.V.

By: _____
Name: _____
Its: _____

Silicon Laboratories Inc., on behalf of itself and its Subsidiaries

By: _____
Necip Sayiner
President and Chief Executive Officer

Silicon Laboratories International Pte. Ltd., on behalf of itself and its Subsidiaries

By: _____
Name: GH LOW
Its: International Finance Director

SINGAPORE )
) SS.
)

I, Sia Moon Joon, a notary public, duly admitted to practice in the region aforesaid, do hereby certify that LOW SIOK HIONG personally known to me to be the International Finance Mgr of Silicon Laboratories International Pte. Ltd., a private limited company organized under the laws of Singapore, appeared before me this day in person and acknowledged that such person signed the above and foregoing instrument as such person's free and voluntary act and as the free and voluntary act of the corporation pursuant to authority granted to such person by the board of directors of the company and on behalf of the company for the uses and purposes therein set forth.

IN WITNESS WHEREOF, I have hereunto set my hand and notarial seal this 22nd day of March, 2007 in SINGAPORE, Singapore.

Notary Public

NOTARY PUBLIC
Sia Moon Joon
1 Apr 2005 - 31 Mar 2007
SINGAPORE

AU4195996.5

ATTACHMENT I

LIST OF ASSIGNED PATENTS AND PATENT APPLICATIONS

The following specifically identified patents and patent applications (and any patents that issue as a result of those patent applications):

1. U.S. patent no. 6163548 issued on December 19, 2000 titled "Code Synchronization Unit and Method"

2. U.S. patent no. 6363055 issued on March 26, 2002 titled "Control Channel Determiner"

3. U.S. patent no. 6104769 issued on August 15, 2000 titled "Method and Apparatus for Acquiring and Tracking the Sampling Phase of a Signal"

4. U.S. patent no. 6301317 issued on October 9, 2001 titled "Synchronization System and Method for Digital Communication Systems"

5. U.S. patent no. 6084891 issued on July 4, 2000 titled "Bi-Directional Channel Analysis"

6. U.S. patent no. 6112325 issued on August 29, 2000 titled "Method and Device for Detecting Rate"

7. U.S. patent no. 5994953 issued on November 30, 1999 titled "Apparatus and Method for Detecting Digital FM"

8. U.S. patent no. 6118770 issued on September 12, 2000 titled "Voice-Channel Frequency Synchronization"

9. U.S. patent no. 6088668 issued on July 11, 2000 titled "Noise Suppresor Having Weighted Gain Smoothing"

10. U.S. patent no. 6208684 issued on March 27, 2001 titled "Cyclic Adaptive Receivers for DS-CDMA Signals"

11. U.S. patent no. 6459398 issued on October 1, 2002 titled "Pulse Modulated Digital to Analog Converter (DAC)" (relating to 5994953)

12. U.S. patent no. 6323727 issued on November 27, 2001 titled "Apparatus and Method for Detecting Digital FM"

13. U.S. patent no. 6345057 issued on February 5, 2002 titled "Bi-Directional Channel Analysis" (relating to 6084891)

14. U.S. patent no. 6298102 issued on October 2, 2001 titled "Maximum Likelihood Sequence Estimation in the Presence of Co-Channel Interference"

15. U.S. patent no. 6317709 issued on November 13, 2001 titled "Noise Suppresor Having Weighted Gain Smoothing" (relating to 6088668)

16. U.S. patent no. 6400784 issued on June 4, 2002 titled "Synchronization System and Method for Digital Communication Systems" (relating to 6301317)

17. U.S. patent no. 5640427 issued on June 17, 1997 titled "Demodulator"

18. Korean patent no. 10-2000-7005393 filed on May 17, 2000 titled "Cyclic Adaptive Receivers for DS-CDMA Signals" (relating to 6208684)

19. U.S. patent application no. 10/426,042 filed on April 29, 2003 titled "Highly Integrated Radio-Frequency Apparatus and Associated Methods"

20. U.S. patent application no. 11/152,136 filed on June 14, 2005 titled "Performing Diagnostics In a Wireless System"

21. U.S. patent application no. 11/153,065 filed on June 15, 2005 titled "Synchronizing A Modem Vocoder of a Mobile Station"

22. U.S. patent application no. 11/170,526 filed on June 29, 2005 titled "A Communication Apparatus Including a Demodulator Having a Tone Detection Mechanism"

23. U.S. patent application no. 11/170,547 filed on June 29, 2005 titled "A Communication Apparatus Including A Demodulator Having a Synchronization Mechanism"

24. U.S. patent application no. 11/170,548 filed on June 29, 2005 titled "A Communication Apparatus Including a Mechanism for Preventing Demodulation of Text Telephone Information During Poor Signal Reception"

25. U.S. patent application no. 11/172,040 filed on June 30, 2005 titled "A Communication Apparatus Including a Mechanism for Reducing Loss of Text Telephone Information During Normal Traffic Channel Preempting"

26. U.S. patent application no. 11/231,182 filed on September 20, 2005 titled "Interchangeable Receive Inputs for Band and System Swappability in Communication Systems and Methods"

27. U.S. patent application no. 11/253,469 filed on September 23, 2005 titled "Synchronizing A Channel Codel And Covoder of a Mobile Station"

28. U.S. patent application no. 11/255,692 filed on October 21, 2005 titled "Digital Rights Management Security Mechanism for Use in a Wireless Communication Apparatus"

29. U.S. patent application no. 11/255,701 filed on October 21, 2005 titled "Method and System for Securing a Wireless Communication Apparatus "

30. U.S. patent application no. 11/362,982 filed on February 27, 2006 titled "System with Linear and Switching Regulator Circuits"

31. U.S. patent application no. 11/427,800 filed on June 30, 2006 titled "Wireless Communication Device with Self Calibration Feature For Controlling Power Output"

32. U.S. patent application no. 11/476,485 filed on June 28, 2006 titled "Managing Audio During A Handover In A Wireless System"

33. U.S. patent application no. 11/476,972 filed on June 28, 2006 titled "Suppressing Uplink Noise Due To Channel Type Mismatches"

34. U.S. patent application no. 11/478,205 filed on June 29, 2006 titled "Partial Frame Detection"

35. U.S. patent application no. 11/479,080 filed on June 30, 2006 titled "Sidetone Generation For A Wireless System That Uses Time Domain Isolation"

36. U.S. patent application no. 11/477,014 filed on June 28, 2006 titled "A Wireless Communication Apparatus Including A Mechanism For Suppressing Uplink Noise"

37. PCT patent application no. PCT/US06/11769 filed on March 30, 2006 titled "Performing Diagnostics In a Wireless System" (relating to 11/152,136)

38. PCT patent application no. PCT/US06/11770 filed on March 30, 2006 titled "Synchronizing A Modem Vocoder of a Mobile Station" (relating to 11/153,065)

39. PCT patent application no. PCT/US06/16533 filed on January 19, 2006 titled "Interchangeable Receive Inputs for Band and System Swappability in Communication Systems and Methods" (relating to 11/231,182)

40. Taiwanese patent application no. 95111195 filed on March 30, 2006 titled "Performing Diagnostics In a Wireless System" (relating to 11/152,136)

41. Taiwanese patent application no. 95111199 filed on March 30, 2006 titled "Synchronizing A Modem Vocoder of a Mobile Station" (relating to 11/153,065)

42. China patent application no. 99802069.9 filed on September 15, 1999 titled "Cyclic Adaptive Receivers for DS-CDMA Signals" (relating to 6208684)

43. European patent application no. 99946409.2 filed on September 15, 1999 titled "Cyclic Adaptive Receivers for DS-CDMA Signals" (relating to 6208684)

44. Japanese patent application no. 2000-571578 filed on September 15, 1999 titled "Cyclic Adaptive Receivers for DS-CDMA Signals" (relating to 6208684)

45. U.S. patent no. 6,175,722 issued on January 16, 2001 titled "Initial frequency synchronization mechanism"

46. U.S. patent no. 6,176,611 issued on January 23, 2001 titled "System and method for reducing power consumption in waiting mode"

47. U.S. patent no. 6,411,830 issued on June 25, 2002 titled "System and method for reducing power consumption in waiting mode" (relating to 6,176,611)

48. U.S. patent no. 6,574,288 issued on June 3, 2003 titled "Method And Apparatus For Adjusting A Digital Control Word To Tune Synthesized High-Frequency Signals For Wireless Communication"

49. U.S. patent no. 6,946,898 issued on September 20, 2005 titled "System And Method For Biasing Electrical Circuit"

50. U.S. patent no. 7,019,571 issued on March 28, 2006 titled "Frequency Synthesizer for a Wireless Communication System"

51. U.S. patent application no. 10/748,425 filed on December 30, 2003 titled "Chopped Intermediate Frequency Wireless Receiver"

52. U.S. patent application no. 10/814,426 filed on March 31, 2004 titled "Communication Apparatus Implementing Time Domain Isolation With Restricted Bus Access"

53. China (CN) patent application no. 200580015536.X (relating to 10/814,426)

54. Europe (EP) patent application no. 05730178.0 (relating to 10/814,426)

55. Japan (JP) patent application no. [not yet assigned] (relating to 10/814,426)

56. U.S. patent application no. 11/479,733 filed on June 30, 2006 titled "Communication Apparatus Implementing Time Domain Isolation With Restricted Bus Access" (relating to 10/814,426)

57. U.S. patent application no. 10/898,057 filed on July 23, 2004 titled "Apparatus Using Interrupts For Controlling A Processor For Radio Isolation and Associated Methods"

58. China (CN) patent application no. [not yet assigned] titled "Apparatus Using Interrupts For Controlling A Processor For Radio Isolation and Associated Methods" (relating to 10/898,057)

59. Europe (EP) patent application no. 05773805.2 titled "Apparatus Using Interrupts For Controlling A Processor For Radio Isolation and Associated Methods" (relating to 10/898,057)

60. U.S. patent application no. 10/955,926 filed on September 30, 2004 titled "Apparatus for Controlling A Digital Signal Processor For Radio Isolation and Associated Methods" (relating to 10/898,057))

61. PCT patent application no. PCT/US05/35279 filed on September 30, 2005 titled "Apparatus for Controlling A Digital Signal Processor For Radio Isolation and Associated Methods" (relating to 10/955,926)

62. Taiwan (TW) patent application no. 094133300 filed on September 26, 2005 titled "Apparatus for Controlling A Digital Signal Processor For Radio Isolation and Associated Methods" (relating to 10/955,926)

63. U.S. patent application no. 10/898,058 filed on July 23, 2004 titled "Apparatus Using Interrupts for Controlling A Processor For Radio Isolation and Associated Methods"

64. U.S. patent application no. 10/902,233 filed on July 29, 2004 titled "Integrated Circuit Having Features to Limit Substrate Current"

65. U.S. patent application no. 10/954,791 filed on September 30, 2004 titled "Wireless Communication System and Method With Frequency Burst Acquisition Feature Using Autocorrelation"

66. U.S. patent application no. 10/955,569 filed on September 30, 2004 titled "Wireless Communication System With Hardware-Based Frequency Burst Detection"

67. U.S. patent application no. 10/955,584 filed on September 30, 2004 titled "Wireless Communication System and Method With Frequency Burst Acquisition Feature Using Autocorrelation"

68. U.S. patent application no. 11/014,142 filed on December 16, 2004 titled "Use of Structures for Improving Metal Density Near Inductors"

69. U.S. patent application no. 11/014,143 filed on December 16, 2004 titled "Semiconductor Integrated Circuit Including Metal Mesh Structure"

70. China (CN) patent application no. [not yet assigned] (relating to 11/014,143)

71. Europe (EP) patent application no. 05747466.0 (relating to 11/014,143)

72. Japan (JP) patent application no. [not yet assigned] (relating to 11/014,143)

73. U.S. patent application no. 11/025,673 filed on December 29, 2004 titled "Communication Apparatus Having a SIM Interface Compatible with Radio Isolation"

74. PCT patent application no. PCT/US05/47387 filed on December 28, 2005 titled "Communication Apparatus Having a SIM Interface Compatible with Radio Isolation" (relating to 11/025,673)

75. U.S. patent application no. 11/094,583 filed on March 30, 2005 titled "System and Method for Efficient Power Supply Regulation Compatible With Radio Frequency Operation"

76. PCT patent application no. PCT/US06/04885 filed on February 10, 2006 titled "System and Method for Efficient Power Supply Regulation Compatible With Radio Frequency Operation" (relating to 11/094,583)

77. Taiwan (TW) patent application no. 095110952 titled "System and Method for Efficient Power Supply Regulation Compatible With Radio Frequency Operation" (relating to 11/094,583)

78. U.S. patent application no. 11/112,616 filed on April 22, 2005 titled "Controlling the Frequency of an Oscillator"

79. U.S. patent application no. 11/166,711 filed on June 24, 2005 titled "Signal Processing Task Scheduling in a Communication Apparatus"

80. PCT patent application no. PCT/US06/24723 filed on June 23, 2006 titled "Signal Processing Task Scheduling in a Communication Apparatus" (relating to 11/166,711)

81. Taiwan (TW) patent application no. 095123046 titled "Signal Processing Task Scheduling in a Communication Apparatus" (relating to 11/166,711)

82. U.S. patent application no. 11/186,501 filed on July 21, 2005 titled "System and Method for Operating a Phase-Locked Loop"

83. U.S. patent application no. 11/220,958 filed on September 7, 2005 titled "Voltage Regulator with Shunt Feedback"

84. PCT patent application no. PCT/US06/34512 filed on September 6, 2006 titled "Voltage Regulator with Shunt Feedback" (relating to 11/220,958)

85. U.S. patent application no. 11/234,517 filed on September 23, 2005 titled "Channel Decoding Using Hard and Soft Decisions"

86. U.S. patent application no. 11/240,745 filed on September 30, 2005 titled "Management of Regulator-Induced Switching Noise for Sampled Systems"

87. PCT patent application no. PCT/US06/35702 filed on September 12, 2006 titled "Management of Regulator-Induced Switching Noise for Sampled Systems" (relating to 11/240,745)

88. U.S. patent application no. 11/241,285 filed on September 30, 2005 titled "Dynamic Bias of Stacked Device in High Voltage applications"

89. U.S. patent application no. 11/241,286 filed on September 30, 2005 titled "Inductor Discharge in DCM to reduce spectrum noise"

90. U.S. patent application no. 11/242,110 filed on September 30, 2005 titled "System and Method for Adjusting Dither in a Delta Sigma Modulator"

91. PCT patent application no. PCT/US06/36609 filed on September 20, 2006 titled "System and Method for Adjusting Dither in a Delta Sigma Modulator" (relating to 11/242,110)

92. U.S. patent application no. 11/385,521 filed on March 21, 2006 titled "Controlling Power Output of a Transceiver"

93. PCT patent application no. PCT/US06/35396 filed on September 12, 2006 titled "Controlling Power Output of a Transceiver" (relating to 11/385,521)

94. U.S. patent application no. 11/394,249 filed on March 30, 2006 titled "Digital Variable Gain Amplifier"

95. U.S. patent application no. 11/510,339 filed on August 25, 2006 titled "EDGE Transceiver Architecture and Related Methods" (Claimed priority to same parent provisional (60/720,413) as 11/394,249)

96. PCT patent application no. PCT/US06/35289 filed on September 12, 2006 titled "EDGE Transceiver Architecture and Related Methods" (relating to 11/510,339)

97. U.S. patent application no. 11/528,383 filed on September 22, 2006 titled "Method and Apparatus for Handling Deeply Discharged Batteries in a Mobile Station"

98. U.S. patent application no. 11/095,263 filed on March 31, 2005 titled "Method and Apparatus for Providing a Sidetone In A Wireless Communication Device:

99. U.S. patent application no. 11/172,212 filed on June 29, 2005 titled "Dynamic Logging Control for TDI"

100. U.S. patent application no. 11/172,213 filed on June 29, 2005 titled "Digital-to-Analog Converter Startup Systems withTDI"

101. U.S. patent application no. 11/025,672 filed on December 29, 2004 titled "Standard Serial Communications Compatible with Radio Isolation"

102. U.S. patent application no. 10/880,239 filed on June 29, 2004 titled "Keypad Scanning With Radio Event Isolation"

103. U.S. patent application no. 10/881,167 filed on June 30, 2004 titled "Communication Apparatus Including Dual Timer Units"

104. Europe (EP) patent application no. EP05767044.0 filed December 14, 2006 – from PCT/US05/23043 (relating to 10/881,167)

105. Japan (JP) patent application no. not yet assigned filed December 21, 2006 – from PCT/US05/23043 (relating to 10/881,167)

106. Singapore (SG) patent application no. 200609112-8 filed December 28, 2006 – from PCT/US05/23043 (relating to 10/881,167)

107. China (CN) patent application no. 200580021686.1 filed December 21, 2006 – from PCT/US05/23043 (relating to 10/881,167)

108. U.S. patent application no. 10/897,953 filed on July 23, 2004 titled "Method of Controlling a Processor For Radio Isolation Using a Timer"

To the extent owned by Seller and its Subsidiaries, any renewals, reissues, reexaminations, extensions, continuations, continuations-in-part (but these only to the extent based on inventions existing as of the Closing Date) , divisions and substitutions relating to any of the specifically identified patents and patent applications, as well as all related foreign patent and patent applications that are counterparts to such specifically identified patents and patent applications.

# ATTACHMENT II

## LIST OF ASSIGNED TRADEMARKS

All Trademarks relating to the following trademark registrations and applications:

| MARK | GOODS AND SERVICES | SERIAL NO. FILING DATE | REG. NO. REG. DATE | STATUS |
|---|---|---|---|---|
| AERO | **Class 9** – Integrated circuits sold to OEMs for incorporation into wireless communication devices | 76/263,322 05/25/2001 | 2,858,341 06/26/2004 | Registered |
| AEROFONE | **Class 9** - Software for use in the design of computer and telephony systems, together with accompanying documentation | 78/706,165 09/02/2005 | | Suspended 03/20/2006 |
| AEROFONE | **Class 9** - Software for use in the design of computer and telephony systems, together with accompanying documentation | 5186692 03/02/2006 | | Filed |
| AEROFONE | **Class 9** - Software for use in the design of computer and telephony systems, together with accompanying documentation | 004917688 02/17/2006 | | Opposed |
| AEROFONE | **Class 9** - Software for use in the design of computer and telephony systems, together with accompanying documentation | 40-2006-10223 02/28/2006 | | Filed |
| AEROFONE | **Class 9** - Software for use in the design of computer and telephony systems, together with accompanying documentation | 095007934 02/21/2006 | | Approved (awaiting registration certificate) |

ATTACHMENT III

LIST OF ASSIGNED SOFTWARE

1. Kernel: Internally developed RTOS emulation
2. Algorithm Development Environment (ADE)
3. Design Verification Environment (DVE)
4. Runsim
5. Verification environment
6. ATE Test Software
7. GUI programmer for EVBs
8. Data analysis software
9. Embedded software
10. Layer 1software
11. Platform and system drivers software
12. Device drivers software
13. GSM/GPRS/EDGE Protocol Stack software
14. Basic MMI software
15. Own Real Time Operating System (SX)
16. Middleware (basic file system, audio manager, battery charging, accessory detection,)
17. Minishell
18. Protocol Trace tool
19. Software Download tool
20. NVRAM Editor Tool
21. File System Builder Tool
22. Factory Test Mode Tool
23. Diagnostic Tools (AudioMon, RadioMon, AeroShell,)
24. PostProcessing tools (Stromster, FieldTestPostProcessor)
25. Test Environment (field testing, module testing, software generation,)
26. Concentrator
27. Digital-ATE control
28. Peripherals software control
29. DSP Algorithms
30. AeroFone software to access Si470x in DAC mode

ATTACHMENT IV

LIST OF ASSIGNED MASKWORKS

Registered Maskworks:

| Die Name | Revision # | Filing Date | Certificate Date | MW No. |
|---|---|---|---|---|
| 4301 | A | 7/30/2002 | 09/23/02 | 16-493 |
| 4201 | A | 10/3/2001 | 10/04/01 | 16-025 |
| 4200 | A | 1/25/2002 | 01/28/02 | 16-224 |
| 4200 | B | 1/25/2002 | 01/28/02 | 16-219 |
| 4113T | A | 4/2/2003 | 04/11/03 | 16-643 |
| 4200 | C | 9/9/2002 | 09/10/02 | 16-565 |
| 4201 | B | 9/9/2002 | 09/10/02 | 16-566 |
| 4200 | D | 9/9/2002 | 09/10/02 | 16-571 |
| 4201 | C | 9/9/2002 | 09/10/02 | 16-572 |
| 4200 | E | 12/18/2002 | 01/16/03 | 16-554 |
| 4200DB | E | 9/23/2003 | 09/30/03 | 16-793 |
| 4200 | F | 9/23/2003 | 09/30/03 | 16-790 |
| 4200 | G | 3/31/2005 | 04/01/05 | 17-470 |
| 4300 | B | 3/31/2005 | 04/01/05 | 17-467 |
| 4210 | A | 10/26/2004 | 10/27/04 | 17-277 |
| 4210 | B | 2/22/2005 | 02/22/05 | 17-430 |
| 4133T | A | 1/25/2002 | 1/28/2002 | 16-232 |
| 4133T | B | 9/9/2002 | 9/10/2002 | 16-570 |
| 4133T | C | 12/18/2002 | 1/9/2003 | 16-502 |
| 4134T | A | 5/28/2004 | 6/1/2004 | 17-008 |

Unregistered Maskworks:

| Die Name | Revision # | Filing Date |
|---|---|---|
| 4210 | B2 | 9/20/2006 |
| 4300 | C | 10/6/2006 |
| 4210 | Z | 9/20/2006 |
| 4210 | C | 9/20/2006 |
| 4300 | C25 | 3/8/2007 |
| 4300 | D | 3/8/2007 |
| 4210 | D | (*) |
| 4210 | D1 | (*) |
| 4901 | A | (*) |
| 4901 | B | (*) |
| 4904/5 | C | (*) |
| 4904/5 | C1 | (*) |
| 4212 | A | (*) |
| 4212 | B | (*) |

| Die Name | Revision # | Filing Date |
|---|---|---|
| 4212 | C | (*) |
| 4212 | C1 | (*) |
| 4212 | Y | (*) |
| 4212 | Z | (*) |
| 4213 | A | (*) |
| 4213 | B | (*) |
| 4210S | A | (*) |
| 4210S | B | (*) |
| 4210S | B1 | (*) |
| 4210S | C | (*) |
| 4212S | A | (*) |
| 4212S | B | (*) |
| 4212S | B1 | (*) |
| 4212S | C | (*) |
| 4214 | A | (*) |
| 4215 | A | (*) |
| 4905S | A | (*) |
| 4905S | B | (*) |
| 4905 | Z | (*) |
| 4300 | D | (*) |
| 4300 | E | (*) |

(*) No application has been filed.