PATENT ASSIGNMENT

Electronic Version v1.1
Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
Zarlink Semiconductor Limited	07/14/2006

RECEIVING PARTY DATA

Name:	Intel Corporation
Street Address:	2200 Mission College Boulevard
City:	Santa Clara
State/Country:	CALIFORNIA
Postal Code:	95052

PROPERTY NUMBERS Total: 1

Property Type	Number
Application Number:	10825601

CH $40.00 10825601

CORRESPONDENCE DATA

Fax Number: (202)638-4810
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ATTORNEY DOCKET NUMBER:	108347-06001
NAME OF SUBMITTER:	George E. Oram, Jr.

Total Attachments: 8
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CONFIRMATORY PATENT APPLICATION ASSIGNMENT

This Confirmatory Patent Application Assignment Agreement, dated July 14, 2006 is made by Zarlink Semiconductor Limited, a corporation incorporated under the laws of England and Wales ("Assignor"), to Intel Corporation, a Delaware corporation ("Assignee").

RECITALS

A. Assignor and Assignee and other parties have entered into an asset purchase agreement dated as of October 7, 2005, (the "Agreement"). All capitalized terms used herein but not otherwise defined shall have the meanings set forth in the Agreement.

B. Pursuant to the Agreement, Assignor has previously assigned to the Assignee all of Assignor's right, title and interest in and to the patent applications set forth on Exhibit A hereto (the "Patent Applications").

AGREEMENT

NOW, THEREFORE, in consideration of the foregoing premises, the mutual covenants and agreements contained in the Agreement and the covenants and agreements in the Assignment, Assignor hereby confirms as follows:

1. For good and valuable consideration, the receipt of which is hereby acknowledged, Assignor has, on October 7, 2005, pursuant to the terms of the Agreement, sold, transferred, conveyed, assigned and delivered to Assignee, with full title guarantee all of Assignor's right, title and interest in and to the Patent Applications and any patents that may issue there from, including any foreign counterparts, divisions continuations, or reissues of such patents, the same to be held by Assignee for Assignee's own use and enjoyment, and for the use and enjoyment of Assignee's successors, assigns and other legal representatives, as fully and entirely as the same would have been held and enjoyed by Assignor if this Assignment and sale had not been made; together with all claims for damages by reason of past infringements of the Patent Applications, along with the right to sue for and collect such damages for the use and benefit of Assignee and its successors, assigns and other legal representatives.

2. Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and, in the case of any patent applications filed with any office of any country or countries foreign to the United States, any officer of such country whose duty it is to issue patents or other evidence or forms of intellectual property protection or applications as aforesaid, to issue the same to Assignee and its successors, assigns and other legal representatives in accordance with the terms of this instrument.

IN WITNESS WHEREOF, Assignor has executed this Assignment on the date first above written.

Assignor:

ZARLINK SEMICONDUCTOR LIMITED

By: _____

Name: Scott Milligan Don McIntyre

Title: Director Director

[SIGNATURE PAGE TO ZARLINK CONFIRMATORY PATENT APPLICATION ASSIGNMENT]

Acknowledgment by Notary Public

State of n/a

County of n/a

On this 14th day of July, 2006, before me, the undersigned Notary Public, personally appeared Don McIntyre and Scott Milligan personally known to me (or proved to me on the basis of satisfactory evidence) to be the person whose name is subscribed to the within instrument, and acknowledged to me that he or she executed the same.

Seal:

Signature: _____

Name: Renata Pontello-Corning Notary Public in and for the Province of Ontario, Canada

EXHIBIT A

CONFIRMATORY PATENT APPLICATION ASSIGNMENT

Z #	Patent Application Title	Inventors	Country	Filed	Serial #
S337	Ultra linear transconductor	Nick Cowley Arshad Madni	Japan	27/12/1999	99/369934
S342	IMPROVED ARCHITECTURE FOR ZERO IF TUNERS	Nick Cowley Mark Mudd	US	08/06/2000	09/590332
S347	Radio frequency amplifier	Arshad Madni Lance Trodd	Europe	03/07/2000	00305610.8
S767	New Architectural Arrangement for Digital Tuners	Nick Cowley Alison Payne Mark Dawkins	Europe	15/08/2001	01306949.7
S768	Tuner	Nick Cowley Arshad Madni	Japan	10/09/2001	2001-273733
			US	05/10/2001	09/971468
S769	Architectural arrangements for universal wired/wireless modem applications	Nick Cowley	US	24/10/2001	10/032879
S781	A Novel Method for Improving the Intermodulation Performance of Radio Receivers	Nick Cowley	Europe	08/08/2001	01306790.5
S833	Radio Frequency Tuner	Nick Cowley	US	04/12/2001	10/004766
			Europe	03/12/2001	01310103.5
S887	A novel method for improving the intermodulation performance of radio receivers	Nick Cowley	US	26/04/2002	10/133601
			Europe	08/04/2002	02252509.1
S888	A novel method for improving the intermodulation performance of radio receivers	Nick Cowley	US	15/07/2002	10/195586
			Europe	09/07/2002	02254809.3
S909	A novel front end implementation for multi channel receivers	Nick Cowley Mark Mudd Arshad Madni Franco Lauria	US	24/04/2002	10/131303
			Europe- UK	24/04/2002	02252905.1
			Japan	25/04/2002	2002-124314
S916	Mixer with emitter follower driven LNA	Arshad Madni Franco Lauria Mark Mudd Lance Trodd Nick Cowley	Europe	23/08/2002	02102214.0
			Japan	30/08/2002	2002-255050
S932	Improvements in Front Ends for Cable Reception Equipment	Nick Cowley Mark Mudd Franco Lauria	US	20/09/2002	10/247952
			Europe	02/09/2002	02102274.4
S943	A Novel CMOS VCO Control	Richard Albon David Johnston	Japan	21/08/2002	2002-240872

Z #	Patent Application Title	Inventors	Country	Filed	Serial #
S963	Tuner Arrangement and Set Top Box	Nick Cowley Theodore Aaron	US	27/11/2002	10/305579
S1015	A Novel method for improving the intermodulation performance of radio receivers	Lance Trodd Franco Lauria	Japan	24/04/2003	2003-119862
			Korea	02/05/2003	10-2003-28253
			Europe	02/05/2003	03101220.6
S1022	New architectural arrangement for digital tuners	Nick Cowley Scot Cuthbertson Matthew Aitken	US	14/07/2003	10/619672
			Europe	03/06/2003	03101604.1
S1072	A method for power and gain distribution optimization in digital terrestrial tuners	Nick Cowley Keith Jones	US	25/02/2004	10/788007
			China	27/02/2004	200410007267.X
S1075	Multi-Channel Tuner Apparatus	Nick Cowley Peter Coe	UK	21/12/2002	0229971.7
			US	18/12/2003	10/741364
S1080	An adaptive technique for suppressing interfering signals in receivers	Nick Cowley Peter Coe	UK	07/05/2003	0310358.7
			US	29/04/2004	10/83469
			China	29/04/2004	200410036655.0
S1085	Architecture for Multi-Tuner Systems	Bob Hanrahan	US	10/04/2003	10/410915
S1099	A Method for cancelling spurious heterodyne products within a radio receiver.	Nick Cowley Alan Elkins	UK	07/06/2003	0313107.5
			US	05/05/2004	10/838656
			China	04/06/2004	200410046094.2
S1102	An Architecture and methodology for automatic alignment calibration in digital terrestrial receivers	Nick Cowley Peter Coe	UK	27/11/2003	0327542.7
			US	22/11/2004	10/992785
			Germany	26/11/2004	102004057241.0
			China	11/11/2004	200410092918.X
S1108	A Novel Architecture for Digital Tuners	Nick Cowley Peter Coe	UK	05/06/2004	0412676.9
			Germany	03/06/2005	102005025612.0
			China	03/06/2005	200510075550.0
			US	24/05/2005	11/135645
S1109	A Technique for Removing Spurious Heterodyning Products in Radio Receivers	Nick Cowley Terry Aliwell	UK	22/05/2004	0411483.1
			US	13/05/2005	11/128335
			China	20/05/2005	200510072791.X
S1110	A New Tuner arrangement for Broadband reception	Nick Cowley	UK	20/06/2005	0512414.4
			China	22/06/2005	200510079070.1
			US	21/06/2005	11/156567
S1111	A novel architecture for digital tuners	Dave Sawyer Nick Cowley	UK	25/07/2005	0515100.6
			China	05/08/2005	200510089724.9
			US	01/08/2005	11/193382
S1112	Adjacent Channel interference rejection using software techniques	Gary Thorpe	UK	18/10/2004	0423079.3

Z #	Patent Application Title	Inventors	Country	Filed	Serial #
			China	to be advised	to be advised
			Germany	to be advised	to be advised
			US	to be advised	to be advised
1115	A method for isolating radio frequency sources	Nick Cowley Peter Coe	UK	17/01/2005	0500825.5
S1116	A Method for receiving multiple channels	Nick Cowley	UK	10/02/2005	0502667.9
S1117	A method for removing interferring signals	Dave Sawyer Nick Cowley Issac Ali	UK	08/06/2005	0511569.6
S1118	A method for aligning filters in I & Q branches used in quadrature down conversion architectures	Issac Ali Nick Cowley	UK	08/06/2005	0511579.5
S1119	A method for minimizing signal crosstalk	Issac Ali Nick Cowley	UK	08/06/2005	0511582.9
S1120	A Frequency agile IF Implementation	Issac Ali Nick Cowley	UK	08/06/2005	0511585.2
S1121	Technique to control drift in analogue delay line	Dave Sawyer Nick Cowley Issac Ali	UK	08/06/2005	0511586.0
S1122	A Method for improving signal crosstalk	Issac Ali Nick Cowley	UK	16/08/2005	0516768.9
S1123	A Method for removing signal contamination	Issac Ali Nick Cowley	UK	16/08/2005	0516766.3
1124	A Method for generating an accurate phase delay	Dave Sawyer Nick Cowley Issac Ali	UK	23/08/2005	0517217.6

Z #	Patent Title	Inventors	Country	Filed	Serial #
S753	QAM Demodulator Synchronisation Algorithm	Bernard Arambepola	Europe	29/03/2001	01303010.1
S780	Impulse Correction Arrangements for DTTSystems	Nick Cowley Allson Payne M Dawkin	US	16/08/2001	09/931458
			Japan	14/08/2001	2001-246042
S954	Impulsive Noise Suppression Circuit for Digital TV Demodulators	Bernard Arambepola	US	30/10/2002	10/283915
			Europe	17/10/2002	02257197.0
S1040	A Novel tuner architecture for digital and analogue media reception	Nick Cowley Richard Crossley	UK	19/10/2002	0224337.6
			US	16/10/2003	10/688556
S1042	Method for in-phase and quadrature analogue to digital conversion using a single converter	Bernard Arambepola Philip Hackney	US	06/08/2003	10/635782
			Europe	07/08/2003	03102462.3
S1043	Method for optimising the analogue filter bandwidth in OFDM receiver	Bernard Arambepola Nick Cowley	US	27/10/2003	10/694931

Z #	Patent Title	Inventors	Country	Filed	Serial #
S1064	Digital television broadcast receiver	Scot Cuthbertson	US	22/10/2003	10/691753
			Europe	26/09/2003	3103573.6
S1098	A Line Based OSD Composition System suitable for Digital TV Systems	Brian Holland	UK	30/04/2003	0309810.0
			US	16/04/2004	10/825588
S1101	A Novel Implementation for a Digital Television Broadcast Receiver	Scot Cuthbertson Richard Crossley Malcolm Lomer	UK	17/05/2003	0311365.1

Z #	Patent Title	Inventors	Country	Filed	Serial #
S750	Amplifiers	Arshad Madni	Europe	06/11/2000	00309862.1
S770	Radio frequency amplifier and tuner	Arshad Madni	Europe	01/05/2001	1303999.5
S1036	Virtuel Earth LNA	Arshad Madni	Europe - UK	21/05/2003	03101451.7
			US	15/05/2003	10/439126
S1083	Lattice Capacitor	Peter Laws	UK	06/02/2003	302735.6
			US	06/02/2004	10/774032
S1086	Technique for Assessing Electro-Static Discharge (ESD) Vulnerabilities in the Design of Integrated Circuits	Keith Strickland Stephen Chadfield John O'Connell	UK	08/04/2003	308070.2
			US	07/04/2004	10/819354

Z #	Patent Title	Inventors	Country	Filed	Serial #
S331	Amplifier circuit arrangement	Slava Souetinov Peter Laws	Europe	14/03/2000	302023.7
S625	Voltage offset compensation circuit	Keith Jones	Japan	01/03/1996	56780/96
S626	Controllable filter arrangement	Colin Perry Marcus Granger-Jones	Japan	01/03/1996	82999/96
S628	Integrated circuit output buffer	David Wilcox	Japan	05/12/1996	340497/96
S648	Amplifier and loop filter arrangement for a PLL using a charge pump	David Clarke Ian Fobbester	Japan	11/04/1997	110460/97
S667	Low Noise Amplifier	Slava Souetinov	Japan	06/01/1998	12084/98
S669	Image Reject Mixer	Slava Souetinov Chan T K	Japan	06/01/1998	12085/98
S671	Low Voltage Mixer	Slava Souetinov	Japan	09/01/1998	15076/98
S685	Image reject mixer arrangements	Slava Souetinov S Graham	Japan	06/11/1998	315527/1998
S688	Low voltage amplifiers	Jeffrey Smith Colin Perry	Japan	13/11/1998	323403/1998
S898	High Gain CMOS Mixer	Slava Souetinov	US	07/03/2002	10/190879
S1065	Automatic Bias Current Trimming	Keith Jones Andrew Talbot	UK	25/02/2003	304275.1
			US	16/08/2005	to be advised

Z #	Patent Title	Inventors	Country	Filed	Serial #
S1071	LO breakthrough reduction using on chip compensation	Andrew Moran Stephen Parry Alun Watkins	UK	22/04/2003	309043.8
			US	16/04/2004	10/825601
S1103	RF Bias Servo Loop	Colin Perry	UK	01/10/2003	323021.6
			US	09/09/2004	10/936877
S1105	Parallel Varactor structure for improved linearity of c/v response	C Shepherd Colin Perry S Parry A Deldda	UK	24/11/2003	327285.3
S1106	Linearised VCO for generating FM signals	C Shepherd Colin Perry S Parry A Deldda	UK	24/11/2003	327284.6