

U.S. PTO
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05-11-2007

U.S. DEPARTMENT OF COMMERCE
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RE	
To the Director of the U.S. Patent and Trademark Office	
103404358	
or the new address(es) below.	
1. Name of conveying party(ies) Pantas SUTARDJA Zining WU	
Additional name(s) of conveying party(ies) attached? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No	
3. Nature of conveyance/Execution Date(s): Execution Date(s) <u>April 24, 2007</u> <input checked="" type="checkbox"/> Assignment <u>MP1142</u> <input type="checkbox"/> Merger <input type="checkbox"/> Security Agreement <input type="checkbox"/> Change of Name <input type="checkbox"/> Joint Research Agreement <input type="checkbox"/> Government Interest Assignment <input type="checkbox"/> Executive Order 9424, Confirmatory License <input type="checkbox"/> Other	
4. Application or patent number(s): <input checked="" type="checkbox"/> This document is being filed together with a new application. A. Patent Application No.(s) 04/30/2007 1 ABDELRI 00000021 11789867 05 FC:8021 40.00 DP Additional numbers attached? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No	
5. Name and address to whom correspondence concerning document should be mailed: Name: <u>Andrew D. Fortney, Ph.D.</u> Internal Address: <u>The Law Offices of Andrew D. Fortney, Ph.D., P.C.</u> Street Address: <u>401 W. Fallbrook Avenue., Suite 204</u> City: <u>Fresno</u> State: <u>CA</u> Zip: <u>93711</u> Phone Number: <u>559-432-6847</u> Fax Number: <u>559-432-6872</u> Email Address: <u>info@fortneylaw.com</u>	
6. Total number of applications and patents involved: <u>1</u>	
7. Total fee (37 CFR 1.21(h) & 3.41) \$ <u>40.</u> <input checked="" type="checkbox"/> Authorized to be charged by credit card <input checked="" type="checkbox"/> Authorized to be charged to deposit account <input type="checkbox"/> Enclosed *** Additional Fees Only *** <input type="checkbox"/> None required (government interest not affecting title)	
8. Payment Information a. Credit Card Last 4 Numbers <u>1007</u> Expiration Date <u>04/10</u> b. Deposit Account Number <u>50-1236</u> Authorized User Name <u>Marvell Semiconductor, Inc.</u>	
9. Signature: <u>Andrew D. Fortney, Ph.D.</u> <u>04-25-2007</u> Signature Date Name of Person Signing Total number of pages including cover sheet, attachments, and documents: <u>3</u>	

Documents to be recorded (including cover sheet) should be faxed to (571) 273-0140, or mailed to:
Mail Stop Assignment Recordation Services, Director of the USPTO, P.O. Box 1450, Alexandria, V.A. 22313-1450

PATENT
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ASSIGNMENT

For good and valuable consideration, the receipt of which is hereby acknowledged, I, the undersigned,

Pantas Sutardja
Zining Wu

who have created a certain invention for which an application for United States Letters Patent was executed by me concurrently herewith and entitled:

**Circuits, Architectures, Apparatuses, Systems, Methods, Algorithms, Software and
Firmware for Using Reserved Cells to Indicate Defect Positions**

Do hereby sell, assign and transfer to Marvell Semiconductor, Inc., a corporation of California, having a place of business at 5488 Marvell Lane, Santa Clara, CA 95054, its successors, assigns, and legal representatives, the full and exclusive right to said invention and said application and to any and all inventions described in said application for the United States, its territorial possessions and all foreign countries, and the entire right, title and interest in and to any and all Letters Patent which may be granted therefor in the United States, its territorial possessions and all foreign countries; and in and to any and all continuations-in-part, continuations, divisions, substitutes, reissues, extensions thereof, and all other applications for Letters Patent relating thereto which have been or shall be filed in the United States, its territorial possessions and/or any foreign countries, and all rights, together with all priority rights, under any of the international conventions, unions, agreements, acts, and treaties, including all future conventions, unions, agreements, acts, and treaties;

Do hereby further sell, assign and transfer to Marvell Semiconductor, Inc., its successors, assigns, and legal representatives, the full and exclusive right to United States Provisional Patent Applications(s) No(s). 60/809,208, filed May 30, 2006, and to any and all inventions described in said provisional patent application(s) for the United States, its territorial possessions and all foreign countries, and the entire right, title and interest in and to any and all Letters Patent which may be entitled to the benefit thereof in the United States, its territorial possessions and all foreign countries; and in and to any and all continuations-in-part, continuations, divisions, substitutes, reissues, extensions thereof, and all other applications for Letters Patent relating thereto which have been or shall be filed in the United States, its territorial possessions and/or any foreign countries, and all rights, together with all priority rights, under any of the international conventions, unions, agreements, acts, and treaties, including all future conventions, unions, agreements, acts, and treaties;

Agree that Marvell Semiconductor, Inc., hereinafter referred to as Assignee, may apply for and receive Letters Patent for said invention and said inventions, hereinafter referred to as said invention, in its own name, in the United States, its territorial possessions, and all foreign countries; and that, when requested to carry out in good faith the intent and purpose of this assignment, at the expense of said Assignee, its successors, assigns and legal representatives, the undersigned will execute all continuations-in-part, continuations, divisions, substitutes, reissues, extensions thereof, execute all rightful oaths, assignments, powers of attorney and other papers, testify in any legal or quasi legal proceedings; communicate to said Assignee, its successors, assigns or legal representatives all facts known to the undersigned relating to said invention and the history thereof; and generally do everything possible which said Assignee, its successors, assigns, or legal representatives shall consider desirable for aiding in securing, maintaining and enforcing proper patent protection for said invention and for vesting title to said invention and all applications for patents on said invention in said Assignee, its successors, assigns, or legal representatives; and

Covenant with said Assignee, its successors, assigns, or legal representatives that no assignment, grant, mortgage, license or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned.

IN TESTIMONY WHEREOF I have hereunto set MY signature on the date indicated below.

Full Name of Sole/First Inventor:

Pantas Sutardja

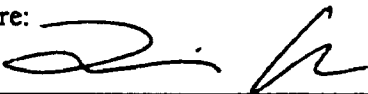
Inventor's Signature:

Date: Month/Day/Year

4/29/07

Express Mail No. EQ451999278US

Attorney Docket No.: MP1142

Full Name of Second Inventor, if any:	Zining Wu
Inventor's Signature: 	Date: Month/Day/Year 4/24/2007

ASSIGNMENT

Corporate

Marvell Semiconductor, Inc., a California corporation, having a principal place of business at 5488 Marvell Lane, Santa Clara, CA 95054 (hereafter the "Assignor"), is the owner by respective Assignment of United States Patent Application(s) identified below (hereafter sometimes called the "Patent Application(s)").

Marvell International Ltd., a corporation of Bermuda, having a place of business at Argyle House, 41A Cedar Avenue, Hamilton, Hm12, Bermuda (hereafter the "Assignee"), desires to acquire all of the rights to the Patent Application(s) and all inventions described and claimed therein or entitled to the benefit thereof.

THEREFORE, in consideration of the sum of One Dollar (\$1.00) or equivalent and other good and valuable consideration paid to it, Assignor hereby sells and assigns to the Assignee the entire right, title and interest in and to the Patent Application(s) identified below, and all inventions described and claimed therein or entitled to the benefit thereof, in any and all Letters Patent therefor, and in any and all reissues, extensions, renewals, reexaminations, divisions and continuations of such applications or Letters Patent to the full end of the term or terms for which such Letters Patent issue, such entire right, title and interest to be held and enjoyed by the above-named Assignee the same as they would have been held and enjoyed by the Assignor had this Assignment and sale not been made.

<u>Marvell Semiconductor, Inc. Case No.</u>	<u>U. S. Serial No.</u>	<u>U. S. Filing Date</u>	<u>Title</u>
MP1142	New Application	Herewith	Circuits, Architectures, Apparatuses, Systems, Methods, Algorithms, Software and Firmware for Using Reserved Cells to Indicate Defect Positions
MP1142PR	60/809,208	May 30, 2006	Using Pilot Cells in Non-Volatile Memory to Indicate Defect Positions

By its undersigned representative, the Assignor agrees

a. to execute all papers necessary in connection with the Patent Application(s) and any continuing, divisional, reissue, reexamination or corresponding application thereof and also to execute separate Assignment in connection with such application as the Assignee may deem necessary or expedient;

b. to execute all papers necessary in connection with any interference which may be declared concerning the Patent Application(s) or any continuation, division, reissue or reexamination thereof and to cooperate with the Assignee in every way possible in obtaining evidence and going forward with such interference; and

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c. to perform all affirmative acts which may be necessary to obtain a grant of a valid United States patent to the Assignee on any of the Patent Application(s) and on any continuation, division, reissue or reexamination of any of the Patent Application.

The Assignor hereby covenants that it has the full right to convey the entire interest herein assigned, and that it has not executed, and will not execute, any agreement in conflict therewith.

Marvell Semiconductor, Inc., a California corporation, certifies that it is the Assignee of the entire right, title and interest in the Patent Application(s) identified above by virtue of Assignment from the inventors of the Patent Application(s) identified above. Assignment to be recorded in the Patent and Trademark Office as identified below.

<u>Marvell Semiconductor, Inc.</u> <u>Case No.</u>	<u>U. S. Serial No.</u>	<u>Assignment Execution Date</u>
MP1142	New Application	April 24, 2007
MP1142PR	60/808,208	April 24, 2007

The undersigned has reviewed the documents in the Patent Application(s) identified above and, to the best of undersigned's knowledge and belief, title is in the Assignor identified above.

The undersigned is empowered to sign this certificate on behalf of the Assignor.

I hereby declare that all statements made herein of my own knowledge are true, and that all statements made on information and belief are believed to be true; and further, that these statements are made with the knowledge that willful false statements, and the like so made, are punishable by fine or imprisonment, or both, under Section 1001, Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

IN WITNESS WHEREOF, executed by the Assignor's undersigned representative on the date following the undersigned's name.

Marvell Semiconductor, Inc.

By:


Eric Janofsky

Title: Vice President Intellectual Property and General Patent Counsel

Date April 25, 2007

RECORDED: 04/25/2007

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