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(Rev. 6-93)

RECORDATION FORM COVER SHEET

U.S. DEPARTMENT OF COMMERCE
Patent and Trademark Office

OMB No. 0651-0011 (exp. 4/94)

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To the Honorable Commissioner of Patents and Trademarks: Please record the attached original documents or copy thereof.

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Zarlink Semiconductor LimitedAdditional name(s) of conveying party(ies) attached? ☐ Yes ☒ No

3. Nature of conveyance:

- ☒ Assignment ☐ Merger
☐ Security Agreement ☐ Change of Name
☐ Other _____

Execution Dates: July 14, 2006

2. Name and address of receiving party(ies)

INTEL CORPORATION

Address:

2200 Mission College Boulevard**Santa Clara, California 95052**Additional names(s) & address(es) attached? ☐ Yes ☒ No

4. Application number(s) or patent number(s):

A. Patent Application No.(s)

Additional numbers attached? ☐ Yes ☒ NoIf this document is being filed together with a new application,
the execution date of the application is: _____

B. Patent No.(s)

SEE SCHEDULEAdditional numbers attached? ☐ Yes ☒ No5. Name and address of party to whom correspondence
concerning document should be mailed:Name: Alan Israel, Esq.Address: KIRSCHSTEIN, et al.489 Fifth AvenueNew York, New York 100176. Total number of applications and patents involved: **10**7. Total Fee (37 C.F.R. 3.41).....\$400.00

- ☒ Enclosed
☐ Authorized to be charged to deposit account

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Alan Israel, Reg. No. 27,564
Name of Person Signing

Signature

May 31, 2007
Date

Total number of pages including cover sheet, attachments, and document: **4****PATENT****700328259****REEL: 019390 FRAME: 0042**

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KIRSCHSTEIN OTTINGER

002

SCHEDULE OF PATENT PROPERTIES**ZARLINK SEMICONDUCTOR LIMITED****TO****INTEL CORPORATION**

U.S. Patent No.	Issue Date
6,278,299	08/21/2001
6,407,632	06/18/2002
5,469,093	11/21/1995
5,684,433	11/04/1997
6,567,488	05/20/2003
6,901,248	05/31/2005
6,304,142	10/16/2001
5,572,165	11/05/1996
6,480,066	11/12/2002
5,815,214	09/29/1998

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001

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(Rev. 5-93)

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☐ Security Agreement ☐ Change of Name
☐ Other _____

Execution Dates: July 14, 2006

Additional names(s) & address(es) attached? ☐ Yes ☒ No

4. Application number(s) or patent number(s):

A. Patent Application No.(s)

Additional numbers attached? ☐ Yes ☒ NoIf this document is being filed together with a new application,
the execution date of the application is: _____

B. Patent No.(s)

SEE SCHEDULE

Additional numbers attached? ☐ Yes ☒ No5. Name and address of party to whom correspondence
concerning document should be mailed:

Name: Alan Israel, Esq.

Address: KIRSCHSTEIN, et al.

489 Fifth Avenue

New York, New York 10017

6. Total number of applications and patents involved: 10

7. Total Fee (37 C.F.R. 3.41).....\$400.00

☐ Enclosed☒ Authorized to be charged to deposit account

8. Deposit account number: 11-1145

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9. Statement and signature.

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true
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Alan Israel, Reg. No. 27,564

Name of Person Signing

Signature

April 30, 2007

Date

Total number of pages including cover sheet, attachments, and document: 11

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003

CONFIRMATORY PATENT ASSIGNMENT

This Confirmatory Patent Assignment Agreement, dated July 14, 2006, is made by Zarlink Semiconductor Limited, a corporation incorporated under the laws of England and Wales ("Assignor"), to Intel Corporation, a Delaware corporation ("Assignee").

RECITALS

A. Assignor and Assignee and other parties have entered into an asset purchase agreement dated as of October 7, 2005, (the "Agreement"). All capitalized terms used herein but not otherwise defined shall have the meanings set forth in the Agreement.

B. Pursuant to the Agreement, Assignor has previously assigned to the Assignee all of Assignor's right, title and interest in and to the patents set forth on Exhibit A hereto (the "Patents").

AGREEMENT

NOW, THEREFORE, in consideration of the foregoing premises, the mutual covenants and agreements contained in the Agreement and the covenants and agreements in this Assignment, Assignor hereby confirms as follows:

1. For good and valuable consideration, the receipt of which is hereby acknowledged, Assignor has, on October 7, 2005, pursuant to the terms of the Agreement, sold, transferred, conveyed, assigned and delivered to Assignee, with full title guarantee all of Assignor's right, title and interest in and to the Patents, including any foreign counterparts, divisions, continuations or reissues thereof, the same to be held by Assignee for Assignee's own use and enjoyment, and for the use and enjoyment of Assignee's successors, assigns and other legal representatives, as fully and entirely as the same would have been held and enjoyed by Assignor if this Assignment and sale had not been made; together with all claims for damages by reason of past infringements of the Patents, along with the right to sue for and collect such damages for the use and benefit of Assignee and its successors, assigns and other legal representatives.

2. Assignor hereby authorizes and requests the Assistant Commissioner of Patents of the United States, and any officer of any country or countries foreign to the United States, whose duty it is to issue patents or other evidence or forms of intellectual property protection or applications as aforesaid, to issue the same to Assignee and its successors, assigns and other legal representatives in accordance with the terms of this instrument.

Zarlink Confirmatory Patent Assignment

MTL_LAW #1824222 v. 2

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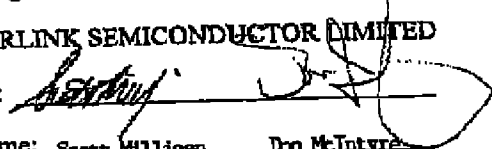
KIRSCHSTEIN OTTINGER

004

IN WITNESS WHEREOF, Assignor has executed this Assignment on the date
first above written.

Assignor:

ZARLINK SEMICONDUCTOR LIMITED

By: Name: Scott Milligan Don McIntyreTitle: Director Director

[SIGNATURE PAGE TO ZARLINK CONFIRMATORY PATENT ASSIGNMENT]

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005

Acknowledgment by Notary Public

State of N/ACounty of N/A

On this 14th day of July, 2006, before me, the undersigned Notary Public, personally appeared Donna Ingle and Scott Milligan personally known to me (or proved to me on the basis of satisfactory evidence) to be the person whose name is subscribed to the within instrument, and acknowledged to me that he or she executed the same.

Seal:

Signature: Name: Donatella Concina Notary Public
in and for the Province of Ontario, Canada

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006

EXHIBIT A TO CONFIRMATORY PATENT ASSIGNMENT

Patent Application Title	Inventors	Country	Filed	Serial #	Patent #	Issue Date
Ultra linear transistor	Nick Cowley Arshad Madni	UK	06/01/1990	8900344.4	2345600	30/07/2003
		USA	10/01/2000	09/479973	6278299	21/08/2001
IMPROVED ARCHITECTURE FOR ZERO IF TUNERS	Nick Cowley Mark Mudd	UK	04/06/1999	9912873.8	2350948	01/10/2003
Radio frequency amplifier	Arshad Madni Lance Tredd	US	05/07/2000	09/811720	6407832	18/06/2002
Improvements in or relating to FM demodulators	Nick Cowley	Canada	20/08/1986	516424	1254858	30/05/1989
Radiation prescalers	Nick Cowley	US	02/02/1987	19276	4862515	29/08/1989
FM receivers	Nick Cowley	US	29/05/1988	869908	4806872	21/02/1990
Radio receiver tuner circuit	Christopher Beale	UK	30/03/1988	8807804.7	2216354	16/09/1992
Tuner circuit including tuning voltage generator	Nick Cowley	UK	25/02/1988	8604652	2188502	08/11/1989
		US	24/02/1987	018216	4792989	20/12/1988
On-chip integrated oscillator circuits	Christopher Beale	UK	09/02/1989	8902870.8	2228154	21/04/1993
Improvements in or relating to FM demodulators	Nick Cowley	US	25/01/1989	301382	4953010	28/08/1990
Improvements in or relating to flip-flops	Nick Cowley Rod Lawton Thomas McClelland	UK	30/11/1987	8727953.5	2213006	20/01/1992
		US	21/11/1990	07/616843	5200650	06/04/1993
Frequency doubling oscillator and heterodyne circuit incorporating same	Nick Cowley Rod Lawton Thomas McClelland	UK	22/10/1985	8525985.1	2187911	20/09/1989
		US	13/08/1987	060708	4810976	07/03/1989
Drive circuit	Ian R MacDonald	US	02/11/1994	08/333264	5469093	21/11/1995
Integrated circuit oscillators	Rod Lawton	UK	11/11/1994	9422822.8	2284315	10/12/1997
		US	22/11/1994	08/343708	5469116	21/11/1995
Phase lock detector	Mark Mudd	UK	28/02/1995	95301287.8	0673121	14/01/1998
Amplifier circuit arrangements	Arshad Madni Nick Cowley Ian Watson	UK	09/12/1994	9524328.8	2295934	14/04/1999
		US	04/12/1995	08/568859	5684433	04/11/1997

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007

Patent Application Title	Inventors	Country	Filed	Serial #	Patent #	Issue Date
Frequency synthesizer	Peter Saul	UK	01/06/1997	8708843.7	2324917	14/03/2001
		US	22/04/1998	09/064278	6090817	08/06/2000
Improvements in or relating to demodulator alignment correction in tuners	Nick Cowley	UK	09/12/1998	9826834.2	2344710	15/05/2002
		US	09/12/1999	09/454542	6567486	20/06/2003
Improved Architecture for Digital Cable Tuners	Nick Cowley	US	11/09/2000	09/658746	6901248	5/31/06
Digital Receiver	Nick Cowley Alison Payne Mark Dawkins	Europe - UK	06/09/2000	00307600.3	1085666	13/07/2006
New Architectural Arrangement for Digital Tuners	Nick Cowley Alison Payne Mark Dawkins	US	22/08/2001	09/935028	6937870	30/08/2005
Architectural arrangements for universal wired/wireless modem applications	Nick Cowley	UK	18/10/2001	0125125.5	2372162	21/07/2004
A Novel Method for Improving the Intermodulation Performance of Radio Receivers	Nick Cowley	US	22/08/2001	09/934610	6714263	30/03/2004
FEC AGC Architecture	Arshad Madni Mark Mudd	US	24/01/2002	10/058155	6878843	05/04/2006
		UK	08/01/2002	02250099.5	1231709	12/11/2003
Mixer with emitter follower driven LNA	Arshad Madni Franco Lauria Mark Mudd Lance Trodd Nick Cowley	US	22/08/2002	10/225790	6727748	27/04/2004
A Novel CMOS VCO Control	Richard Albon David Johnston	US	20/08/2002	10/223869	6883509	27/01/2004
Tuner Arrangement and Set Top Box	Nick Cowley Theodore Aaron	UK	25/11/2002	02102834.9	1317073	12/01/2005
		Germany	25/11/2002	02102834.9	1317073	12/01/2005
A Novel method for improving the intermodulation performance of radio receivers	Lance Trodd Franco Lauria	US	28/04/2003	10/424899	6791414	14/08/2004
A method for power and gain distribution optimization in digital terrestrial tuners	Nick Cowley Keith Jones	UK	28/02/2003	0304584.8	2398943	31/08/2005
Patent Title	Inventors	Country	Filed	Serial #	Patent #	Issue Date
QAM Demodulator Synchronisation Algorithm	Bernard Arambaccio	US	13/03/2001	09/894693	6878848	12/04/2005

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008

Patent Title	Inventors	Country	Filed	Serial #	Patent #	Issue Date
Impulse Correction Arrangements for DTT Systems	Nick Cowley Alison Payne M. Davids	UK	06/08/2001	01308712.9	1180851	22/10/2003
Method for optimising the analogue filter bandwidth in OFDM receiver	Esmerald Arambapola Nick Cowley	UK	02/11/2003	0225598.2	2304847	07/08/2005
A Novel Implementation for a Digital Television Broadcast Receiver	Scott Cuthbertson Richard Crossley Malcolm Lomer	Germany	11/05/2004	20 2004 007 528.1	20 2004 007 528.1	22/07/2004
Patent Title	Inventors	Country	Filed	Serial #	Patent #	Issue Date
Class AB amplifier with improved phase margin	S Franck	US	18/08/1993	08/155584	5412344	02/05/1995
Variable transconductance amplifier	Arshad Madni	UK	10/06/1999	9913423.1	2334163	21/02/2001
		US	08/06/2000	08/590330	6304142	18/10/2001
Analogue to digital converters	Ian Dedic	UK	02/06/1989	8912779.9	2218453	20/06/1992
Delta sigma modulator arrangements	Ian Dedic Alexander Vogt	UK	04/08/1989	8917927.9	2222330	15/07/1992
		US	03/08/1988	388368	4994806	19/02/1991
Analogue to digital converters	Ian Dedic Alexander Vogt	UK	12/09/1989	8920647.8	2223137	15/07/1992
Analogue to digital converter	Peter Saul	UK	30/01/1987	87901078.3	258076	28/03/1990
		US	01/01/1981	110763	4982193	01/01/1991
Analogue-to-digital converters	Andrew Joy Thomas Leslie	UK	18/08/1988	8819838.1	2223369	18/11/1992
Digital phase locked loop	Bhajan Singh Vincent Conaldine	UK	02/03/1988	8804974	2215539	29/04/1992
		US	15/07/1991	729315	5193103	09/03/1993
Analogue to digital converter	David Mapleston	UK	03/02/1986	8602568.9	2188135	08/09/1989
		US	23/01/1987	921450	4774499	27/09/1988
Amplifier arrangements	Arshad Madni	UK	10/08/1993	9417493.5	2281832	27/08/1997
		US	26/08/1994	08/295020	5572165	05/11/1996
Overvoltage protection circuit	Mark Mudd Ross Addinall	UK	28/09/1994	94307100.1	851490	10/06/1996
		US	22/10/1996	08/324814	5568345	22/10/1996
Improvements in or related to analogue to digital converters	Thomas Jackson	UK	21/02/1986	8604336	2171589	27/07/1988
		US	21/02/1986	925634	4738188	05/04/1988
Amplifiers	Arshad Madni	Europe	08/11/2000	00309862.1		
		US	02/11/2000	09/705499	6480066	12/11/2002

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009

Patent Title	Inventors	Country	Filed	Serial #	Patent #	Issue Date
Radio frequency amplifier and tuner	Arshad Madni	Europe	01/05/2001	1303899.5		
		US	10/05/2001	08/832600	8538501	25/03/2003
BES FECLNA	Arshad Madni	UK	12/03/2002	205888.2	2388608	12/03/2002
		US	07/03/2003	10/383712	6747512	08/09/2004
Patent Title	Inventors	Country	Filed	Serial #	Patent #	Issue Date
Amplifier circuit arrangement	Sieva Souetinov Peter Laws	US	08/03/2000	08/628632	8842787	04/11/2003
A frequency synthesiser	David A. Johnson	UK	22/09/1989	892144.9	2233844	05/05/1993
Phase detector	Ian Fobbeater David Clarke	UK	22/08/1989	8919089.8	2235839	18/08/1993
		US	10/08/1990	585085	5157341	20/10/1992
Mixer for direct conversion receiver	Peter Laws	UK	19/07/1991	9115618.2	2248879	30/03/1994
		US	06/08/1991	91/740892	5303417	12/04/1994
Charge pump circuit	Ian Fobbeater Peter Good	UK	31/10/1990	9023871.2	2249443	08/06/1994
Improvements in true logarithmic amplifiers	Peter Chadwick	US	08/03/1991	07/866359	5159280	27/10/1992
Integrated circuit amplifier arrangements	Peter Laws	UK	12/01/1994	9400454.8	2275382	13/11/1996
		US	08/08/1995	191281	5440271	08/08/1995
Logarithmic detector	Ian Watson	UK	27/08/1993	9317848.1	2281425	29/10/1997
		US	23/08/1994	294550	5467093	14/11/1996
Modulators	Peter Chadwick	UK	28/08/1993	9318854.5	2282284	21/01/1998
		US	24/08/1994	08/295017	5493257	20/02/1998
Automatic frequency control arrangement	David Mepleson	Japan	29/07/1994	198237/94	3493458	21/11/2003
		UK	08/08/1993	9318383.2	2280801	10/12/1997
		US	03/08/1994	08/285407	5530723	25/08/1998
Detectors	Marcus Granger-Jones	China	16/07/1994	94108397.7	ZL94108397.7	15/05/2002
		UK	16/07/1993	9314841.9	2280324	22/07/1998
		US	11/07/1994	08/272839	5612876	18/03/1997
Equalisation arrangement	Peter Chadwick	UK	16/07/1993	9314851.8	2280800	12/11/1997
		US	30/06/1994	08/289074	5557840	17/09/1998
Signal strength detector	Phil Moon	Canada	20/08/1984	461363	1243354	18/10/1988
Bias circuit	Phil Moon	US	05/02/1987	011098	4783018	09/09/1988
Bias current circuit, and cascade and ring circuits incorporating same	Phil Moon Simon Atkinson	US	05/02/1987	010930	4833344	23/05/1988

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010

Patent Title	Inventors	Country	Filed	Serial #	Patent #	Issue Date
Improvements in or relating to integrated circuits	Peter Laws	US	27/02/1987	018801	4808880	21/02/1989
Clock driven data sampling circuit	Peter Laws Graham Fletcher	UK	29/03/1988	8807402.8	2222751	26/08/1992
		US	10/07/1990	328718	4840848	10/07/1990
Broad band VCO control system for clock recovery	Peter Laws Graham Fletcher	UK	28/03/1988	8807320.0	2223138	14/10/1992
		US	28/03/1989	328719	4843788	24/07/1990
Manchester clock code and data recovery system	Peter Laws	UK	11/02/1988	8803139.8	2202716	06/02/1991
		US	18/02/1988	166486	4853943	01/08/1989
Improved transconductors and voltage gain devices incorporating same	Simon Atkinson	UK	18/09/1988	8822394	2195211	10/01/1990
A phase shift arrangement	Paul Denny	US	18/09/1988	245154	4922127	01/05/1990
Electrical circuits	Simon Atkinson Frank Carr	UK	17/07/1987	8718884.5	2208340	22/01/1992
		US	17/05/1989	335087	4947141	07/08/1990
Transconductance floating resistor circuit	Peter Laws	UK	28/01/1994	9401718.3	2288090	11/11/1996
Direct conversion receivers	Red Lawton	China	14/09/1994	94115885.3	94115885.3	24/09/1997
		UK	14/09/1993	9318981.9	2282030	24/09/1997
		US	08/09/1994	08/302510	5451889	19/09/1995
D.C. restoration circuit	Ian Fobbeater	Japan	25/10/1994	204226/94	3500442	12/12/2003
		US	24/12/1996	324811	5567681	24/12/1996
Control arrangements for digital radio receivers	Ian Fobbeater	UK	28/10/1994	9421409.8	2283871	02/09/1997
		US	18/10/1994	324910	5683989	02/09/1997
Voltage offset compensation circuit	Keith Jones	UK	21/02/1995	9503425.2	2298329	16/02/2000
		US	01/03/1996	604876	5812023	22/09/1998
Controllable filter arrangement	Colin Perry Marcus Granger-Jones	US	01/03/1996	08/604494	5760641	02/06/1998
Integrated circuit output buffer	David Wilcox	UK	08/12/1995	9524967.9	2308027	10/05/2000
		US	26/11/1996	08/756885	5982207	09/11/1999
Amplifier and loop filter arrangement for a PLL using a charge pump	David Clarke Ian Fobbeater	UK	25/04/1996	9603622.8	2312578	05/07/2000
		US	31/03/1997	08/828719	5874862	23/02/1999
Low Noise Amplifier	Slava Souel'nov	UK	11/01/1997	9700487.3	2321150	11/12/2000
		US	07/01/1998	09/003909	5945880	31/08/1999

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