

PATENT ASSIGNMENT

Electronic Version v1.1

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
Soo Hyun KIM	06/29/2007
Baek Mann KIM	06/29/2007
Young Jin LEE	06/29/2007
Sun Woo HWANG	06/29/2007
Dong Ha JUNG	06/29/2007
Jeong Tae KIM	06/29/2007
RECEIVING PARTY DATA	
Name:	HYNIX SEMICONDUCTOR INC.
Street Address:	San 136-1, Ami-ri, Bubal-eub, Icheon-si
City:	Kyoungki-do
State/Country:	KOREA, REPUBLIC OF
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	11776905
CORRESPONDENCE DATA	
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ATTORNEY DOCKET NUMBER:	CU-5767 WWP/DS
NAME OF SUBMITTER:	Woochoon W. Park

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REEL: 019551 FRAME: 0035

Total Attachments: 2

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PATENT

REEL: 019551 FRAME: 0036

UNITED STATES OF AMERICA
ASSIGNMENT

In consideration of the payment of ASSIGNEE to ASSIGNOR of the sum of One Dollar (\$1.00), the receipt of which is hereby acknowledged, and for the other good and valuable consideration,

ASSIGNOR:

Name : Soo Hyun KIM

Address : 103-1105, Hyundai Apt., Wolgye-dong, Nowon-gu, Seoul, Korea

Name : Baek Mann KIM

Address : 441-1503, Hyundai Home Town 4-cha Apts. 3, Jukjeon 1-dong, Suji-gu, Yongin-si, Kyongki-do, Korea

Name : Young Jin LEE

Address : 103-502, Sinil Utovill Apt., Sangha-dong, Giheung-gu, Yongin-si, Kyongki-do, Korea

Name : Sun Woo HWANG

Address : 103-314, Godam Dormitory, 72-1, Godam-dong, Icheon-si, Kyongki-do, Korea

Name : Dong Ha JUNG

Address : Morningvill, 13-86, Seohyeon-dong, Bundang-gu, Seongnam-si, Kyongki-do, Korea

Name : Jeong Tae KIM

Address : 405-102, 401 Kkachi-maeul Lotte Apt., Gumi-dong, Bundang-gu, Seongnam-si, Kyongki-do, Korea

(INVENTOR NAME AND ADDRESS)

hereby sells, assigns and transfers to

ASSIGNEE:

Name : Hynix Semiconductor Inc.

Address : San 136-1, Ami-ri, Bubal-eub, Icheon-si, Kyongki-do, Korea

(ASSIGNEE NAME AND ADDRESS)

and the successors, assigns and legal representatives of the ASSIGNEE the entire right, title and interest for the United States and its territorial possessions in and to, any and all improvements which are disclosed in the invention entitled:

METHOD FOR MANUFACTURING SEMICONDUCTOR DEVICE CAPABLE OF REDUCING PARASTIC BIT LINE CAPACITANCE

(TITLE)

and which is found in *(check one applicable item below)*

☒ U.S. patent application executed on even date herewith

☐ U.S. Application Serial No. _____ filed on _____

and, in and to all Letters Patent to be obtained for said invention by the above application or any continuation, division, renewal, substitute, reissue or re-examination thereof.

ASSIGNOR hereby covenants that no assignment, sale agreement or encumbrance has been or will be made or entered into which would conflict with this assignment and sale;

ASSIGNOR further covenants that ASSIGNEE will, upon its request, be provided promptly with all pertinent facts and documents relating to said invention and said Letters Patent and legal equivalents as may be known and accessible to ASSIGNOR and will testify as to the same in any interference or litigation related thereto and will promptly execute and deliver to ASSIGNEE or its legal representatives any and all papers, instruments or affidavits required to apply for, obtain, maintain, issue and enforce said application, said invention and said Letters Patent and said equivalents thereof which may be necessary or desirable to carry out the purposes thereof.

IN WITNESS WHEREOF, I/we have set our hand and seal

June 29, 2007

Date

S. Kim

INVENTOR: Soo Hyun KIM

June 29, 2007

Date

B. M. Kim

INVENTOR: Baek Mann KIM

June 29, 2007

Date

Y. J. Lee

INVENTOR: Young Jin LEE

June 29, 2007

Date

S. W. Hwang

INVENTOR: Sun Woo HWANG

June 29, 2007

Date

Jung, Dong Ha

INVENTOR: Dong Ha JUNG

June 29, 2007

Date

Kim Jeong Tae

INVENTOR: Jeong Tae KIM

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