

PATENT ASSIGNMENT

Electronic Version v1.1

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SUBMISSION TYPE:

NEW ASSIGNMENT

NATURE OF CONVEYANCE:

LICENSE

CONVEYING PARTY DATA

Name	Execution Date
Intellectual Ventures Holding 19, LLC	04/13/2006

RECEIVING PARTY DATA

Name:	Polytechnic University
Street Address:	502 East John Street
City:	Carson City
State/Country:	NEVADA
Postal Code:	89706

PROPERTY NUMBERS Total: 50

Property Type	Number
Patent Number:	6389031
Patent Number:	6370144
Patent Number:	6081507
Patent Number:	6487213
Patent Number:	6667984
Patent Number:	6449283
Patent Number:	7006514
Patent Number:	6940851
Patent Number:	6599442
Patent Number:	5750049
Patent Number:	RE37658
Patent Number:	5604413
Patent Number:	5742145
Patent Number:	6154000
Patent Number:	6405533

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Patent Number:	6316899
Patent Number:	5970393
Patent Number:	6980524
Patent Number:	7173931
Patent Number:	7103056
Patent Number:	7046661
Patent Number:	7058362
Application Number:	10780139
Application Number:	10285332
Application Number:	10776574
Application Number:	10776575
Application Number:	10872332
Application Number:	10872187
Application Number:	11004260
Application Number:	09941072
Application Number:	10081880
Application Number:	09573766
Application Number:	11246030
Application Number:	10155832
Application Number:	60641855
Application Number:	60646080
Application Number:	60270399
Application Number:	60293028
Application Number:	60335503
Application Number:	60386359
Application Number:	60479733
Application Number:	60553361
Application Number:	60064867
Application Number:	60085672
Application Number:	60070404
Application Number:	60186056
Application Number:	60194374
Application Number:	60252006
Application Number:	60253335
Application Number:	60294877

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REEL: 019562 FRAME: 0645

CORRESPONDENCE DATA

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IV190001

NAME OF SUBMITTER:

Linda Garrett

Total Attachments: 10

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PATENT LICENSE AGREEMENT

This PATENT LICENSE AGREEMENT ("*Agreement*") is entered into on JUNE 13 ("*Effective Date*") by and among Intellectual Ventures Holding 19, LLC, a Nevada limited liability company with offices at 1117 Desert Lane, Suite 1433 Las Vegas, NV 89102 ("*Licensee*") and Polytechnic University, a Non-Profit Research Institution with offices at Six MetroTech Center, Brooklyn, NY 11201 ("*Licensor*").

The parties hereby agree as follows:

2. DEFINITIONS

"Patents" means (i) each of the patents and/or applications identified on Exhibits A and B, respectively, (ii) all patents or patent applications to which any of the foregoing claim priority; (iii) and all reissues, reexaminations, extensions, continuations, continuations in part, continuing prosecution applications, and divisions of such patents and applications; (iv) foreign counterparts to any of the foregoing, including without limitation utility models, and (v) any of the foregoing in (ii)-(iv) whether or not expressly listed in Exhibits A or B and whether or not abandoned, rejected, or the like.

3. GRANT OF EXCLUSIVE LICENSE

- 3.1 Patent License. Subject to the terms and conditions of this Agreement, Licensor hereby grants to Licensee an exclusive, worldwide license of all rights of any kind conferred by the Patents, including, without limitation, the rights to make, use, offer to sell, sell or import any products covered by the Patents.
- 3.2 Sublicenses. The exclusive license granted to Licensee under the Patents shall include the right to grant and authorize sublicenses subject to the terms and conditions of this Agreement.

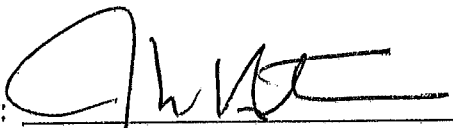
3.7 Term and Termination. The term of this Agreement (the "Term") hereunder shall become effective on the Effective Date and shall continue in effect until the last Patent expires.

- 4.3 Conduct of Prosecution. The conduct of the filing, prosecution, maintenance or defense of Patents shall be under the Licensee's sole control and discretion, provided, that Licensee shall consult with Licensor on such matters from time to time upon the reasonable request of Licensor.

IN WITNESS WHEREOF, the parties have duly executed this Agreement the day and year set forth above.

LICENSOR

Polytechnic University

By: _____

Its: Managing Director

Date: 4/13/06

LICENSEE

Intellectual Ventures Holding 19, LLC

By: _____

Its: Authorized Person

Date: 6/13/06

EXHIBIT A: EXISTING ISSUED PATENTS

Patent Number	Country	Date Filed	Title	Inventors
6,389,031	U.S.A.	11/4/1998	Methods and Apparatus for Fairly Scheduling Queued Packets Using a RAM-based Search Engine	Chao, Jonathan; Jenq, Yau-Ren
6,370,144	U.S.A.	11/4/1998	Methods and Apparatus for Shaping Queued Packets Using a Two-dimensional RAM-based Search Engine	Chao, Jonathan; Jenq, Yau-Ren
6,081,507	U.S.A.	11/4/1998	Methods and Apparatus for Handling Time Stamp Aging	Chao, Jonathan; Guo, Xiaolei
6,487,213	U.S.A.	1/4/1999	Methods and Apparatus for Fairly Arbitrating Contention for an Output Port	Chao, Jonathan
6,667,984	U.S.A.	5/14/1999	Methods and Apparatus for Arbitrating Output Port Contention in a Switch Having Virtual Output Queuing	Chao, Jonathan; Park, Jin-Soo
6,449,283	U.S.A.	5/14/1999	Methods and Apparatus for Providing a Fast Ring Reservation Arbitration	Chao, Jonathan; Altinordu, Alper
7,006,514	U.S.A.	6/1/2001	Pipelined Maximal-Sized Matching Cell Dispatch Scheduling	Chao, Jonathan
6,940,851	U.S.A.	7/23/2001	Scheduling the Dispatch of Cells in Non-Empty Virtual Output Queues of Multistage Switches Using a Pipelined Arbitration Scheme	Chao, Jonathan; Oki, Eiji; Rojas-Cessa, Roberto
6,599,442	U.S.A.	2/26/2001	Temperature Measurement and Temperature Controlled Switching Based on Helical Sense Dependent Liquid Crystal Phases	Green, Mark
5,750,049	U.S.A.	1/16/1996	Chiral Optical Polymer Based Information Storage Material	Green, Mark
RE37,658	U.S.A.	5/9/2000	Chiral Optical Polymer Based Information Storage Material	Green, Mark

Patent Number	Country	Date Filed	Title	Inventors
5,604,413	U.S.A.	9/7/1994	Apparatus for Improving Operational Performance of a Machine or Device	Khorrami, Farshad; Rastegar, Jahangir
5,742,145	U.S.A.	12/1/1995	Apparatus for Reducing Vibration Inputs to a Device and/or for Positioning the Device	Khorrami, Farshad; Rastegar, Jahangir
6,154,000	U.S.A.	4/13/1998	Apparatus for Reducing Vibration Inputs to a Device and/or for Micropositioning	Khorrami, Farshad; Rastegar, Jahangir
6,405,533	U.S.A.	1/10/2000	Apparatus for Reducing Vibration Inputs to a Device and/or for Micropositioning	Khorrami, Farshad; Rastegar, Jahangir
6,316,899	U.S.A.	7/27/2000	Apparatus for Reducing Vibration Inputs to a Device and/or for Micropositioning	Khorrami, Farshad; Rastegar, Jahangir
5,970,393	U.S.A.	10/18/1999	Integrated Micro-strip Antenna Apparatus and a System Utilizing the Same for Wireless Communications for Sensing and Actuation Purposes	Khorrami, Farshad
6,980,524	U.S.A.	5/22/2000	Methods and Apparatus for Routing in a Mobile Ad Hoc Network	Lu, I-Tai; Joa-Ng, Mario

EXHIBIT B: PENDING PATENT APPLICATIONS**PROVISIONAL PATENT APPLICATIONS**

Application Serial Number	Country	Date Filed	Title	Inventors
60/641,855	U.S.A.	1/6/2005	Invariance Based Concurrent Error Detection for the Advanced Encryption Standard	Karri, Ramesh; Joshi, Nikhil
60/646,080	U.S.A.	1/21/2005	On-Demand P2P Video Streaming with Multiple Description Coding	Panwar, Shivendra S.; Ross, Keith; Wang, Yao
60/270,399 (expired)	U.S.A.	2/21/2001	Hardware Implementation of a Signaling Protocol	Karri, Ramesh; Veeraraghavan, Malathi; Douglas, Brian; Wang, Haobo
60/293,028 (expired)	U.S.A.	5/23/2001	Preliminary Specification and Explanation of Zing: An End-To-End Protocol For Transporting Bulk Data Over (Virtual) Circuits	Veeraraghavan, Malathi; Moors, Tiimothy
60/335,503 (expired)	U.S.A.	10/31/2001	Dual Round Robin Matching Switch with Exhaustive Service	Chao, Jonathan; Li, Yihan; Panwar, Shivendra
60/386,359 (expired)	U.S.A.	6/6/2002	Dual Round Robin Matching Switch with Exhaustive Service	Chao, Jonathan; Li, Yihan; Panwar, Shivendra
60/479,733 (expired)	U.S.A.	6/19/2003	Highly scalable multi-plane multi-stage buffered packet switch	Chao, Jonathan; Park, Jinsoo
60/553,361 (expired)	U.S.A.	3/15/2004	Maintaining Packet Sequence Integrity in a Buffered Multi-Path Packet Switch Using a Window Flow Control Scheme	Chao, Jonathan; Park, Jinsoo
60/064,867 (expired)	U.S.A.	11/5/1997	A Delay-Bound Guarantee Packet Scheduler Using a RAM-Based Searching Engine	Chao, Jonathan; Jenq, Yau-Ren
60/085,672 (expired)	U.S.A.	5/15/1998	Multicast Crosspoint Switching Architecture With Tunneling Ring Reservation	Chao, Jonathan; Altinordu, Alper
60/070,404 (expired)	U.S.A.	1/5/1998	Fast Hierarchical Arbitration Scheme for Multicast Packet Switches	Chao, Jonathan
60/186,056 (expired)	U.S.A.	2/29/2000	Method for Temperature Measurement Using Chirality	Green, Mark

Application Serial Number	Country	Date Filed	Title	Inventors
60/194,374 (expired)	U.S.A.	4/4/2000	Method for Temperature Measurement Using Chirality	Green, Mark
60/252,006 (expired)	U.S.A.	11/20/2000	"Crrd: A Concurrent Round-Robin Dispatching Scheme for Clos-Network Switches"	Oki, Eiji; Chao, Jonathan
60/253,335 (expired)	U.S.A.	11/27/2000	Scalable Round-Robin-Based Dispatching Scheme for Large-Scale Clos-Network Switches	Oki, Eiji; Chao, Jonathan
60/294,877 (expired)	U.S.A.	5/31/2001	PMM a Pipelined Maximal-Sized Matching Scheduling Approach for Input-Buffered Switches	Chao, Jonathan; Oki, Eiji; Rojas-Cessa, Roberto

Application Serial Number	Country	Date Filed	Title	Inventors
10/780,139	U.S.A.	2/17/2004	Locating an Object of Interest Using Back Tracked-Rays Derived From Multipath Signals	Bertoni, Henry; Sugla, Binay
09/851,461 7,173,931	U.S.A.	5/8/2001	Scheduling the Dispatch of Cells in Multistage Switches	Chao, Jonathan; Oki, Eiji
09/872,892 7,103,056	U.S.A.	6/1/2001	Scheduling the Dispatch of Cells in Multistage Switches Using a Hierarchical Arbitration Scheme for Matching Non-Empty Virtual Output Queues of a Module with Outgoing Links of the Module	Chao, Jonathan; Oki, Eiji
09/911,038 7,046,661	U.S.A.	7/23/2001	Scheduling the Dispatch of Cells in Non-Empty Virtual Output Queues of Multistage Switches Using a Pipelined Hierarchical Arbitration Scheme	Chao, Jonathan; Oki, Eiji
10/285,332	U.S.A.	10/31/2002	Arbitration Using Dual Round Robin Matching with Exhaustive Service of Winning Virtual Output Queue	Chao, Jonathan; Li, Yihan; Panwar, Shivendra
10/776,574	U.S.A.	2/11/2004	Packet Sequence Maintenance with Load Balancing, and Head-of-Line blocking Avoidance in a Switch	Chao, Jonathan; Park, Jinsoo

Application Serial Number	Country	Date Filed	Title	Inventors
10/776,575	U.S.A.	2/11/2004	Switch Module Memory Structure and Perdestination Queue Flow Control for Use in a Switch	Chao, Jonathan; Park, Jinsoo
10/872,332	U.S.A.	6/18/2004	Packet Reassembly and Deadlock Avoidance for Use in a Packet Switch	Chao, Jonathan; Park, Jinsoo
10/872,187	U.S.A.	6/18/2004	Packet-Level Multicasting	Chao, Jonathan
11/004,260	U.S.A.	12/3/2004	Maintaining Packet Sequence Using Cell Flow Control	Chao, Jonathan; Park, Jinsoo
09/941,072	U.S.A.	8/28/2001	Tracking Files of Storage Media and Enabling Users to Quickly Associate Such Files with the Storage Media on which they are Stored	Goodman, David; Chang, Jason; Mehta, Pratik
10/081,880	U.S.A.	2/21/2002	Signaling for Reserving a Communications Path	Karri, Ramesh; Veeraraghavan, Malathi; Douglas, Brian
09/407,184 7,058,362	U.S.A.	9/27/1999	Integrated Micro-strip Antenna Apparatus and a System Utilizing the Same for Wireless Communications for Sensing and Actuation Purposes	Khorrami, Farshad
09/573,766	U.S.A.	5/18/2000	Methods and Apparatus for Synchronizing and/or Determining the Location of Nodes	Lu, I-Tai; Joa-Ng, Mario
11/246,030	U.S.A.	10/7/2005	Cooperative Wireless Communications, such as Cooperative Medium Access Control For Example	Panway, Shivendra S.; Liu, Pei; Tao, Zhifeng
10/155,832	U.S.A.	5/23/2002	Transferring Data Such As Files	Veeraraghavan, Malathi; Moors, Tiimothy

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