

PATENT ASSIGNMENT

Electronic Version v1.1
 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
Sarnoff Corporation	10/07/2005
RECEIVING PARTY DATA	
Name:	Transpacific IP, Ltd.
Street Address:	14Fl., No. 168, Tun Hwa Rd.
City:	Taipei
State/Country:	TAIWAN
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	11825328
CORRESPONDENCE DATA	
Fax Number:	(503)439-6558
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ATTORNEY DOCKET NUMBER:	112.P56021RE
NAME OF SUBMITTER:	Julianne Flynn

Total Attachments: 22
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ASSIGNMENT

This Assignment (the "Assignment") is made between Sarnoff Corporation, a New Jersey corporation ("Assignor") and Transpacific IP Ltd. ("Assignee").

WHEREAS, Assignor possesses a legal interest in, subject to certain disclosed pre-existing third-party rights: (1) patents and patent applications; (2) any and all continuing, divisional, renewal, substitute, reexamination, reissue and any other applications of the patents and patent applications; and (3) all rights of priority, as listed in Exhibit A (collectively the "Assigned Portfolio");

WHEREAS, Assignor and Assignee entered into a certain Sale Agreement dated September 30, 2005 (the "Agreement");

WHEREAS, the parties wish to ensure the assignment of the Assigned Portfolio such that Assignee shall acquire and thus hereby acquires the entire right, title and interest in and to said Assigned Portfolio, subject to the terms and conditions of the Agreement;

WHEREAS, it is desired that the assignment of the Assigned Portfolio be made a matter of record in the appropriate domestic and international patent offices; and

NOW THEREFORE for good and valuable consideration, receipt of which is hereby acknowledged, Assignor hereby sells, assigns and transfers to Assignee all of Assignor's worldwide right, title, and interest in and to the Assigned Portfolio, to be held and enjoyed by Assignee, for its own use and benefit, and by Assignee's successors and assigns for their own use and benefit, for the full duration of the terms for which patent right and letters patent may be granted in the U.S. or any other country, subject to the terms and conditions of the Agreement.

IN WITNESS WHEREOF, Assignor executes this Assignment and is made effective as of the date below. The person who signs this Assignment below represents that such person is fully authorized to sign the Assignment on behalf of Assignor.

SARNOFF CORPORATION, a New Jersey corporation

By: Robert J. Levin

Robert J. Levin

Vice President, General Counsel and Secretary

Effective Date: October 7, 2005

Exhibit A

10915, 10915R, 11426, 11426A, 11426AA, 11426B, 11522, 11522A, 12241, 12271, 12344,
12344A, 12356, 12356A, 12477, 12477A, 12577, 12577A, 12675, 12962, 13023, 13030, 13124,
13124A, 13124B, 13273, 13359, 13438, 13438A, 13438B, 13488, 13632, 13633, 13640, 13817,
13830, 13875, 14285, 14620, 14670, 14672, 14672A, 14707, 15042, 15159, 15237

Exhibit A - Transpacific IP Applications and Patents

Case No.	Title	Inventor(s)	Status	Application No.	Filing Date	Patent No.	Issue Date	Country
10915	Active Matrix Electroluminescent Display And Method Of Operation	Stewart, R. G.	Granted	06-500688	5/28/1993	3510248	1/9/2004	JP
10915	R Active Matrix Electroluminescent Display And Method Of Operation	Stewart, R. G.	Pending	08/447717	5/23/1995			US
11426	Active Matrix Electroluminescent Display Pixel And Methods Of Fabricating Same	Dolny, G. M. Hsueh, F. L. Ipri, A. C. Stewart, R. G.	Pending	08-508253	8/24/1995			JP
11426	Active Matrix Electroluminescent Display Pixel And Methods Of Fabricating Same	Dolny, G. M. Hsueh, F. L. Ipri, A. C. Stewart, R. G.	Granted	97-701166	2/24/1997	385378	5/14/2003	KR
11426	A Active Matrix Electroluminescent Display Pixel Element Having a Field Shield Means Between The Pixel And The Switch	Dolny, G. M. Hsueh, F. L. Ipri, A. C. Stewart, R. G.	Granted	08/710271	9/16/1996	5736752	4/7/1998	US

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Case No.	Title	Inventor(s)	Status	Application No.	Filing Date	Patent No.	Issue Date	Country
11426 AA	High Voltage Transistor	DoIny, G. M. Hsueh, F. L. Ipri, A. C. Stewart, R. G.	Granted	08/993495	12/18/1997	5932892	8/3/1999	US
11426 B	Switching Circuitry Layout For An Active Matrix Electroluminescent Display Pixel With Each Pixel Provided With The Transistors	Hsueh, F. L. Ipri, A. C.	Granted	09/087570	5/29/1998	6104041	8/15/2000	US
11522	Tessellated Electroluminescent Display Having A Multilayer Ceramic Substrate	Cherukuri, S. C. Onyshkevych, L. S. Salsman, K. E. Yocom, P. N. Prabhu, A. N.	Pending	09-501195	6/7/1998			JP
11522	Tessellated Electroluminescent Display Having A Multilayer Ceramic Substrate	Cherukuri, S. C. Onyshkevych, L. S. Salsman, K. E. Yocom, P. N. Prabhu, A. N.	Granted	97-708753	6/7/1996	470282	1/27/2005	KR

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Case No.	Title	Inventor(s)	Status	Application No.	Filing Date	Patent No.	Issue Date	Country
11522	Tessellated Electroluminescent Display Having A Multilayer Ceramic Substrate	Cherukuri, S. C. Onyshkevych, L. S. Salsman, K. E. Yocom, P. N. Prabhu, A. N.	Granted	08/472540	6/7/1995	5644327	7/1/1997	US
11522 A	Mounting Structure For A Tessellated Electronic Display Having A Multilayer Ceramic Structure And Tessellated Electronic Display	Cherukuri, S. C. Onyshkevych, L. S. Salsman, K. E. Yocom, P. N. Prabhu, A. N.	Granted	08/813085	3/7/1997	5880705	3/9/1999	US
12241	Active Matrix Organic Light Emitting Diode (AMOLED) Display Pixel Structure And Data Load/Illuminate Circuit Therefor	Stewart, R. G. Ipri, A. C.	Pending	10-102736	4/14/1998			JP
12241	Active Matrix Organic Light Emitting Diode (AMOLED) Display Pixel Structure And Data Load/Illuminate Circuit Therefor	Stewart, R. G. Ipri, A. C.	Pending	98-13187	4/14/1998			KR
12241	Active Matrix Organic Light Emitting Diode (AMOLED) Display Pixel Structure And Data Load/Illuminate Circuit Therefor	Stewart, R. G. Ipri, A. C.	Granted	08/834067	4/14/1997	5952789	9/14/1999	US

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Case No.	Title	Inventor(s)	Status	Application No.	Filing Date	Patent No.	Issue Date	Country
12271	Circuit And Method For Driving An Organic Light Emitting Diode (O-LED)	Dingwall, A. G.	Granted	08/632890	4/4/1997	5903246	5/11/1999	US
12344	Sealing Of Large Area Display Structures	Matthies, D. L. Atherton, J. H. Stewart, R. G. Shen, Z.	Granted	99803054.6	2/17/1999	99803054.6	12/22/2004	CN
12344	Sealing Of Large Area Display Structures	Matthies, D. L. Atherton, J. H. Stewart, R. G. Shen, Z.	Published	99908203.5	2/17/1999			EP
12344	Sealing Of Large Area Display Structures	Matthies, D. L. Atherton, J. H. Stewart, R. G. Shen, Z.	Pending	00-531872	2/17/1999			JP
12344	Sealing Of Large Area Display Structures	Matthies, D. L. Atherton, J. H. Stewart, R. G. Shen, Z.	Pending	00-7009021	2/17/1999			KR

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Case No.	Title	Inventor(s)	Status	Application No.	Filing Date	Patent No.	Issue Date	Country
12344	Sealing Of Large Area Display Structures	Matthies, D. L. Atherton, J. H. Stewart, R. G. Shen, Z.	Granted	09/250329	2/16/1999	6370019	4/9/2002	US
12344 A	Sealing Of Large Area Display Structures	Matthies, D. L.	Published	10056551.6	2/17/1999			CN
12356	Active Matrix Light Emitting Diode Pixel Structure And Concomitant Method	Kane, M. G. Atherton, J. H. Stewart, R. G. Cuomo, F. P.	Published	98307912.0	9/29/1998			EP
12356	Active Matrix Light Emitting Diode Pixel Structure And Concomitant Method	Kane, M. G. Atherton, J. H. Stewart, R. G. Cuomo, F. P.	Pending	10-311569	9/28/1998			JP
12356	Active Matrix Light Emitting Diode Pixel Structure And Concomitant Method	Kane, M. G. Atherton, J. H. Stewart, R. G. Cuomo, F. P.	Granted	09/161862	9/28/1998	6229508	5/8/2001	US

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Case No.	Title	Inventor(s)	Status	Application No.	Filing Date	Patent No.	Issue Date	Country
12356 A	Active Matrix Light Emitting Diode Pixel Structure And Concomitant Method	Kane, M. G. Atherton, J. H. Stewart, R. G. Cuomo, F. P.	Granted	09793933	2/27/2001	6618030	9/9/2003	US
12477	Active Matrix Light Emitting Diode Pixel Structure And Concomitant Method	Dawson, R. M. A. Hsu, J. Y. Kane, M. G. Hsueh, F. L. Ipri, A. C. Stewart, R. G.	Pending	10-546378	4/23/1998			JP
12477	Active Matrix Light Emitting Diode Pixel Structure And Concomitant Method	Dawson, R. M. A. Hsu, J. Y. Kane, M. G. Hsueh, F. L. Ipri, A. C. Stewart, R. G.	Allowed	99-7009657	4/23/1998			KR
12477	Active Matrix Light Emitting Diode Pixel Structure And Concomitant Method	Dawson, R. M. A. Kane, M. G. Hsu, J. Y. Hsueh, F. L. Ipri, A. C. Stewart, R. G.	Granted	09/064696	4/22/1998	6229508	6/6/2001	US

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Case No.	Title	Inventor(s)	Status	Application No.	Filing Date	Patent No.	Issue Date	Country
12477 A	Active Matrix Organic Light Emitting Diode (AMOLED) Pixel Structure With Voltage-To-Current Conversion Transformed In The Pixel	Dawson, R. M. A. Hsu, J. Y Kane, M. G.	Pending	2005-7012271	6/29/2005			KR
12577	Tiled Electronic Display Structure	Matthies, D. L. Stewart, R. G. Atherton, J. H. Busta, H. H. Shen, Z. Bechis, D. J.	Granted	99803049.X	2/17/1999	99803049.X	5/26/2004	CN
12577	Tiled Electronic Display Structure	Matthies, D. L. Stewart, R. G. Atherton, J. H. Busta, H. H. Shen, Z. Bechis, D. J.	Published	99932532.7	2/17/1999			EP
12577	Tiled Electronic Display Structure	Matthies, D. L. Stewart, R. G. Atherton, J. H. Busta, H. H. Shen, Z. Bechis, D. J.	Pending	00-531834	2/17/1999			JP

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Case No.	Title	Inventor(s)	Status	Application No.	Filing Date	Patent No.	Issue Date	Country
12577	Tiled Electronic Display Structure	Matthies, D. L. Stewart, R. G. Atherton, J. H. Busta, H. H. Shen, Z. Bechis, D. J.	Pending	00-7009022	2/17/1999			KR
12577	Tiled Electronic Display Structure	Matthies, D. L. Stewart, R. G. Atherton, J. H. Busta, H. H. Shen, Z. Bechis, D. J.	Granted	09/250324	2/16/1999	6897855	5/24/2005	US
12577 A	Tiled Electronic Display Structure	Matthies, D. L. Stewart, R. G. Atherton, J. H. Bechis, D. J. Busta, H. H. Shen, Z.	Published	10/956661	10/12/2004			US

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Case No.	Title	Inventor(s)	Status	Application No.	Filing Date	Patent No.	Issue Date	Country
12875	Modular Display System	Firester, A. H. Bechtis, D. J. Burstyn, H. C. McGinn, J. T. Singh, B. Carlin, D. B.	Pending	00-523812	12/2/1998			JP
12875	Modular Display System	Firester, A. H. Carlin, D. B. Burstyn, H. C. Bechtis, D. J. Singh, B. McGinn, J. T.	Granted	09/200081	11/25/1998	6611241	8/28/2003	US
12962	Line Scanning Circuit For A Dual-Mode Display	Atherton, J. H. Connor, S. J. Dawson, R. M. A. Ipri, A. C. Shen, Z. Stewart, R. G.	Published	99967064.9	9/2/1999			EP

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Case No.	Title	Inventor(s)	Status	Application No.	Filing Date	Patent No.	Issue Date	Country
12962	Line Scanning Circuit For A Dual-Mode Display	Atherton, J. H. Connor, S. J. Dawson, R. M. A. Ipri, A. C. Shen, Z. Stewart, R. G.	Pending	00-572886	9/2/1999			JP
12962	Line Scanning Circuit For A Dual-Mode Display	Atherton, J. H. Connor, S. J. Dawson, R. M. A. Ipri, A. C. Shen, Z. Stewart, R. G.	Pending	01-7002837	9/2/1999			KR
12962	Line Scanning Circuit For A Dual-Mode Display	Atherton, J. H. Connor, S. J. Dawson, R. M. A. Ipri, A. C. Shen, Z. Stewart, R. G.	Granted	088115220	9/14/1999	NI-223828	11/11/2004	TW

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Case No.	Title	Inventor(s)	Status	Application No.	Filing Date	Patent No.	Issue Date	Country
12962	Line Scrambling Circuit For A Dual-Mode Display	Atherton, J. H. Connor, S. J. Dawson, R. M. A. Ipri, A. C. Shen, Z. Stewart, R. G.	Granted	09/384063	8/28/1999	6348906	2/19/2002	US
13023	Contrast Enhancement For An Electronic Display Device	Matthies, D. L. Atherton, J. H. Stewart, R. G. Shen, Z.	Granted	99803050.3	2/17/1999	99603050.3	6/23/2004	CN
13023	Contrast Enhancement For An Electronic Display Device	Matthies, D. L. Atherton, J. H. Stewart, R. G. Shen, Z.	Published	99908197.9	2/17/1999			EP
13023	Contrast Enhancement For An Electronic Display Device	Matthies, D. L. Stewart, R. G. Atherton, J. H. Shen, Z.	Pending	00-531673	2/17/1999			JP

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Case No.	Title	Inventor(s)	Status	Application No.	Filing Date	Patent No.	Issue Date	Country
13023	Contrast Enhancement For An Electronic Display Device	Matthies, D. L. Stewart, R. G. Atherton, J. H. Shen, Z.	Pending	00-7009023	2/17/1999			KR
13023	Contrast Enhancement For An Electronic Display Device By Using A Black Matrix And Lens Array On Outer Surface Of Display	Matthies, D. L. Stewart, R. G. Shen, Z. Atherton, J. H.	Granted	09/250347	2/16/1999	6476783	11/5/2002	US
13030	Analog Active Matrix Emissive Display	Ipri, A. C. Stewart, R. G.	Granted	09/200513	11/25/1998	6417825	7/9/2002	US
13124	Compact Rear Projection System	Burstyn, H. C.	Granted	09/611706	7/7/2000	6406150	6/18/2002	US
13124 A	Compact Rear Projection System Using Birefringent Optics	Burstyn, H. C.	Granted	09/611707	7/7/2000	6561649	5/13/2003	US
13124 B	Focus And Aim Device And Method For Image Projection	Burstyn, H. C. Lurie, M. J.	Granted	09/611708	7/7/2000	6511186	1/28/2003	US

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Case No.	Title	Inventor(s)	Status	Application No.	Filing Date	Patent No.	Issue Date	Country
13273	Patterned Deposition Of A Material	Matthies, D. L.	Pending	00-579366	11/3/1999			JP
13273	Patterned Deposition Of A Material	Matthies, D. L.	Pending	01-7005498	11/3/1999			KR
13273	Patterned Deposition Of A Material	Matthies, D. L.	Granted	09/250328	2/16/1999	6221438	4/24/2001	US
13359	Thin-Film Transistor Circuitry With Reduced Sensitivity To Variance In Transistor Threshold Voltage	Dawson, R. M. A. Shen, Z. Ipri, A. C. Stewart, R. G. Kane, M. G.	Pending	01-548440	12/28/2000			JP
13359	Thin-Film Transistor Circuitry With Reduced Sensitivity To Variance In Transistor Threshold Voltage	Dawson, R. M. A. Shen, Z. Ipri, A. C. Stewart, R. G. Kane, M. G.	Granted	09/473504	12/28/1999	6307322	10/23/2001	US

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Case No.	Title	Inventor(s)	Status	Application No.	Filing Date	Patent No.	Issue Date	Country
13438	Display Device	Roach, W. R. Cherukuri, S. C. Singh, B. Shen, Z.	Pending	00-601698	2/23/2000			JP
13438 A	Fiber Carrying Light Emitting Elements	Roach, W. R. Singh, B. Shen, Z. Chiang, W. Patel, V. K.	Pending	00-601886	2/23/2000			JP
13438 B	Fiber-Based Flat Panel Display	Roach, W. R. Singh, B. Chiang, W.	Granted	09/418774	10/15/1999	6228228	5/8/2001	US
13488	A Method And Apparatus For Calibrating Display Devices And Automatically Compensating For Loss In Their Efficiency Over Time	Shen, Z. Matthies, D. L. Atherton, J. H. Stewart, R. G.	Pending	01805502.8	2/22/2001			CN
13488	A Method And Apparatus For Calibrating Display Devices And Automatically Compensating For Loss In Their Efficiency Over Time	Shen, Z. Matthies, D. L. Atherton, J. H. Stewart, R. G.	Published	01925104.0	2/22/2001			EP

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Case No.	Title	Inventor(s)	Status	Application No.	Filing Date	Patent No.	Issue Date	Country
13488	A Method And Apparatus For Calibrating Display Devices And Automatically Compensating For Loss In Their Efficiency Over Time	Shen, Z. Matthies, D. L. Atherton, J. H. Stewart, R. G.	Pending	01-562472	2/22/2001			JP
13488	A Method And Apparatus For Calibrating Display Devices And Automatically Compensating For Loss In Their Efficiency Over Time	Shen, Z. Matthies, D. L. Atherton, J. H. Stewart, R. G.	Pending	02-7010905	2/22/2001			KR
13488	A Method And Apparatus For Calibrating Display Devices And Automatically Compensating For Loss In Their Efficiency Over Time	Shen, Z. Matthies, D. L. Atherton, J. H. Stewart, R. G.	Granted	09/610159	7/5/2000	6414661	7/2/2002	US
13632	Electrode Structure Which Supports Self Alignment Of Liquid Deposition Of Materials	Matthies, D. L.	Published	01986466.9	11/21/2001			EP
13632	Electrode Structure Which Supports Self Alignment Of Liquid Deposition Of Materials	Matthies, D. L.	Pending	02-545299	11/21/2001			JP

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Case No.	Title	Inventor(s)	Status	Application No.	Filing Date	Patent No.	Issue Date	Country
13632	Electrode Structure Which Supports Self Alignment Of Liquid Deposition Of Materials	Matthies, D. L.	Allowed	09/718225	11/21/2000			US
13633	Display Tile Structure Using Organic Light Emitting Materials	Matthies, D. L.	Published	01988262.0	11/9/2001			EP
13633	Display Tile Structure Using Organic Light Emitting Materials	Matthies, D. L.	Pending	02-548911	11/9/2001			JP
13633	Display Tile Structure Using Organic Light Emitting Materials	Matthies, D. L.	Granted	09/709904	11/10/2000	6498592	12/24/2002	US
13640	Tiled Electronic Display Structure And Method For Modular Repair Thereof	Matthies, D. L.	Granted	09/716535	11/20/2000	6683665	1/27/2004	US
13817	Light-Emitting Fiber, And Method For Making Same	Roach, W. R. Szostak, D. J.	Granted	09/691882	10/19/2000	6560398	5/6/2003	US

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Case No.	Title	Inventor(s)	Status	Application No.	Filing Date	Patent No.	Issue Date	Country
13830	Electrical Interconnection Of Light-Emitting Fibers, And Method Therefor	Roach, W. R. Kaganowicz, G. Singh, B.	Granted	09/698913	10/27/2000	6541919	4/1/2003	US
13875	Retractable Rear Projection Display	Burstyn, H. C. Yocom, P. N. Shapiro, L.	Granted	09/671525	9/27/2000	6873460	3/29/2005	US
14285	Method And Apparatus For Providing A High-Performance Active Matrix Pixel Using Organic Thin-Film Transistors	Kane, M. G. Cuomo, F. P. Greening, B. K.	Published	02717575.1	3/7/2002			EP
14285	Method And Apparatus For Providing A High-Performance Active Matrix Pixel Using Organic Thin-Film Transistors	Kane, M. G. Cuomo, F. P. Greening, B. K.	Pending	02-581598	3/7/2002			JP
14285	Method And Apparatus For Providing A High-Performance Active Matrix Pixel Using Organic Thin-Film Transistors	Kane, M. G. Cuomo, F. P. Greening, B. K.	Pending	03-7012959	3/7/2002			KR

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Case No.	Title	Inventor(s)	Status	Application No.	Filing Date	Patent No.	Issue Date	Country
14285	Method And Apparatus For Providing A High-Performance Active Matrix Pixel Using Organic Thin-Film Transistors	Kane, M. G. Cuomo, F. P. Greening, B. K.	Granted	10/093208	3/7/2002	6639281	10/28/2003	US
14620	Pixel Circuit For An Active Matrix Organic Light-Emitting Diode Display	Kane, M. G.	Published	10/953087	9/29/2004			US
14670	Thermal Management In Electronic Displays	Palanisamy, P.	Granted	10/435775	5/12/2003	6914379	7/5/2005	US
14670	Thermal Management In Electronic Displays	Palanisamy, P.	Published	03/23655	7/30/2003			WO
14672	Low-Cost Circuit Board Materials And Processes For Area Array Electrical Interconnections Over A Large Area Between A Device And The Circuit Board	Palanisamy, P.	Granted	10/435960	5/12/2003	6849935	2/1/2005	US

Case No.	Title	Inventor(s)	Status	Application No.	Filing Date	Patent No.	Issue Date	Country
14672	Low-Cost Circuit Board Materials And Processes For Area Array Electrical Interconnections Over A Large Area Between A Device And The Circuit Board	Palanisamy, P.	Published	03/23755	7/30/2003			WO
14672 A	Low-Cost Circuit Board Materials And Processes For Area Array Electrical Interconnections Over A Large Area Between A Device And The Circuit Board	Palanisamy, P.	Allowed	10/991707	11/18/2004			US
14707	Driver Circuit, As For An OLED Display	Kane, M. G.	Published	10/926621	8/26/2004			US

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Exhibit A - Transpacific IP Disclosures

Case No.	Title	Inventor(s)	Status	Disclosure Date
15042	A Leaky-Wave Coupler Is Used to Increase The External Quantum Efficiency Of An OLED	Burstyn, H. C.	Open	1/21/2004
15159	High Efficiency Outcoupling OLED Utilizing Two-Dimensional Grating	Khalfin, V. B.	Open	8/30/2004
15237	A Method To Increase The External Efficiency Of Organic Light Emitting Diodes (OLEDs)	Chan, W. K. Khalfin, V. B.	Open	2/4/2005

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