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FORM PTO-1595

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(Rev. 06/93) **PATENTS ONLY**
OMB No. 0651-0011 (Exp. 04/94)

103434889

Patent and Trademark Office
Attorney Docket No.: CD05219Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Please record the attached original documents or copy thereof.

1. Name of conveying party(ies): Ramesha DODDAMANE Eswar VADLAMANI Gopalakrishnan Perur KRISHNAN Tarjinder SINGH Additional name(s) of conveying party(ies) attached.	2. Name and address of receiving party(ies): NetLogic Microsystems, Inc. 1875 Charleston Road Mountain View, CA 94043 Additional name(s) and address(es) attached.
3. Nature of conveyance: <input checked="" type="checkbox"/> Assignment <input type="checkbox"/> Merger <input type="checkbox"/> Security Agreement <input type="checkbox"/> Change of Name <input type="checkbox"/> Other: _____ Execution Date: 05/24/2007, 07/21/2007, 07/20/2007, and 07/25/07 respectively.	
4. Application number(s) or patent number(s): This document is being filed together with a new application, the execution date of the application is: <input checked="" type="checkbox"/> Patent Application Serial No.(s): 11/713,258 filed on : March 02, 2007 <input type="checkbox"/> Patent No.(s): _____ <input type="checkbox"/> Additional numbers attached. _____	
5. Name and address of party to whom correspondence concerning document should be mailed: Bradley T. Sako HAVERSTOCK & OWENS LLP 162 North Wolfe Road Sunnyvale, CA 94086	6. Total number of applications and patents involved: <u>1</u> 7. Total fee(s) [37 CFR §§ 1.21(h) and 3.41]: <input checked="" type="checkbox"/> Check(s) in the amount of \$40.00 is enclosed. 8. Authorization to Charge Additional Fees: <input checked="" type="checkbox"/> The Commissioner is hereby authorized to charge any additional fees or credit any overpayment associated with this communication and which may be required under 37 CFR §§ 1.21(h) and 3.41 to Deposit Account No. 08-1275.
9. To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document. Dated: <u>August 2, 2007</u> Signature: <u><i>Bradley T. Sako</i></u> Name: <u>Bradley T. Sako</u> Reg. No.: <u>37,923</u> Total number of pages including cover sheet, attachments, and document: <u>6</u>	

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FINANCE SECTION

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PATENT
REEL: 019711 FRAME: 0351

ASSIGNMENT (Page 1 of 3)

In consideration of good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, I/we, **Ramesha Doddamane, Eswar Vadlamani, Gopalakrishnan Perur Krishnan, and Tarjinder Singh**, the undersigned Assignor(s), believing to be the original, first and sole or joint inventor(s) of any and all new and useful improvements disclosed in the application for the United States patent entitled

PROGRAMMABLE ADDRESS SPACE BUILT-IN SELF TEST (BIST) DEVICE AND METHOD FOR FAULT DETECTION

hereby sell, assign, and transfer to **NetLogic Microsystems, Inc.** a Delaware Corporation, having a principal place of business at 1875 Charleston Road, Mountain View, CA 94043, ("Assignee"), and its successors, assigns, and legal representatives, the entire right, title, and interest for the United States and all foreign countries, in and to any and all improvements that are disclosed in the same patent application

 X which was filed with the United States Patent and Trademark Office on 03/02/2007, and which was assigned Application Number 11/713,258.

 which has been executed by the undersigned prior hereto or concurrently herewith on the dates indicated below.

I/We hereby further sell, assign, and transfer to Assignee said application and all divisional applications, continuation applications, continued prosecution applications, continuation-in-part applications, substitute applications, renewal applications, reissue applications, reexaminations, extensions, and all other patent applications that have been or shall be filed in the United States and all foreign countries on any of said improvements; and in and to all original patents, reissued patents, reexamination certificates, and extensions, that have been or shall be issued in the United States and all foreign countries on said improvements; and in and to all rights of priority resulting from the filing of said United States application; and the right to file foreign applications directly in the name of the Assignee and to claim for any such foreign applications any priority rights to which such applications are entitled under international conventions, treaties or otherwise.

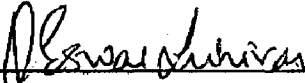
I/We agree that said Assignee may apply for and receive a patent or patents for said improvements in its own name; and that, when requested, without charge to, but at the expense of, said Assignee, its successors, assigns, and legal representatives, to carry out in good faith the intent and purpose of this Assignment, the undersigned will execute all divisional applications, continuation applications, continued prosecution applications, continuation-in-part applications, substitute applications, renewal applications, reissue applications, reexaminations, extensions and all other patent applications on any and all said improvements; execute all rightful oaths, assignments, powers of attorney, and other papers; communicate to said Assignee, its successors, assigns, and representatives all facts known to the undersigned relating to said improvements and the history thereof; and generally assist said Assignee, its successors, assigns, or representatives in securing and maintaining proper patent protection for said improvements and for vesting title to said improvements, and all applications for patents and all patents on said improvements, in said Assignee, its successors, assigns, and legal representatives.

For: **PROGRAMMABLE ADDRESS SPACE BUILT-IN SELF TEST (BIST) DEVICE
AND METHOD FOR FAULT DETECTION**

To: Netlogic Microsystems, Inc.

21st July, 2007
Date

Eswar Vadlamani
Name:


Signature

_____, 2007
Date

Gopalakrishnan Perur Krishnan
Name:

Signature

_____, 2007
Date

Tarjinder Singh
Name:


Signature

For: PROGRAMMABLE ADDRESS SPACE BUILT-IN SELF TEST (BIST) DEVICE
AND METHOD FOR FAULT DETECTION

To: Netlogic Microsystems, Inc.

_____, 2007	<u>Eswar Vadlamani</u>
Date	Name:
_____ Signature	

_____ Signature

<u>JULY 20</u> , 2007	<u>Gopalakrishnan Perur Krishnan</u>
Date	Name:
	_____ Signature

_____ Signature

_____, 2007	<u>Tarjinder Singh</u>
Date	Name:
_____ Signature	

_____ Signature

ASSIGNMENT (Page 3 of 3)

For: **PROGRAMMABLE ADDRESS SPACE BUILT-IN SELF TEST (BIST) DEVICE
AND METHOD FOR FAULT DETECTION**

To: **Netlogic Microsystems, Inc.**

_____, 2007 **Eswar Vadlamani**
Date Name:

Signature

_____, 2007 **Gopalakrishnan Perur Krishnan**
Date Name:

Signature

95th July, 2007 **Tarjinder Singh**
Date Name:

Signature