

PATENT ASSIGNMENT

Electronic Version v1.1
 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
EFFECTIVE DATE:	11/15/1996

CONVEYING PARTY DATA

Name	Execution Date
Rockwell International Corporation	11/15/1996

RECEIVING PARTY DATA

Name:	Rockwell Science Center, Inc.
Street Address:	1049 Camino Dos Rios
City:	Thousand Oaks
State/Country:	CALIFORNIA
Postal Code:	91360

PROPERTY NUMBERS Total: 3

Property Type	Number
Patent Number:	5694521
Patent Number:	5774839
Patent Number:	5781128

CORRESPONDENCE DATA

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NAME OF SUBMITTER:	Farshad Farjami

Total Attachments: 5

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PATENT
REEL: 019767 FRAME: 0161

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ASSIGNMENT OF INVENTIONS, PATENT APPLICATIONS AND PATENTS

WHEREAS, Rockwell International Corporation ("Assignor"), a corporation organized and existing under the laws of the State of Delaware, desires to assign to Rockwell Science Center, Inc., ("Assignee"), a corporation organized and existing under the laws of the State of Delaware, pursuant to that certain Assignment and Assumption Agreement between Assignor and Assignee dated November 15, 1996, certain assets of Assignor identified as Contributed Assets in Annex A to that certain Agreement and Plan of Merger dated as of July 31, 1996 among Rockwell International Corporation, The Boeing Company and Boeing NA, Inc., and:

WHEREAS, Assignor is the owner of, or has rights in, those inventions, patent applications and patents included among the Contributed Assets, including those inventions, patent applications and patents listed on the Schedule attached hereto ("Contributed Inventions, Patent Applications and Patents").

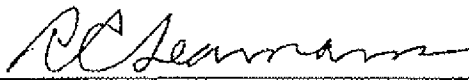
NOW, THEREFORE, in consideration of the issuance of stock in Assignee to Assignor and for other good and valuable consideration delivered by Assignee to Assignor, the receipt and sufficiency of which are hereby acknowledged, said Assignor does hereby sell, grant, assign, convey and transfer unto the said Assignee, Assignor's entire right, title and interest in and to the Contributed Inventions, Patent Applications and Patents, including any applications or patents arising therefrom or any continuations or divisions thereof, and any and all re-examinations or reissues thereof and the subject matter of any and all claims which may be obtained in every such patent, the same to be held and enjoyed by the said Assignee, its successors, assigns or other legal representatives, for its own use and advantage, and for the use and advantage of its successors, assigns or other legal representatives, to the end of the term or terms for which patents are or may be granted, reissued or re-examined, as fully and entirely as the same would have been held and enjoyed by Assignor had this assignment not been made, including all rights to profits and damage by reason of past infringement by any party or parties, with the right to sue

and collect the same for its own use and benefit, and for the use and benefit of its successors, assigns or other legal representatives.

Assignor hereby appoints Assignee its attorney-in-fact to act in Assignor's name, place and stead to execute, deliver and record any document or instrument of assignment required in any country in which any of the Contributed Inventions, Patent Applications and Patents are pending or issued, granting or confirming the rights granted herein, but only to the extent of those rights granted herein in connection with the Contributed Inventions, Patent Applications and Patents.

IN WITNESS WHEREOF, Assignor has caused this Assignment to be duly executed on this 15th day of November 1996.

ROCKWELL INTERNATIONAL CORPORATION

By: 
Assistant Secretary

Attest: 

(Corporate Seal)

ROCKWELL SEMICONDUCTOR SYSTEMS OPEN DISCLOSURES

96E046 A Hybrid Analog/Digital PSTN Videophone System Using a Scalable Source Corder
96E045 Highly Efficient 3.6 Volt Cellular HBT Power Amplifier Using Sub Harmonic
Regenerative Enhancement
96E044 Dual Output HBT Amplifier Circuit
96E043 On Chip Partial Output Match for High Power MMICs
96E042 Current Limiting Low Impedance Active Bias Circuit for Power Amplifiers
96E038 Circuit Triggered Power Supply Clamp SCRs for ESD Protection
96E032 Bandwidth Extrapolated Analog Simultaneous voice and Data Communication System
96E030 VRPI: A Videophone Access Method
96E029 Dynamically Controlled PLL Loop Bandwidth
96E027 User ID String for Modems
96E026 Low Power Clock Technique for CMOS Latches
96E024 Buffered Regenerative Sense Amp
96E021 Extension Pickup
96E020 GSTN/PSTN Wireline Data Connectivity to Analog Wireless Networks
96E019 Silent Answer for Telephone from Pre-Recorded Message
96E018 A Modem Control Channel for V.34/V.34Q
96E017 Formation of Self-Aligned Bipolar Transistor
96E013 RF Probe with Electrically Tunable Filter
96E009 An Iterative Method to Estimate the Power Increase of Trellis Precoder
96E008 Sub-Band Based Hybrid Simultaneous Voice and Data Modem
96E007 Hybrid Simultaneous Voice and Data Modem
96E006 Ultra-Clean Negative Voltage Power Supply Generator
96E005 Intelligent Pre-Quantization in Motion Compensated Video Coding
96E002 Pager with Acoustic DTMF Auto-Dialer
96E001 A Class AB Servo Amplifier for Fast DC Setting
95E146 Scalar-Code Excited GSM Speech Encoder for Speech Enhancement
95E145 Trellis-Coded Quantization of Prediction Residuals of GSM Speech Coder for Message
Recording
95E144 A Blind Equalization Algorithm Based on a Dual Decision Feedback Approach
95E141 c/w Predicted Scalar Quantization of Transformed Reflection Coefficients
95E141 95E142
95E139 Simultaneous Estimation of Echo Paths and ISI Channel Responses Using an Orthogonal
LMS...
95E138 Fast Modem Startup
95E136 Method of Measuring Line Quality with QADM
95E135 Adaptive Gain Setting for QADM Voice Frames
95E134 Rate Optimization for QADM Using a Secondary Channel
95E133 Improved Method of Silence-Frame Deletion in Framed Voice Transmission
95E132 Orthogonal LMS Algorithms for Fast Echo Canceller Training
95E131 Online Telephone
95E123 Automatic Impedance Tuning of Resonators
95E122 Power Amplifier Control Loop with Dynamic Response Adjustment
95E120 A Simplified Frequency Synthesizer Architecture for Implementation in Bipolar
Technology
95E111 New Technique to Form a Capacitor with Improved Contact in an Integrated Circuit
95E107 Device Architecture for Enhanced Testability and Observability for Microprocessors
95E096 Soft Handoff Capable Base Stations for GSM
95E054 A Double-Sampling, Single-Bit, Two Capacitor, Differential DAC for Sigma Delta
Modulators
→ 95E048 Neural Network Approach to Vector Quantization ←
95E041 An Improved Automatic Gain Control Method for Voice QADM
95E034 Interface for Data Radio

ROCKWELL SEMICONDUCTOR SYSTEMS APPLICATIONS FILED

96E014	Signal Compression Using Index Mapping Technique for the Sharing of Quantization Tables	702,780
95E113	Voice Activity Detection (VAD)	589,509
95E110	Usage of Voice Activity Detection for Efficient Coding of Speech	589,132
95E089	Gain Imbalance Compensation for a Quadrature Receiver in a Cordless Direct Sequence Spread...	568,210
95E088	12 Chip Coded Spread Spectrum Modulation for Direct Conversion Radio Architectures in a ...	568,047
95E084	Selection of Communication Channel in a Digital Cordless Telephone	568,871
95E082	Echo Canceling Method and Apparatus in a Communication Device	568,843
95E080	Method and Apparatus for Signal Quality Estimation a Direct Sequence Spread Spectrum...	568,330
95E077	Audio Mute for Digital Cordless Telephone	568,049
95E074	CID Demodulator for DCT	569,489
95E073	Selective Configuration of Operating Programs Stored in an Application Specific Integrated ...	568,840
95E072	Variable Digital Automatic Gain Control in a Cordless Direct Sequence Spread Spectrum ...	568,161
95E071	Extended Time Tracking and Peak Energy In-Window Demodulation for Use in a Direct Sequence...	568,053
95E070	Baseband Demodulator for Polar or Rectangular Modulated Signal in a Cordless Spread Spectrum ...	568,045
95E063	Frequency Acquisition Method for Direct Sequence Spread Spectrum Systems	568,058
95E062	Frequency Error Compensation for Direct Sequence Spread Spectrum Systems	568,095
95E059	Destination Field Control to Access of Pipeline Latches as Instruction Inputs	568,714
95E057	Low Overhead Interface to a Coprocessor	570,167
95E056	Jitter Circuit for Reduced Switching Noise	644,762
95E044	Reduced Sensitivity Power-On Reset Circuitry	616,421
95E043	Single-ended to Differential Converter with Relaxed Common Mode Input Requirements	536,405
95E042	Offset Cancellation for Differential Amplifiers	536,331
95E040	Optimized Simultaneous Audio and Data Transmission Using QADM with Phase Randomization	
95E030	Efficient Speakerphone Anti-Howling System	531,993
95E028	Loop Gain Processing System for Speakerphone Applications	531,992
95E010 c/w	Integrated Circuit Device with Embedded Flash Memory and Method for	
95E011/012/013	Manufacturing Same	
95E007	Data Access Arrangement with Telephone Interface	536,886
95E006	Remote Hang Up Detector	653,185
95E003	An Adaptive Frequency Correction Burst Detector for the GSM System	537,376
95E002	An Iterative Filtering Frequency Estimator and Estimation Method	515,403
94E078	Base-N Resolution Converter	456,137
94E077	Delayed Decision Switched Prediction Multi-Stage LSF Quantization	536,890
94E075	Method for Manufacturing Multi-Chip Modules Utilizing Direct Lead Attach	629,926
94E071	Timing Recovery Scheme for Packet Speech in Multiplexing Environment of Voice with Data App.	443,651
94E068	Directional Steering and Navigation Indicator	402,434
94E064 (c/w	Improved Performance CMOS Process	617,452
94E063)		
94E063 (c/w	Active First CMOS Process Flow with Blanket NFET Punchthrough and P-LDD	620,858
94E064)	Implants	
94E061	Clock Signal Frequency Multiplier	691,765

ROCKWELL SEMICONDUCTOR SYSTEMS APPLICATIONS FILED

94E059	An apparatus and Method of Providing an Extremely Low-Power Self-Awakening Function to a Processing.	514,509
94E056	Spike Code-Excited Linear Prediction	536,329
94E054	Self-Testing and Evaluation System	590,699
94E052	Method and Apparatus for Coupling Multiple Independent On-Chip Vdd Busses to an ESD Core...	427,017
94E050	Five Volt Tolerant Protection Circuit	590,382
94E044-CIP	Pitch Lag Estimation System Using Linear Predictive Coding Residual	454,477
94E044 (c/w 95E018)	Pitch Lag Estimation System	342,494
94E025 (c/w 94E006)	Cache Architecture and Method of Operation	436,049
94E019	Flat Panel Display and Fabrication Process Utilization Photolithography Alignment Marks	383,143
94E004	Single Bit Bandpass Analog-to-Digital Converter	196,354
93SC091	GaAs HBT Tri-level Logic Circuit	319,696
93E070	Unified Trellis Encoder	327,079
93E069	Modem System	364,901
93E056	Position Estimation Using Satellite Range Rate Measurements	235,845
→ 93E041	Variable Speed Playback System	371,258 ←
92E032	Direct Connect Radio and Antenna Assembly	233,289
92E011-C	Device and Method for Selecting and Addressing Extended Memory Addresses	352,296
91E013-C	Space Vector Data Path	630,231
91E004 c/w	Streamer for RISC Digital Signal Processor	917,872
91E008		
91E004-C	Triple Register RISC Digital Signal Processor	547,050
91E003	Method for Integrating a Signal Analyzer Scope onto a Single Chip	920,727
90E072	Register File with Multi-Tasking Support	287,017
90E057	Distribution of Modem Error Correction and Compression Processing	887,193

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