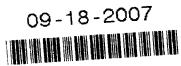


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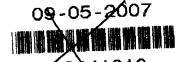
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- ATENTO ON	10344592	NITT.0400
To the Honorab	ole Commissioner of Patents and Trademarks. Ple	ease record the attached original documents or copy thereof.
(1) KAT: NOMU (4) HITO OHTOS (7) SUM TANAK Additional nam 3. Nature of	nes of conveying parties attached:	<ol> <li>Name and Address of Receiving Party(ies):</li> <li>Name: Hitachi, Ltd.         Address: 6-6, Marunouchi 1-chome         City: Chiyoda-ku, Tokyo, Japan</li> <li>Name: Sophia School Corporation         Address: 7-1, Kioityo,         City: Chiyoda-ku, Tokyo, Japan</li> <li>Name: Sony Corporation         Address: 1-7-1 Konan,         City: Minato-ku, Tokyo, Japan</li> </ol>
	Dates: (1)(2) June 8, 2007, (3) June 28, 2007, (4)(5)(6)(7) July 13, 2007, (8) July 17, 2007	
If this documer the execution d	Application Number(s):  11/882,055 at is being filed together with a new application, ate of the application is:  Numbers Attached.	4. (b) Patent Numbers:
	Address of Party to whom Correspondence Socument Should be Mailed:	6. Total Number of Applications and Patents Involved: 1
Name:	Stanley P. Fisher	7. Total Fee: \$40.00 (37 C.F.R. § 3.41)
Address:	Reed Smith LLP 3110 Fairview Park Dr. Suite 1400 Falls Church, VA 22042	<ul> <li>≅ Enclosed</li> <li>☒ Authorized to be charged to deposit account.</li> </ul>
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	Designation No. 24, 344	Juan Cartus A. Marquez, Registration, No. 34,072
	ner, Registration No. 24, 344	2/2005
Total number	of pages comprising cover sheet: 1	09/1

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1. Name of Conve. (1) KATSUM NOMURA, (4) HITOSHI OHTOSHI, (7) SUMIKO TANAKA Additional names of 3. Nature of Conv.  Assignment Security A	ying Party(ies):  I KISHINO, (2) ICHIRO  (3) TSUNENORI ASATSUMA,  NAKAMURA, (5) TSUKURU  (6) TAKESHI KIKAWA,  FUJISAKI, AND (8) SHIGEHISA  conveying parties attached:   Yes No  eyance:  I Merger	2. Name and Address of Receiving Party(ies):  (1) Name: Hitachi, Ltd. Address: 6-6, Marunouchi l-chome City: Chiyoda-ku, Tokyo, Japan  (2) Name: Sophia School Corporation Address: 7-1, Kioityo, City: Chiyoda-ku, Tokyo, Japan  (3) Name: Sony Corporation Address: 1-7-1 Konan, City: Minato-ku, Tokyo, Japan		
(A) July 17, 2007  4. (a) Patent Application Number(s):  11/822,055  If this document is being filed together with a new application, the execution date of the application is:  Additional Numbers Attached.		4. (b) Patent Numbers:		
	lress of Party to whom Correspondence cument Should be Mailed:	6. Total Number of Applications and Patents Involved: 1		
Name:	Stanley P. Fisher	7. Total Fee: \$40.00 (37 C.F.R. § 3.41)		
Address:	Reed Smith LLP 3110 Fairview Park Dr. Suite 1400 Falls Church, VA 22042	<ul> <li>≅ Enclosed.</li> <li>⋈ Authorized to be charged to deposit account.</li> </ul>		
		8. Deposit Account Number: 08-1480  ATTACH DUPLICATE COPY OF THIS PAGE IF PAYING BY DEPOSIT ACCOUNT		
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9. Statement and Signature:  To the best of my knowledge and belief, the foregoing is true and correct and any attached copy is a true copy of the				
original document. August 30, 2007				
09/04/2007 NJANA1 00000045 11822055 01 FC: 8021 40.00 0P  Stanley P. Fisher, Registration No. 24, 344  Juan Carlos A. Marquez, Registration No. 34,072				
Total number of pages comprising cover sheet: 1				

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**PATENT** REEL: 019853 FRAME: 0797

## ASSIGNMENT (譲渡証)

## As a below named inventor, I hereby declare that:

IN CONSIDERATION of the sum of One Dollar (\$1.00) or the equivalent thereof, and other good and valuable consideration paid to the below named inventor(s), citizens of Japan by Hitachi, Ltd., and Sophia School Corporation and Sony Corporation, corporations organized under the laws of Japan, located at 6-6, Marunouchi 1-chome, Chiyoda-ku, Tokyo, Japan, and 7-1, Kioityo, Chiyoda-ku, Tokyo, Japan, and 1-7-1 Konan, Minato-ku, Tokyo, Japan respectively, receipt of which is hereby acknowledged, we, the below named inventor(s), do hereby sell and assign to said Hitachi, Ltd., and Sophia School Corporation and Sony Corporation, their successors and assigns, all our right, title and interest, in and for the United States of America, in and to

## Semiconductor Laser Diode Device

invented by us and described in the application for United States Letters Patent therefore, executed on even date herewith, and all United States Letters Patent which may be granted therefore, and all divisions, continuations and extensions thereof, the said interest being the entire ownership of the said Letters Patent when granted, to be held and enjoyed by said Hitachi, Ltd., and Sophia School Corporation and Sony Corporation, their successors, assigns or other legal representatives, to the full end of term for which said Letters Patent may be granted, as fully and entirely as the same would have been held and enjoyed by us if this assignment and sale had not been made;

And we hereby agree to sign and execute any further documents or instruments which may be necessary, lawful, and proper in the prosecution of said above-named application or in the preparation and prosecution of any continuing, continuation-in-part, substitute, divisional, renewal, reviewed or reissue applications or in any amendment, extension, or interference proceedings, or otherwise to secure the title thereto in said assignee;

And we do hereby authorize and request the Commissioner of Patents to issue said Letters Patent to said Hitachi, Ltd., and Sophia School Corporation and Sony Corporation.

Signed on the date(s) indicated aside signatures:

RECORDED: 09/13/2007

INVENTOR(S)	Date Signed
(発明者フルネームサイン)	(署名日)
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Shigehisa TANAKA	
9)	
10)	

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