

# PATENT ASSIGNMENT

Electronic Version v1.1

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
Intersil Corporation	07/15/2003
Intersil Americas, Inc.	07/15/2003
RECEIVING PARTY DATA	
Name:	GlobespanVirata, Inc.
Street Address:	100 Schulz Drive
City:	Red Bank
State/Country:	NEW JERSEY
Postal Code:	07701
PROPERTY NUMBERS Total: 4	
Property Type	Number
Patent Number:	6891440
Patent Number:	6876319
Patent Number:	6690715
Patent Number:	6674998
CORRESPONDENCE DATA	
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ATTORNEY DOCKET NUMBER:	14733.058
NAME OF SUBMITTER:	Dennis Hopkins

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**PATENT**

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**Total Attachments: 14**

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### AFFIRMATION OF PATENT ASSIGNMENT

WHEREAS, Intersil Corporation, a Delaware corporation, and all its subsidiaries, specifically including Intersil Americas, Inc., located at 7585 Irvine Center Drive, Suite 100, Irvine, California, USA 92618, (collectively "Intersil"), owned the entire right, title and interest in and to the patent applications, invention disclosures and issued patents identified in Schedule A, attached hereto, and each and every invention contained and expressed therein, including each and every continuation, continuation-in-part, reissue, reexamination, extension, substitute or division of any of the patent applications listed in Schedule A (collectively the "Patent Property");

WHEREAS, Intersil, in accordance with the Asset Purchase Agreement dated July 15, 2003, and related documents, including the Intellectual Property Assignment and License Agreement, (collectively the "APA"), did sell, assign, transfer and convey to Globespanvirata, Incorporated, a Delaware corporation located at 100 Schulz Drive, Red Bank, New Jersey, USA 07701 (hereinafter "Globespan"), Intersil's entire right, title, and interest in and to the Patent Property;

NOW THEREFORE, in consideration of the sum of Ten Dollars (\$10.00) and other good and valuable consideration as set forth in the APA, the receipt and sufficiency of which is hereby acknowledged, Intersil affirms the assignment and transfer to Globespan of Intersil's entire right, title and interest in and to the Patent Property;

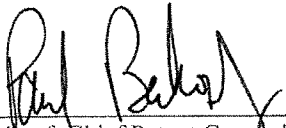
Intersil further acknowledges and confirms that Globespan may, solely in Globespan's name, conduct all necessary procedures for recording and registering this Affirmation of Patent Assignment with any Patent office, Intellectual Property office or other governmental agency throughout the world;

Intersil hereby further acknowledges and confirms that Intersil assigned to Globespan all claims, demands and causes of action for the past infringement of the Patent Property, including the right to collect damages for past infringement, or for unfair competition in business in connection therewith wheresoever or by whomsoever committed, the same to be held and enjoyed by Globespan, and Globespan's subsidiaries, affiliates, successors and assigns, as fully and entirely as the same would or could have been held and enjoyed by Intersil had the Assignment not been made;

Globespan formally declares to accept all right, title and interest in and to the Patent Property.

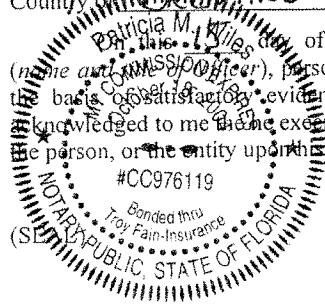
[SIGNATURES APPEAR ON NEXT PAGE]

INTERSIL CORPORATION

By:  Date: 9/15/04, 2004  
Paul Bernkopf, Chief Patent Counsel and Secretary, Intersil Corporation  
on behalf of Intersil Corporation and all of its subsidiaries,  
specifically including Intersil Americas, Inc.

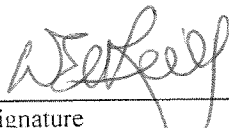
State of FLORIDA )  
County of DADE )

On this 15th day of September, 2004, before me PATRICIA M. WILES, Notary  
(name and title of Officer), personally appeared Paul Bernkopf, Esq., personally known to me (or proved to me on  
the basis of satisfactory evidence) to be the person whose name is subscribed to the within instrument and  
acknowledged to me that he executed the same in his authorized capacity, and that by his signature on the instrument  
the person, or the entity upon his behalf of which the person acted, executed the instrument.




Patricia M. Wiles  
Notary Public  
My Commission Expires: 10/18/04

GLOBESPANVIRATA, INCORPORATED  
(now named Conexant, Inc.)

By:  Date: JANUARY 30, 2007  
Signature  
Name: DENNIS E. O'REILLY  
Title: SENIOR VICE PRESIDENT, CHIEF LEGAL OFFICER AND SECRETARY

State of NEW YORK )  
Country of NEW YORK )

On this 30TH day of JANUARY 2007, before me SEY-HYO LEE  
(name and title of Officer), personally appeared DENNIS E. O'REILLY,  
personally known to me (or proved to me on the basis of satisfactory evidence) to be the person whose name is  
subscribed to the within instrument and acknowledged to me that he executed the same in his authorized capacity,  
and that by his signature on the instrument the person, or the entity upon his behalf of which the person acted,  
executed the instrument.

(SEAL)   
Notary Public  
My Commission Expires: 10/26/2010

SF1:560081.1

**SEY-HYO LEE**  
Notary Public, State of New York  
No. 31-5003516  
Qualified in New York County  
Certificate Filed in New York County  
Commission Expires October 26, 2010

## Schedule A

### Pending U.S. Patent Applications

Client Ref. No.	Application Number	Filing Date	Invention Title
SE-1301-WR	10/005483	09-Nov-2001	HIGH DATA RATE SPREAD SPECTRUM TRANSCEIVER AND ASSOCIATED METHODS
SE-1417-WR	09/426847	26-Oct-1999	TRANSCEIVER INCLUDING REACTIVE TERMINATION TO IMPROVE CROSS-MODULATION PERFORMANCE AND RELATED METHODS
SE-1479-WR	09/823845	30-Mar-2001	RAKE RECEIVER WITH EMBEDDED DECISION FEEDBACK EQUALIZER
SE-1480-WR	09/612823	10-Jul-2000	RAPID ESTIMATION OF WIRELESS CHANNEL IMPULSE RESPONSE
SE-1481-WR	09/494000	28-Jan-2000	BIASED-CORRECTED RAKE RECEIVER FOR DIRECT SEQUENCE SPREAD SPECTRUM WAVEFORM
SE-1482-WR	09/266386	10-Mar-99	SUCCESSIVE APPROXIMATION CORRECTION OF DC OFFSET IN FILTER-BUFFER BASEBAND PATH OF DATA RADIO
SE-1492-WR	09/231184	28-Oct-2002	WIRELESS LOCAL AREA NETWORK SPREAD SPECTRUM TRANSCEIVER WITH MULTIPATH MITIGATION
SE-1500-WR	09/231228	14-Jan-1999	SPREAD SPECTRUM TRANSCEIVER FOR USE IN WIRELESS LOCAL AREA NETWORK AND HAVING MULTIPATH MITIGATION
SE-1520-WR	09/573782	17-May-2000	APPARATUS FOR RADIO FREQUENCY PROCESSING WITH DUAL MODULUS SYNTHESIZER
SE-1595-WR	09/860681	19-May-2001	CLOCK GENERATOR WITH A DIGITAL DIVIDE-BY-2.2 CIRCUIT

Client Ref. No.	Application Number	Filing Date	Invention Title
SE-1596-WR	10/087109	01-Mar-2002	INTEROPERABLE FORWARD ERROR CORRECTION IN A NETWORK WITH HETEROGENEOUS STATION CAPABILITIES
SE-1614-WR	09/864676	24-May-2001	A COMMUNICATION SYSTEM USING ORTHOGONAL WAVELET DIVISION MULTIPLEXING (OWDM) AND OWDM-SPREAD SPECTRUM (OWSS) SIGNALING
SE-1616-WR	09/586571	02-Jun-2000	A DUAL PACKET CONFIGURATION FOR WIRELESS COMMUNICATIONS
SE-1618-WR	09/849053	04-May-2001	SYSTEM AND METHOD FOR SYNCHRONIZING DATA TRANSMISSION ACROSS AN INTERFACE WITH VARIABLE TIMING
SE-1619-WR	09/849101	04-May-2001	A SYSTEM AND METHOD FOR PROVIDING A SELECTABLE RETRY STRATEGY FOR FRAME-BASED COMMUNICATIONS
SE-1620-WR	09/849002	04-May-2001	A SYSTEM AND METHOD OF REPETITIVE TRANSMISSION OF FRAMES FOR FRAME-BASED COMMUNICATIONS
SE-1622-WR	09/677975	02-Oct-2000	A CALIBRATED DC COMPENSATION SYSTEM FOR A WIRELESS COMMUNICATION DEVICE CONFIGURED IN A ZERO INTERMEDIATE FREQUENCY ARCHITECTURE
SE-1623-WR	09/918409	30-Jul-2001	PACKET ACQUISITION AND CHANNEL TRACKING FOR A WIRELESS COMMUNICATION DEVICE CONFIGURED IN A ZERO INTERMEDIATE FREQUENCY ARCHITECTURE
SE-1627-WR	10/192037	10-Jul-2002	FORWARD ERROR CORRECTION SYSTEM FOR WIRELESS COMMUNICATIONS
SE-1628-WR	10/113743	02-Apr-2002	A FREQUENCY CORRECTION SYSTEM FOR A WIRELESS DEVICE COMMUNICATING IN A WIRELESS LOCAL AREA NETWORK
SE-1629-WR	10/027386	21-Dec-2001	PRECISION AUTOMATIC GAIN CONTROL CIRCUIT
SE-1631-WR	09/747163	21-Dec-2000	QUADRATURE OSCILLATOR WITH PHASE ERROR CORRECTION

Client Ref. No.	Application Number	Filing Date	Invention Title
SE-1641-WR	09/747138	21-Dec-2000	SYSTEM AND METHOD FOR DETECTING AND CORRECTING PHASE ERROR BETWEEN DIFFERENTIAL SIGNALS
SE-1670-WR	09/922084	03-Aug-2001	WIRELESS NETWORK SITE SURVEY TOOL
SE-1732-TD			SYMMETRIC INDUCTING DEVICE FOR AN INTEGRATED CIRCUIT HAVING A GROUND SHIELD
SE-1735-WR	09/943803	31-Aug-2001	SYSTEM AND METHOD FOR ORDERING DATA MESSAGES HAVING DIFFERING LEVELS OF PRIORITY FOR TRANSMISSION OVER A SHARED COMMUNICATION CHANNEL
SE-1741-WR	10/143126	10-May-2002	MIXED WAVEFORM CONFIGURATION FOR WIRELESS COMMUNICATIONS
SE-1742-WR	10/143134	10-May-2002	WIRELESS COMMUNICATION SYSTEM CONFIGURED TO COMMUNICATE USING A MIXED WAVEFORM CONFIGURATION
SE-1748-WR	10/172463	14-Jun-2002	RAPID ACQUISITION AND TRACKING SYSTEM FOR A WIRELESS PACKET-BASED COMMUNICATION DEVICE
SE-1749-WR	10/011580	04-Dec-2001	TRANSMIT FREQUENCY DOMAIN EQUALIZER
SE-1751-WR	10/121762	12-Apr-2002	INTELLIGENT CONTROL ALGORITHM FOR DECIDING WHEN TO EMPLOY COMPENSATION ALGORITHMS IN A WIRELESS INDOOR COMMUNICATIONS SYSTEM
SE-1752-WR	10/011794	04-Dec-2001	SOFT-DECISION GAIN COMPENSATION FOR RECEIVE FILTER ATTENUATION
SE-1754-WR	10/217506	13-Aug-2002	METHOD AND SYSTEM FOR DETERMINING TAP GAIN VALUES FOR A TRANSMIT FREQUENCY DOMAIN EQUALIZER TO ACHIEVE UNITY POWER GAIN
SE-1755-WR	10/024949	18-Dec-2001	AN EFFICIENT MULTI-RATE, CLOSED-LOOP TRANSMIT POWER CONTROL SCHEME FOR WIRELESS OFDM PACKET COMMUNICATIONS

Client Ref. No.	Application Number	Filing Date	Invention Title
SE-1758-WR	10/191901	09-Jul-2002	SINGLE-CARRIER TO MULTI-CARRIER WIRELESS ARCHITECTURE
SE-1760-BW	10/004548	04-Dec-2001	MULTIHOP, MULTI-CHANNEL, WIRELESS COMMUNICATION NETWORK WITH SCHEDULED TIME SLOTS
SE-1761-WR	10/191221	09-Jul-2002	SAMPLE RATE CHANGE BETWEEN SINGLE-CARRIER AND MULTI-CARRIER WAVEFORMS
SE-1766-WR	10/081045	22-Feb-2002	THRESHOLD DETECTOR FOR DETECTING SYNCHRONIZATION SIGNALS AT CORRELATOR OUTPUT DURING PACKET ACQUISITION
SE-1768-BW	10/006631	04-Dec-2001	FORWARDING COMMUNICATION NETWORK AND WIRELESS CHANNEL ALLOCATION METHOD THEREFOR
SE-1783-WR	10/295596	15-Nov-2002	TRANSMISSION PROTECTION FOR COMMUNICATIONS NETWORKS HAVING STATIONS OPERATING WITH DIFFERENT MODULATION FORMATS
SE-1789-TD			INDUCTOR DEVICE WITH PATTERNED GROUND SHIELD AND RIBBING
SE-1790-TD			SOLID STATE INDUCTING DEVICE
SE-1795-WR	10/383339	07-Mar-2003	SHARED-COMMUNICATIONS CHANNEL UTILIZATION FOR APPLICATIONS HAVING DIFFERENT CLASS OF SERVICE REQUIREMENTS
SE-1798-WR	60/449042	21-Feb-2003	FAST OFDM CARRIER TRACKER
SE-1877-WR	10/421265	23-Apr-2003	PARTITIONED MEDIUM ACCESS CONTROL
SE-1878-WR		22-May-2003	NETWORK ACCESS POINT FOR PROVIDING MULTIPLE LEVELS OF SECURITY



Client Ref. No.	Application Number	Filing Date	Invention Title
SE-1879			NETWORK ACCESS POINT FOR PROVIDING MULTIPLE LEVELS OF SECURITY
SE-1886-WR	10/377324	28-Feb-2003	TRANSMIT POWER MANAGEMENT IN SHARED-COMMUNICATIONS CHANNEL NETWORKS
SE-1889-WR	10/353391	29-Jan-2003	DIRECT LINK PROTOCOL IN WIRELESS LOCAL AREA NETWORKS
SE-1890-WR		23-May-2003	MULTI-PROTOCOL INTERCHIP INTERFACE
SE-1895-WR		23-May-2003	COORDINATION OF COMPETING PROTOCOLS
SE-1896-WR	10/424803	26-Apr-2003	SECURE TELECOMMUNICATIONS SYSTEM FOR WIRELESS LOCAL AREA NETWORKS
SE-1897-WR	10/273799	18-Oct-2002	EFFICIENCY IMPROVEMENT FOR SHARED COMMUNICATIONS NETWORKS
SE-1904-WR	10/321116	17-Dec-2002	HIGH FREQUENCY DIFFERENTIAL VOLTAGE CONTROLLED OSCILLATOR
SE-1905-WR	60/396691	17-Jul-2002	DISTRIBUTED, DYNAMIC ASSIGNMENT OF STATION MAC ADDRESSES FOR (WIRELESS) LAN
SE-1907-WR	10/338362	08-Jan-2003	ADAPTIVE GAIN AND PHASE IMBALANCE CANCELLATION
SE-1908-WR	60/449042	21-Feb-2003	FAST OFDM CARRIER TRACKER
SE-1909-WR	10/324218	19-Dec-2002	WIRELESS RECEIVER FOR SORTING PACKETS

Client Ref. No.	Application Number	Filing Date	Invention Title
SE-1911-WR	10/306020	27-Nov-2002	WEAVER MODULATOR AND DEMODULATOR IMPLEMENTATION
SE-1912-WR	10/376411	28-Feb-2003	EMBEDDING CLASS OF SERVICE INFORMATION IN MAC CONTROL FRAMES
SE-1915-WR		29-May-2003	ADAPTIVE TRANSMISSION RATE AND FRAGMENTATION THRESHOLD MECHANISM FOR LOCAL AREA NETWORKS
SE-1916-WR		29-May-2003	A MECHANISM FOR SHARING A DIVERSITY ANTENNA SYSTEM BETWEEN COLOCATED 802.11 AND BLUETOOTH RADIOS
SE-1917-WR	10/421275	23-Apr-2003	ACHIEVING HIGH PRIORITY AND BANDWIDTH EFFICIENCY IN A SHARED COMMUNICATIONS MEDIUM
SE-1920-WR	10/407350	04-Apr-2003	FASTER ZIF AGC/DC ALGORITHM
SE-1922-WR	60/450287	27-Feb-2003	LOW POWER BARKER-MODULATED SIGNAL DETECTOR
SE-1923-WR	10/421230	23-Apr-2003	LIMITED CONTENDER QUEUE (LCQ)
SE-1927-WR	10/421274	23-Apr-2003	OPTIMIZATION OF TRANSMISSIONS ON A SHARED COMMUNICATIONS CHANNEL
SE-1931-WR	60/438829	09-Jan-2003	COMPLEX MATCHING CIRCUIT TO SUPPRESS SECOND HARMONIC ENERGY IN DUAL BAND POWER AMPLIFIERS
SE-1931-WR	60/436016	20-Dec-2002	COMPLEX MATCHING CIRCUIT TO SUPPRESS SECOND HARMONIC ENERGY IN DUAL BAND POWER AMPLIFIERS
SE-1938-WR	60/446328	10-Feb-2003	UPPER-TO-LOWER INTERFACE BUS

Client Ref. No.	Application Number	Filing Date	Invention Title
SE-1939-WR	60/452309	05-Mar-2003	A MECHANISM FOR COLLABORATION AND INTERFERENCES PREVENTION BETWEEN 802.11 AND BLUETOOTH THROUGH ADVANCE REQUEST FOR ACCESS FROM BLUETOOTH TO 802.11
SE-1940-WR	60/452310	05-Mar-2003	A MECHANISM FOR COLLABORATION AND INTERFERENCE PREVENTION BETWEEN 802.11 AND BLUETOOTH THROUGH ADVANCE NOTIFICATION OF IDLE TIME FROM 802.11 MAC
SE-1941-WR	60/439697	14-Jan-2003	DYNAMIC ORTHOGONAL FREQUENCY DIVISION MULTIPLEXING PROTECTION METHODS FOR ACCESS POINTS
SE-1942-WR	60/436157	23-Dec-2002	RF POWER AMPLIFIER BIAS MANAGEMENT SYSTEM
SE-1948-WR	60/443076	28-Jan-2003	ANTENNA DIVERSITY METHOD
SE-1949-WR	60/443077	28-Jan-2003	SOFTWARE-BASED ANTENNA DIVERSITY ALGORITHM FOR WLAN APPLICATIONS
SE-1951-WR	60/453613	11-Mar-2003	INTER-RADIO SIGNALLING WITH A HOST COMPUTER
SE-1952-WR		30-May-2003	TECHNIQUE FOR ACHIEVING CONNECTIVITY BETWEEN TELECOMMUNICATION STATIONS
SE-1957-WR	60/446339	10-Feb-2003	PARTIAL QUEUING USING AN INTERFACE WITH BOUNDED LATENCY
SE-1958-WR			TECHNIQUE FOR STEERING AN ANTENNA SYSTEM
SE-1959-WR	60/449040	21-Feb-2003	FLICKER (1/f) NOISE CANCELLER FOR USE IN WIDEBAND COMMUNICATIONS SYSTEMS
SE-1959-WR	60/463938	18-Apr-2003	FLICKER (1/f) NOISE CANCELLER FOR USE IN WIDEBAND COMMUNICATIONS SYSTEMS

Client Ref. No.	Application Number	Filing Date	Invention Title
SE-1961-WR	60/455322	17-Mar-2003	BACKOFF SUBSTITUTION ON A SHARED COMMUNICATION CHANNEL
SE-1962-WR	60/454539	13-Mar-2003	ON-NIC FLASH STORAGE FOR NIC DRIVER
SE-1967-WR	10/377323	28-Feb-2003	EMBEDDING CLASS OF SERVICE INFORMATION IN MAC CONTROL FRAMES
SE-1969-WR			VARIABLE-WIDTH MODULATION SCHEMES FEATURING BACKWARD-COMPATIBLE LOCAL AREA NETWORK OPERATION
SE-1970-WR	60/464598	22-Apr-2003	TUNING 802.11a FOR NARROW CHANNELS
SE-1973-WR	60/455323	17-Mar-2003	TECHNIQUE FOR STEERING AN ANTENNA SYSTEM
SE-1973-WR	60/448527	18-Feb-2003	TECHNIQUE FOR STEERING AN ANTENNA SYSTEM
SE-1974-WR		08-Apr-2003	WIRELESS LAN RADAR DETECTOR MISSING PULSE CAPABILITY
SE-1975-WR		09-May-2003	VARIABLE-WIDTH MODULATION SCHEMES FEATURING BACKWARD-COMPATIBLE LOCAL AREA NETWORK OPERATION
SE-1976-WR		26-Mar-2003	DCT PROGRESSIVE RESERVATION MECHANISM (DPRM)
SE-1984-WR		08-Jul-2003	ADAPTIVE FREQUENCY EQUALIZER
SE-1990-WR	10/421264	23-Apr-2003	SHARED-MEDIUM CONTENTION ALGORITHM EXHIBITING FAIRNESS

Client Ref. No.	Application Number	Filing Date	Invention Title
SE-1992-WR		29-May-2003	ADAPTIVE TRANSMISSION RATE AND FRAGMENTATION THRESHOLD MECHANISM FOR LOCAL AREA NETWORKS
SE-2000-WR	60/xxx,xxx	30-Jul-2003	USE OF OUI TO CONVEY INFORMATION IN A WIRELESS NETWORK
SE-2001-WR			INTELLIGENT DOWNSTREAM TRAFFIC DELIVERY TO MULTI-PROTOCOL STATIONS
SE-2002-WR		30-Jul-2003	MANAGING COEXISTENCE OF SEPARATE PROTOCOLS SHARING THE SAME COMMUNICATIONS CHANNEL
SE-2003-WR		30-Jul-2003	MANAGING VIA AN ACCESS POINT COEXISTENCE OF SEPARATE PROTOCOLS SHARING THE SAME COMMUNICATIONS CHANNEL

Issued U.S. Patents

Client Ref. No.	Application Number	Filing Date	Patent Number	Issue Date	Invention Title
SE-1076-WR	08/873899	13-Jun-1997	5896053	20-Apr-1999	SINGLE ENDED TO DIFFERENTIAL CONVERTER AND 50% DUTY CYCLE SIGNAL GENERATOR AND METHOD
SE-1147-TL	08/509462	31-Jul-95	5712870	27-Jan-98	PACKET HEADER GENERATION AND DETECTION CIRCUITRY
SE-1148-WR	08/509588	31-Jul-1995	5654991	05-Aug-1997	FAST ACQUISITION BIT TIMING LOOP CIRCUIT METHOD AND APPARATUS
SE-1149-WR	08/509587	31-Jul-1995	5883921	16-Mar-1999	SHORT BURST ACQUISITION CIRCUIT AND METHOD FOR DIRECT SEQUENCE SPREAD SPECTRUM LINKS
SE-1150-WR	08/509586	31-Jul-1995	5732105	24-Mar-1998	A METHOD OF ESTIMATING SIGNAL QUALITY FOR A DIRECT SEQUENCE SPREAD SPECTRUM RECEIVER DEVICE
SE-1151-WR	08/509589	31-Jul-1995	5675339	07-Oct-1997	A/D REFERENCE LEVEL ADJUSTMENT CIRCUIT TO MAINTAIN OPTIMUM DYNAMIC RANGE AT THE A/D
SE-1153-WR	08/512405	08-Aug-1995	5606288	25-Feb-1997	DIFFERENTIAL TRANSIMPEDANCE AMPLIFIER
SE-1164	637132	24-Apr-1996	5717243	10-Feb-1998	INTEGRATED CIRCUIT WITH AN IMPROVED INDUCTOR STRUCTURE AND METHOD OF FABRICATION
SE-1234-WR	08/725924	04-Oct-1996	5812029	22-Sep-1998	GAIN CONTROL CIRCUIT AND METHOD
SE-1301-WR	08/819846	17-Mar-1997	5982807	09-Nov-1999	HIGH DATA RATE SPREAD SPECTRUM TRANSCEIVER AND ASSOCIATED METHODS

Client Ref. No.	Application Number	Filing Date	Patent Number	Issue Date	Invention Title
SE-1322-WR	09/246815	09-Feb-1999	5999080	07-Dec-1999	FREQUENCY DEPENDENT RESISTIVE ELEMENT
SE-1322-WR	08/941704	01-Oct-1997	5883565	16-Mar-1999	FREQUENCY DEPENDENT RESISTIVE ELEMENT
SE-1407-WR	09/231608	14-Jan-1999	6563858	13-May-2003	METHOD OF PERFORMING ANTENNA DIVERSITY IN SPREAD SPECTRUM IN WIRELESS LOCAL AREA NETWORK
SE-1453-WR	09/163802	30-Sep-1998	6377608	23-Apr-2002	PULSED BEACON-BASED INTERFERENCE REDUCTION MECHANISM FOR WIRELESS COMMUNICATION NETWORKS
SE-1454-WR	09/378532	20-Aug-1999	6577670	10-Jun-2003	PROGRAMMABLE FILTERING MECHANISM TO ALLOW BANDWIDTH OVERLAP BETWEEN DIRECT SEQUENCE SPREAD SPECTRUM COMMUNICATION DEVICE AND FREQUENCY-HOPPING TRANSMITTER
SE-1479-WR	09/342583	29-Jun-1999	6233273	15-May-2001	RAKE RECEIVER WITH EMBEDDED DECISION FEEDBACK EQUALIZER
SE-1486-WR	09/261981	04-Mar-1999	6172556	09-Jan-2001	FEEDBACK-CONTROLLED LOW VOLTAGE CURRENT SINK/SOURCE
SE-1489-WR	09/571822	16-May-2000	6346856	12-Feb-2002	AN ULTRA LINEAR HIGH FREQUENCY TRANSCONDUCTOR STRUCTURE
SE-1510-WR	09/612848	10-Jul-2000	6452452	17-Sep-2002	NEGATIVE FEEDBACK GAIN CONTROL FOR COMMON ELECTRODE TRANSISTOR
SE-1519-WR	09/572417	17-May-2000	6405022	11-Jun-2002	APPARATUS FOR RADIO FREQUENCY PROCESSING WITH SINGLE OSCILLATOR FOR INTERMEDIATE FREQUENCY PROCESSING
SE-1621-WR	09/678901	02-Oct-2000	6560448	06-May-2003	DC COMPENSATION SYSTEM FOR A WIRELESS COMMUNICATION DEVICE CONFIGURED IN A ZERO INTERMEDIATE FREQUENCY ARCHITECTURE

Client Ref. No.	Application Number	Filing Date	Patent Number	Issue Date	Invention Title
SE-1632-WR	09/746736	21-Dec-2000	6580591	17-Jun-2003	SWITCHED ELECTROSTATIC DISCHARGE RING FOR INTEGRATED CIRCUITS WITH MULTIPLE POWER INPUTS
SE-1633-WR	09/952184	14-Sep-2001	6426677	30-Jul-2002	LINEARIZATION BIAS CIRCUIT FOR BJT AMPLIFIERS
SE-1642-WR	10/007479	03-Dec-2001	6600372	29-Jul-2003	ATTENUATOR CONTROL CIRCUIT
SE-1659-WR	09/943668	31-Aug-2001	6570427	27-May-2003	A VARIABLE TRANSCONDUCTANCE AMPLIFIER
SE-1664-WR	09/911060	23-Jul-2001	6529047	04-Mar-2003	MIXER DRIVER CIRCUIT
SE-1669-WR	10/005096	03-Dec-2001	6538513	25-Mar-2003	COMMON MODE OUTPUT CURRENT CONTROL CIRCUIT AND METHOD
SE-1673-WR	09/944000	31-Aug-2001	6573802	03-Jun-2003	SINGLE-ENDED TO DIFFERENTIAL AND DIFFERENTIAL TO SINGLE-ENDED CONVERSION USING A COMMON MODE SHUNT
SE-1696-WR	10/079983	21-Feb-2002	6538507	25-Mar-2003	AN AUTOMATIC GAIN CONTROL CIRCUIT WITH HIGH LINEARITY AND MONOTONICALLY CORRELATED OFFSET VOLTAGE
SE-1630-WR	09/747321	21-Dec-2000	6344762	05-Feb-2002	BIAS CIRCUIT FOR A LOW VOLTAGE DIFFERENTIAL CIRCUIT