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To the Honorable Commissioner for Patents and Trademarks: Please record the attached original document or copy thereof.

1. Name(s) of conveying party(ies):

1) DAVID RESNICK

2)
3)
4)
5)
6)

10-17-07

Additional names of conveying parties attached? ☐ Yes ☒ No

3. Nature of conveyance:

☒ Assignment ☐ Merger

☐ Security Agreement ☐ Change of Name

☐ Other _____

Execution Dates:

1) SEPTEMBER 25, 2007 4) _____
2) _____ 5) _____
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2. Name and address of receiving party:

Name: MICRON TECHNOLOGY, INC.

Internal Address: _____

Street Address: 8000 SOUTH FEDERAL WAY

P.O. BOX 6 / 83707-0006

City: BOISE State: IDAHO

Zip: 83716-9632

Additional names & addresses attached? ☐ Yes ☒ No

4. Application number(s) or patent number(s): _____ This document is being filed together with a new application.

A. Patent Application No(s).

11/893,593 FILED AUGUST 15, 2007

B. Patent No(s).

Additional numbers attached? ☐ Yes ☒ No

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: DORSEY & WHITNEY LLP

Internal Address: EDWARD W. BULCHIS

SUITE 3400

Street Address: 1420 FIFTH AVENUE

City: SEATTLE State: WA ZIP: 98101

6. Total number of applications and patents involved.....

One (1)

7. Total Fee (37 CFR 3.41): \$40

☒ Enclosed

☐ Authorized to be charged to deposit account

8. Deposit account number:

50-1266

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9. Statement and signature.

EDWARD W. BULCHIS

Name of Person Signing

Signature

Date

Total number of pages including cover sheet, attachments, and document: 3

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

David Resnick

Serial No.: 11/893,593

Filed : August 15, 2007

For : MEMORY DEVICE AND METHOD
HAVING ON-BOARD PROCESSING
LOGIC FOR FACILITATING
INTERFACE WITH MULTIPLE
PROCESSORS, AND COMPUTER
SYSTEM USING SAME

Docket No. 446602-2942/US01P (446602-
657.02)

Disclosure No. 2007-0812

Group Art Unit 2186

ASSIGNMENT:

☒ Enclosed for recording

☐ Previously recorded

Date: _____

Reel: _____

FOR GOOD AND VALUABLE CONSIDERATION, the receipt, sufficiency and adequacy of which are hereby acknowledged, the undersigned does hereby:

SELL, ASSIGN AND TRANSFER to **Micron Technology, Inc.** (the "Assignee"), a corporation of Delaware, having a place of business at 8000 South Federal Way, Boise, Idaho 83716-9632, the entire right, title and interest for the United States and all foreign countries, in and to any and all improvements which are disclosed in the application for United States Letters Patent, which was filed on August 15, 2007, and assigned Application No. 11/893,593 and is entitled "MEMORY DEVICE AND METHOD HAVING ON-BOARD PROCESSING LOGIC FOR FACILITATING INTERFACE WITH MULTIPLE PROCESSORS, AND COMPUTER SYSTEM USING SAME"; such application and all divisional, continuing, substitute, renewal, reissue and all other applications for patent which have been or shall be filed in the United States and all foreign countries on any of such improvements; all original and reissued patents which have been or shall be issued in the United States and all foreign countries on such improvements; and specifically including the right to file foreign applications under the provisions of any convention or treaty and claim priority based on such application in the United States of America;

