

PATENT ASSIGNMENT

Electronic Version v1.1

Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	Exclusive License
CONVEYING PARTY DATA	
Name	Execution Date
INDUSTRIAL TECHNOLOGY RESEARCH INSTITUTE	09/29/2005
RECEIVING PARTY DATA	
Name:	MEDIATEK INC.
Street Address:	No. 1, Dusing Rd. 1st
Internal Address:	Science-Based Industrial Park
City:	Hsin-Chu
State/Country:	TAIWAN
Postal Code:	300, R.O.C.
PROPERTY NUMBERS Total: 22	
Property Type	Number
Patent Number:	5268682
Patent Number:	5389948
Patent Number:	5714974
Patent Number:	5321425
Patent Number:	5589850
Patent Number:	6008782
Patent Number:	5751290
Patent Number:	6104413
Patent Number:	6377273
Patent Number:	5268681
Patent Number:	5140544
Patent Number:	5422657
Patent Number:	6057861
Patent Number:	5963220

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PATENT
REEL: 020010 FRAME: 0148

Patent Number:	5933147
Patent Number:	5745739
Patent Number:	5924111
Patent Number:	5946005
Patent Number:	6263410
Patent Number:	6321301
Patent Number:	5918057
Patent Number:	5905897

CORRESPONDENCE DATA

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ATTORNEY DOCKET NUMBER:	251314-9011
NAME OF SUBMITTER:	Daniel R. McClure

Total Attachments: 19

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Partially Literal Translation of Exclusive License Agreement

Patent Exclusive License Agreement

This Patent Exclusive License Agreement ("Agreement") is entered into by and between MediaTek Inc. (hereinafter referred as "MTK") and Industrial Technology Research Institute (hereinafter referred as "ITRI"). NOW THEREFORE, in consideration of granting license of specific patents contained herein, and intending to be legally bound thereby, the parties agree as follows:

Article 1

Article 2 Definition

1. "Patent" shall mean the "Patent license list" set forth in Attachment I (1).
2. "Products" shall mean products manufactured or assembled by MTK, by using part or all of the Scope of the Exclusive License, or by using part or all of technological content of the Scope of the Exclusive License. These products are hereinafter referred to collectively as "Products".

Article 3 Content of the Exclusive License

1. "Scope of the Exclusive License" shall mean ITRI exclusively licenses MTK to practice the patent right within computer and communication (including Optical Storage, Consumer Electronics) products (/technology) field, provided that ITRI agrees to give prior notice to MTK, if ITRI intends to license the third party to practice the Patent in other products (/technology) field, and in this case, ITRI undertakes that MTK will have priority right to negotiate license conditions with ITRI alone.
2. "Exclusive License Items " shall mean MTK is licensed to use, practice the Patent, practice the Patent to manufacture, make an offer for sale, sell, use Products, or import for above purposes the Products, and sublicense the above rights to the third party. When MTK sublicenses the above rights to the third party, MTK shall promptly notify in written the information of such sublicensee and conditions of sublicense to ITRI. Such sublicensee shall be liable for the same obligations as MTK will be liable under the Agreement, including complying with confidential provision of the Agreement, Act Governing Relations between Peoples of the Taiwan Area and the Mainland Area and its pertaining regulations.
3. "Term of Exclusive License" shall mean the period from effective date of the Agreement to the expiration of the patent term.
5. ITRI is entitled to the patentee's rights, including but not limited to practice right, and the right to claim the patent against the third party. ITRI has the right to enter into agreement with the third party for licensing part of the Patent which is beyond the item 1 of this Article of the Scope of the Exclusive License.

Article 4-15

Article 16 Term of the Agreement

1. The Agreement is duly signed by MTK and ITRI. Term of Exclusive License is from effective date on this Agreement to the last expiration of the patent term set forth in Attachment I(1).
2.
3.

Article 17-20

Licensee: MediaTek Inc. (Stamp)

Representative: Ming-Kai TSAI (Stamp)

Title: Chairman

Address: No. 1, Dusing Rd. 1st, Science-Based Industrial Park, Hsin Chu 300, Taiwan, R.O.C.

Telephone No.:03-567-0766

Company ID number: 84149961

Licensor: Industrial Technology Research Institute (Stamp)

Representative: Johnsee LEE (Stamp)

Title: President

Address: No. 195, Chung Hsing Rd., Sec. 4, Chu Tung Township, Hsin Chu 310, Taiwan, R.O.C.

Company ID number: 02750963

Execution Date: September 29, 2005

Attachment I

(1) Patent License List

Invention Number	Country	Title of the Invention	Patent No.	The Effective Date of Patent Term	The Expiration Date of Patent Term
GR-2-20	JP	Resolution independent raster display system	3241769	20011019	20111128
GR-2-21	TW	Resolution independent raster display system	57484	19920711	20120211
GR-2-22	US	Resolution independent raster display system	5,268,682	19931207	20111006
GR-2-23	TW	Dithering circuit and method	69369	19941221	20120226
GR-2-24	US	Dithering circuit and method	5,389,948	19950214	20120213
GR-2-25	TW	Dithering method and circuit using dither matrix rotation	69198	19941211	20120226
GR-2-26	US	Dithering method and circuit using dither matrix rotation	5,714,974	19980203	20150203
GR-2-27	DE	Resolution independent screen refresh strategy	P4217010.9	19970220	20120521
GR-2-28	JP	Resolution independent screen refresh strategy	3100251	20000818	20130202
GR-2-29	TW	Resolution independent screen refresh strategy	65108	19940201	20120528
GR-2-30	US	Resolution independent screen refresh strategy	5,321,425	19940614	20120218
GR-2-31	TW	Apparatus for converting two-dimensional pixel image into one-dimensional pixel array	71281	19950321	20140306
GR-2-32	US	Apparatus for converting two-dimensional pixel image into one-dimensional pixel array	5,589,850	19961231	20140925
GR-2-33	TW	Video compression method and device	116358	20000611	20140906
GR-2-34	JP	Mapping apparatus for use with a cathode-ray tube controller for generating special screen effects	3335824	20020802	20151123

GR-2-35	TW	Mapping apparatus for use with a cathode-ray tube controller for generating special screen effects	86859	19970511	20150724
GR-2-36	US	Mapping apparatus for use with a cathode-ray tube controller for generating special screen effects	6,008,782	19991228	20161227
GR-2-37	TW	Three dimensional graphics system and method with correction to z buffer value errors	108690	19991011	20161021
GR-2-38	US	Three dimensional graphics system and method with correction to z buffer value errors	5,751,290	19980512	20160919
GR-2-39	TW	Methods and systems for storing texels retrievable in a single cycle	118155	20000621	20180427
GR-2-40	US	Methods and systems for storing texels retrievable in a single cycle	6,104,413	20000815	20180310
GR-2-41	TW	Fast area-coverage computing method for anti-aliasing in graphics	140303	20010821	20190225
GR-2-42	US	Fast area-coverage computing method for anti-aliasing in graphics	6,377,273	20020423	20181103
GR-2-43	TW	Memory architecture with graphics generator including a divide by five divider	57468	19920721	20110929
GR-2-44	US	Memory architecture with graphics generator including a divide by five divider	5,268,681	19931207	20111006
GR-2-45	TW	Divide-by-five divider	57201	19920621	20110929
GR-2-46	US	Divide-by-five divider	5,140,544	19920818	20111006
GR-2-47	DE	Graphics memory architecture for multimode display system	4431304	19980219	20140901
GR-2-48	TW	Graphics memory architecture for multimode display system	84986	19970301	20131028
GR-2-49	US	Graphics memory architecture for multimode display system	5,422,657	19950606	20130912

GR-2-50	US	Mip map/rip map texture linear addressing memory organization and address generator	6,057,861	20000502	20190601
GR-2-51	JP	Mip map/rip map texture linear addressing memory organization and address generator	2894989	19990305	20160602
GR-2-52	TW	Mip map/rip map texture linear addressing memory organization and address generator	82090	19961001	20160109
GR-2-53	US	Mip map/rip map texture linear addressing memory organization and address generator	5,963,220	19991005	20160207
GR-2-54	TW	A computer graphics memory architecture which processes a full pixel of 3d color and z value data in a single i/o transaction	119299	20000801	20151217
GR-2-55	US	A computer graphics memory architecture which processes a full pixel of 3d color and z value data in a single i/o transaction	5,933,147	19990803	20150920
GR-2-56	TW	Virtual coordinate to linear physical memory address converter for computer graphics system	86410	19970501	20160204
GR-2-57	US	Virtual coordinate to linear physical memory address converter for computer graphics system	5,745,739	19980428	20160207
GR-2-58	TW	Method and system for interleaving data in multiple memory bank partitions	90885	19971111	20160325
GR-2-59	US	Method and system for interleaving data in multiple memory bank partitions	5,924,111	19990713	20151016
GR-2-60	TW	Computer graphics memory architecture having a graphics processor and a buffer	103333	19990501	20171026
GR-2-61	US	Computer graphics memory architecture having a graphics processor and a buffer	5,946,005	19990831	20170826

GR-2-62	TW	Apparatus and method for asynchronous dual port fifo	122485	20000921	20180922
GR-2-63	US	Apparatus and method for asynchronous dual port fifo	6,263,410	20010717	20180914
GR-2-64	TW	Cache memory device with prefetch function and method for asynchronously renewing tag addresses and data during cache miss states	136294	20010616	20190505
GR-2-65	US	Cache memory device with prefetch function and method for asynchronously renewing tag addresses and data during cache miss states	6,321,301	20011120	20190811
MP-1-74	TW	Method and apparatus for dispatching multiple interrupt requests simultaneously	95469	19980621	20170425
MP-1-75	US	Method and apparatus for dispatching multiple interrupt requests simultaneously	5,918,057	19990629	20170319
MP-1-76	TW	Method and apparatus for selecting a nonblocked interrupt request	99391	19981201	20170508
MP-1-77	US	Method and apparatus for selecting a nonblocked interrupt request	5,905,897	19990518	20170319

(2).....

(3).....

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專利專屬授權契約書

立約人：聯發科技股份有限公司(以下簡稱「甲方」)及財團法人工業技術研究院(以下簡稱「乙方」)。緣甲乙雙方為特定專利之授權事，特立本契約，並同意條件如下：

正本

第一條：雙方合意

- 一、甲方知悉乙方係受經濟部委託執行分散式資訊處理技術發展五年計畫，獲得如附件一之特定專利。乙方同意依下列條款授權予甲方；甲方同意依下列條款，承受乙方授權實施特定專利之權利。
- 二、甲乙雙方同意遵守「經濟部推動研究機構開發產業技術辦法」及「經濟部及所屬各機關科學技術委託或補助研究發展計畫研發成果歸屬及運用辦法」之規定。前述法令變動時，亦同。
- 三、甲方應詳實填妥「廠家基本資料表」(如附件二)。

第二條：名詞定義

- 一、本專利：係指附件一第一項「專利授權清單」
- 二、本產品：係指甲方使用本專利專屬授權範圍之一部或全部所製造或組裝之產品，或由甲方利用或修改本專利專屬授權範圍之一部或全部之技術內容所產生之技術/專利所製造或組裝之產品，於本契約中統稱為「本產品」。

第三條：專屬授權內容

- 一、專屬授權範圍：以電腦與通訊(包含光儲存與消費性電子)產品(/技術)領域為限專屬授權甲方實施本專利。但乙方同意就本專利如欲授權他人於其他產品(/技術)領域實施時，應事先通知甲方，且承諾優先接洽甲方就授權條件單獨進行議約談判。
- 二、專屬授權項目：甲方得於專屬授權範圍內使用、實施本專利、及實施本專利製造、為販賣之要約、販賣、使用本產品，或為上述目的進口本產品及將上述權利再授權予其他第三人。惟甲方再授權予第三人時，應立即將授權廠商之資料及授權詳細條件以書面通知乙方。甲方將本條第一項權利再授權予第三人時，各該第三人應與甲方就本契約負同一義務，包括但不限於遵守契約保密義務、台灣地區與大陸地區人民關係條例及其子法之規定等。
- 三、專屬授權期間：自本契約生效之日起至各該本專利之專利期間屆滿為止。

影本

- 五、 乙方享有專利法上專利權人之權利，包括但不限於實施權及對第三人主張專利權之權利。乙方於第一項授權範圍外，得再將本專利與第三人訂立授權契約。

第四條：授權費用及付款方式

一、 授權費用

REDACTED

二、 付款方式

甲方應於簽訂本契約後十五日內一次全額支付前條專利授權費及營業稅予乙方。

- 三、 甲方依本契約應支付乙方之款項應依乙方之要求以現金、銀行本票或支票、或其他方式支付之，且其內容、形式及交付方式亦應符合乙方之要求。

- 四、 甲方依本契約應支付乙方之款項應以新台幣計付之。

- 五、 除契約另有訂定者外，雙方應各自根據相關法律負擔為完成本契約交易所應繳納之各項稅捐與規費。

第五條：專利維護及授權登記

- 一、 本專利仍屬乙方所有，但未經甲方同意，乙方不得將之任意放棄或讓與予他人。甲方同意乙方自行決定本專利得為專利法第六十四條之申請更正，惟應先通知甲方申請更正之理由；倘若申請更正之理由係為申請專利範圍之減縮，則應先經甲方同意，始得為之。

- 二、 本專利專屬授權登記手續應由甲方負責辦理，乙方應提供必要之協助，且登記所須之一切費用悉由甲方負擔。

- 三、 本專利專屬授權期間因維護程序所生之維護年費全部由甲方負擔，但為確保乙方之權益，甲方同意委託乙方進行專利維護等相關程序，並同時負擔專利維護年費及相關程序費用；乙方應盡善良管理人注意義務為甲方之利益為本專利之維護。

- 四、 雙方同意本條第三項費用給付之具體條件，另以服務契約書訂定之。

第六條：無擔保條款

乙方不擔保本專利不侵害第三人之智慧財產權，亦不擔保本專利之有效



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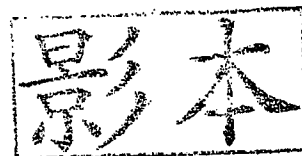
性、合用性及商品化之可能性，且不為其他擔保。甲方或第三人因本專利發生任何損害時，乙方不負擔任何責任，包括相關侵權與瑕疵擔保責任。惟乙方同意若知悉任一本專利被舉發時，應立即通知甲方，並提供必要之答辯，以履行本契約第五條第三項規定之善良管理人注意義務。

第七條：保密義務

- 一、甲方應以善良管理人之注意，妥善保管本契約及其附件，並採取適當之保密措施，且非經乙方事前書面之同意，不得洩漏或交付任何第三人。
- 二、甲方應負責要求其員工或關係企業及其員工遵守本條之約定。甲方之員工或關係企業及其員工違反本項約定者，視為甲方違反本項之約定。

第八條：使用限制

- 一、甲方於國內外販賣本產品時，應依當地法律規定在本產品或其包裝上附加如附件一之專利標示與專利證書號數。
- 二、未獲得乙方事先書面同意，甲方不得於商業推廣時(如：廣告、產品/投資說明等)使用乙方、乙方之員工及乙方所屬各單位相同或類似之名稱、徽章、商標及其他標章。
- 三、甲方承諾輸出本產品時應遵守中華民國戰略性高科技產品出口管制相關之規定。
- 四、甲方應負責要求其員工、其關係企業及其員工、其經銷商及其員工、或其代理商及其員工遵守本條之約定。甲方之員工、關係企業及其員工、經銷商及其員工、或代理商及其員工違反本條約定者，視為甲方違反本條約定。
- 五、基於尊重智慧財產並維護合法授權者權利之前提，甲方得對第三人主張專屬授權人之權利。除甲方欲對中華民國境內第三人就本專利主張其權利時，應事前取得乙方書面同意外，甲方得任意對中華民國境外第三人就本專利主張其權利，無須事先通知乙方或取得乙方同意，乙方亦不得異議之。
- 六、甲方或其再授權之第三人因使用或實施本專利遇他人請求或被訴任何智慧財產權侵權情事，或因發現本專利被侵害，甲方或其授權之第三人欲主張權利或提起訴訟請求時，乙方同意盡力協助並配合渠等提供技術鑑定、諮詢或其他與訴訟相關等支援，惟甲方應支付服務費用予乙方，雙方同意該協助之方式、範圍及費用等相關條件，以另約約定之。如因甲方訴訟之需要，乙方需一併列為當事人，則乙方所有相關訴訟費用由甲方負擔。



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第九條：轉讓限制

除法令或契約另有訂定者外，任一方於本契約中一部或全部之權利及義務，非經他方事前書面同意，不得轉讓予任何第三人。

第十條：專案管理

REDACTED

第十一條：特約條款

- 一、 甲方如有下列各款情事之一時，經乙方以書面通知三十日改善仍未改善者，乙方得終止甲方專屬授權之權利：
- (一) 甲方於本契約有效期間無正當理由未有效運用本專利，經第三人向乙方或經濟部提出授權申請者。
 - (二) 甲方以妨礙環境保護、公共安全或公共衛生之方式實施本專利者。



(三) 為增進國家重大利益者。

- 二、 如有前項各款情事之一發生時，乙方得以書面通知終止甲方專屬授權之權利，改以非專屬授權方式授權甲方，且乙方已收取之金額無需返還。

第十二條：契約終止事由及違約效果

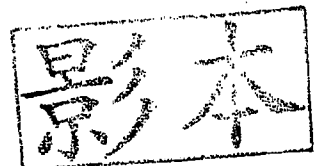
- 一、 甲方不履行本契約相關費用給付義務之約定或不照該約定履行時，甲方應就遲延給付之金額，依年利率百分之十(10%)按實際遲延日數(含星期例假日；不足一日者，以一日計)計付遲延利息予乙方。甲方遲延支付乙方款項累積達三十日以上時，乙方得以書面通知終止本契約。
- 二、 甲方不履行第七條、第八條之約定或不照該約定履行時，乙方得以書面終止本契約，及返還就此所得之一切利益且應另行賠償因此所生之一切損害。
- 三、 任一方不履行其他條款約定或不照各該約定履行時，他方得以書面通知他方於三十日內改正。逾期未改正者，違約方除應返還就此所得之一切利益且應另行賠償無違約方因此所生之一切損害外，無違約方得另以書面通知終止本契約。
- 四、 甲方重整或申請或被申請重整；解散或決議解散或被命令或裁定解散；被合併或決議合併而被消滅；破產或申請或被申請宣告破產；主要資產被查封；無法償還債務；或有相當事實足證有發生本項情事之虞時，乙方得以書面通知終止本契約。

第十三條：終止效果

- 一、 本契約終止、解除或有效期間屆滿，甲方應立即停止行使其對本專利及本產品所得行使之權利。
- 二、 於本契約若因可歸責甲方之事由終止、解除時，甲方之前已給付乙方之金額無需返還甲方；本契約若因可歸責乙方之事由終止、解除時，乙方因本契約而致依本契約之約定及依有關法令之規定所應負擔之責任之全部累計之責任範圍，仍以乙方依本契約實際受領自甲方受領之授權費用總額百分之十為上限，且依約免責之部份仍應免責。

第十四條：責任上限

任一方因本契約而致依本契約之約定及依有關法令之規定所應負擔之責任之全部累計之責任範圍以本契約第四條所規定之授權費用總額百分之十為上限，且依約免責之部份仍應免責，但甲方依本契約第四條及第五條之給付義務無前述責任上限之適用。



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第十五條：契約修改

本契約之增刪或修改，非經契約當事人以書面協議之方式為之修改，不生效力。

第十六條：契約期間

- 一、本契約經雙方依法簽章，授權期間自本契約生效之日起，至附件一
所列專利期間最後屆滿之日止。
- 二、甲乙雙方同意於本契約有效期間內若本專利中任一專利有撤銷、專
利範圍變動或消滅之情事發生時，甲方於前述情事確定前已發生的
給付義務不因此而免除，且乙方已收取之金額無需返還之，亦無須
負擔損害賠償責任。
- 三、除本契約另有約定外，契約當事人在本契約下列條款中之權義不因
本契約終止、解除或有效期間屆滿而消滅：第五條、第六條、第七
條、第九條、第十二條、第十三條、第十四條。

第十七條：準據法與合意管轄

- 一、本契約之準據法為中華民國法律。
- 二、若因本契約而涉訟時，契約當事人同意以台灣新竹地方法院為第一審
管轄法院。

第十八條：條文名稱與一部無效

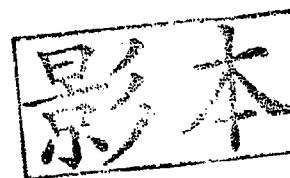
- 一、本契約各條文之標題，僅係為方便閱讀之用，不得據以解釋、限制或
影響各該條文內容所含之意義。
- 二、本契約部分條款若依法被認為無效時，其他條款仍應繼續有效。

第十九條：完整合意

- 一、本契約本文及其附件構成雙方對本案完整之合意，於本契約生效前經
雙方協議而未記載於本契約或其附件之事項，對雙方均無拘束力。
- 二、附件之效力與本契約同，但兩者有抵觸時，以本契約為準。
- 三、本契約未盡事宜，應本誠信原則互相協議解決。

第二十條：契約份數

本契約設正本壹式貳份，副本肆份，甲方執正本壹份、副本壹份，乙
方執正本壹份、副本叁份為憑，印花自理。



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立約人

甲方：聯發科技股份有限公司

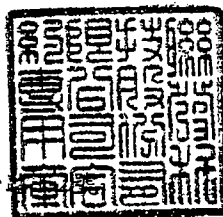
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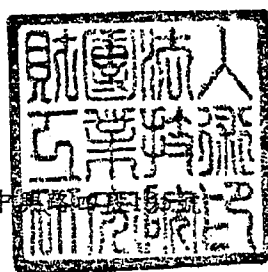
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統一編號：02750963



中華民國九十四年九月廿九日

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附件一

一、專利授權清單:

標的物 編號	國別	名稱	專利證號	專利起期	專利迄期
GR-2-20	JP	與解析度無關之光域顯示系統	3241769	20011019	20111128
GR-2-21	TW	與解析度無關之光域顯示系統 (與GR-2-20同案)	57484	19920711	20120211
GR-2-22	US	Resolution independent raster display system (與GR-2-20同案)	5,268,682	19931207	20111006
GR-2-23	TW	提高顯示裝置之色彩或灰度顯示 能力的方法或裝置	69369	19941221	20120226
GR-2-24	US	Dithering circuit and method (與GR-2-23同案)	5,389,948	19950214	20120213
GR-2-25	TW	借時間分佈法以提高顯示裝置之 色彩或灰度顯示能力的裝置	69198	19941211	20120226
GR-2-26	US	Dithering method and circuit using dither matrix rotation (與GR-2-25同案)	5,714,974	19980203	20150203
GR-2-27	DE	Resolution independent screen refresh strategy	P4217010.9	19970220	20120521
GR-2-28	JP	一種與解析度無關之銀幕更新方 法 (與GR-2-27同案)	3100251	20000818	20130202
GR-2-29	TW	一種與解析度無關之銀幕更新方 法 (與GR-2-27同案)	65108	19940201	20120528
GR-2-30	US	Resolution independent screen refresh strategy (與GR-2-27同案)	5,321,425	19940614	20120218
GR-2-31	TW	將二維座標轉為線性座標之裝置	71281	19950321	20140306
GR-2-32	US	Apparatus for converting two-dimensional pixel image into one-dimensional pixel array (與GR-2-32同案)	5,589,850	19961231	20140925
GR-2-33	TW	影像壓縮方法與裝置	116358	20000611	20140906
GR-2-34	JP	配合螢幕控制器產生特殊畫面之 對應裝置	3335824	20020802	20151123



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GR-2-35	TW	配合螢幕控制器產生特殊畫面之對應裝置 (與GR-2-34同案)	88859	19970511	20150724
GR-2-36	US	Mapping apparatus for use with a cathode-ray tube controller for generating special screen effects (與GR-2-34同案)	6,008,782	19991228	20161227
GR-2-37	TW	三維電腦繪圖系統中 Z 值誤差修正之方法和裝置	108690	19991011	20161021
GR-2-38	US	Three dimensional graphics system and method with correction to z buffer values error (與GR-2-37同案)	5,751,290	19980512	20160919
GR-2-39	TW	一種映素資料儲存的方法	118155	20000621	20180427
GR-2-40	US	Method and systems for storing texels retrievable in a single cycle (與GR-2-39同案)	6,104,413	20000815	20180310
GR-2-41	TW	去除繪圖中齒狀邊緣效應之圖素有效面積值計算方法與裝置	140303	20010821	20190225
GR-2-42	US	Fast area-coverage computing method for anti-aliasing in graphics (與GR-2-41同案)	6,377,273	20020423	20181103
GR-3-43	TW	一種具專屬除法器之顯示記憶體架構	57468	19920721	20110929
GR-3-44	US	Memory architecture with graphics generator including a divide by five divider (與GR-3-43同案)	5,268,681	19931207	20111006
GR-3-45	TW	一種具特定除數之除法器	57201	19920621	20110929
GR-3-46	US	Divide-by-five divider (與GR-3-45同案)	5,140,544	19920818	20111006
GR-3-47	DE	Graphics memory architecture for multimode display system	4431304	19980219	20140901
GR-3-48	TW	多重模式顯示系統之繪圖記憶體架構 (與GR-3-47同案)	84986	19970301	20131028
GR-3-49	US	Graphics memory architecture for multimode display system (與GR-3-47同案)	5,422,657	19950606	20130912

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GR-3-50	US	Mip map/rip map textured linear addressing memory organization and address generator	6,057,861	20000502	20190601
GR-3-51	JP	一個 MIP MAP/RIP MAP 紋理的記憶體線性定址結構與位址產生器	2894989	19990305	20160602
GR-3-52	TW	一個 MIP MAP/RIP MAP 紋理的記憶體線性定址結構與位址產生器 (與GR-3-51同案)	82090	19961001	20160109
GR-3-53	US	Mip map/rip map texture linear addressing memory organization and address generator (與GR-3-51同案)	5,963,220	19991005	20160207
GR-3-54	TW	電腦繪圖記憶體架構	119299	20000801	20151217
GR-3-55	US	Computer graphics memory architecture which processes a full pixel of 3d color and z value data in a single I/O transaction (與GR-3-54同案)	5,933,147	19990803	20150920
GR-3-56	TW	電腦繪圖系統中虛擬座標至線性實際記憶體位址的轉換器	86410	19970501	20160204
GR-3-57	US	Virtual coordinate to linear physical memory address converter for computer graphics system (與GR-3-56同案)	5,745,739	19980428	20160207
GR-3-58	TW	一個提升系統效率並減少成本的記憶體架構	90885	19971111	20160325
GR-3-59	US	Method and system for interleaving data in multiple memory bank partitions (與GR-3-58同案)	5,924,111	19990713	20151016
GR-3-60	TW	一種電腦繪圖記憶體架構和管理方法	103333	19990501	20171026

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GR-3-61	US	Computer graphics memory architecture having a graphics processor and a buffer (與GR-3-60同案)	5,946,005	19990831	20170826
GR-3-62	TW	非同步雙埠先進先出記憶體之構造與方法	122485	20000921	20180922
GR-3-63	US	Apparatus and method for asynchronous dual port fifo (與GR-3-62同案)	6,263,410	20010717	20180914
GR-3-64	TW	快取裝置與方法	136294	20010616	20190505
GR-3-65	US	Cache memory device with prefetch function and method for asynchronously renewing tag addresses and data during cache miss states (與GR-3-64同案)	6,321,301	20011120	20190811
MP-1-74	TW	多處理機系統中多中斷請求同時分派的方法與裝置	95469	19980621	20170425
MP-1-75	US	Method and apparatus for dispatching multiple interrupt requests simultaneously (與MP-1-74同案)	5,918,057	19990629	20170319
MP-1-76	TW	多處理機系統中無阻塞多中斷請求的選取方法與裝置	99391	19981201	20170508
MP-1-77	US	Method and apparatus for selecting a nonblocked interrupt request (與MP-1-76同案)	5,905,897	19990518	20170319

二、乙方並無提供任何有關本專利之資料文件予甲方，或是對甲方提供有關本專利之諮詢講解或訓練之義務。

三、基於友好互助，維護合法授權者之前提，甲方若獲悉任何第三人違法侵害乙方本專利之事證，甲方同意儘速通知乙方並提供相關證據協助乙方採取相關行動。

影本

DECLARATION OF TRANSLATOR

I Pei-Chi WU translator of the Patent Exclusive License Agreement for the recordation of the exclusive license of the 22 U.S. Patents listed in the table of Attachment I do hereby declare that I am literate in both Chinese and English language, and I certify that the accompanying English-language document is, to the best of my knowledge, a true and accurate partial translation of the originally-filed Chinese-language document of Patent Exclusive License Agreement.

Pei-Chi Wu
Signature

October 25, 2007
Date