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ACTION FORM COVER SHEET

U.S. Department of Commerce

Patent and Trademark Office

Attorney Docket No.: NETL-CD04101D

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Please record the attached original documents or copy thereof.

1. Name of conveying party(ies):

Cypress Semiconductor Corporation

Additional name(s) of conveying party(ies) attached.

2. Name and address of receiving party(ies):

NetLogic Microsystems, Inc.
1875 Charleston Road
Mountain View, CA 94043

Additional name(s) and address(es) attached.

3. Nature of conveyance:

☒ Assignment ☐ Merger
☐ Security Agreement ☐ Change of Name
☐ Other: _____

Execution Date: February 15, 2006

11/983382

4. Application number(s) or patent number(s):

☒ This document is being filed together with a new application, the execution date of the application is: January 14, 2005
☐ Patent Application Serial No.(s): filed on .
☐ Patent No.(s): .
☐ Additional numbers attached.

5. Name and address of party to whom correspondence concerning document should be mailed:

Bradley T. Sako
HAVERSTOCK & OWENS LLP
162 North Wolfe Road
Sunnyvale, CA 94086

6. Total number of applications and patents involved: 1.

7. Total fee(s) [37 CFR §§ 1.21(h) and 3.41]:

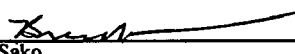
☒ Check(s) in the amount of \$1,220.00 is enclosed (\$1,180.00 for the Basic Filing Fee and \$40.00 for the Assignment Recordation Fee)

8. Authorization to Charge Additional Fees:

☒ The Commissioner is hereby authorized to charge any additional fees or credit any overpayment associated with this communication and which may be required under 37 CFR §§ 1.21(h) and 3.41 to Deposit Account No. 08-1275.

9. To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

Dated: November 7, 2007

Signature: 
Name: Bradley T. Sako
Reg. No.: 37,923

Total number of pages including cover sheet, attachments, and document: 8

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X5 FC:8021

40.00 DP

PATENT
REEL: 020155 FRAME: 0077

ASSIGNMENT OF PATENTS AND PATENT APPLICATIONS

THIS ASSIGNMENT OF PATENTS AND PATENT APPLICATIONS (this "*Assignment*") is made as of February 15, 2006 (the "*Assignment Date*") by Cypress Semiconductor Corporation, a Delaware corporation (the "*Assignor*"), to NetLogic Microsystems, Inc., a Delaware corporation (the "*Assignee*").

RECITALS

A. Pursuant to the Agreement for the Purchase and Sale of Assets, dated January 25, 2006, by and between Assignor and Assignee (the "*Purchase Agreement*"), Assignor agreed to sell, assign, transfer, convey and deliver to Assignee certain assets, including all of Assignor's right, title and interest in and to the Patent and Patent Applications (as defined below).

B. The obligation of Assignee to consummate the transactions contemplated by the Purchase Agreement is conditioned in part on the execution and delivery of this Assignment.

ASSIGNMENT

For good and valuable consideration, the receipt, adequacy and legal sufficiency of which are hereby acknowledged:

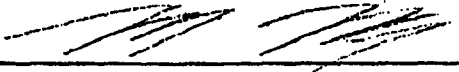
Assignor hereby sells, assigns, transfers, conveys and delivers to Assignee, its successors and assigns, (a) all worldwide right, title and interest in, to and under the patents and patent applications listed on Schedule A to this Assignment (collectively, the "*Patents and Patent Applications*"), including but not limited to all patent applications, patents, or similar legal protections that have been, or will be, obtained, based on any of said Patents and Patent Applications, (b) all right, title and interest in any provisional application of which any of the Patents and Patent Applications claims the benefit (c) all rights to obtain renewals, extensions, continuations, continuations-in-part, reissues, re-examinations, divisions, substitutes, or equivalents thereof, based on any of said Patents and Patent Applications, in the United States and all foreign countries, and (d) all past, present and future claims, counterclaims, causes of action, choses in action, rights of recovery, rights of set off, and rights of recoupment in or with respect to any of the Patents and Patent Applications.

Assignor grants the attorney of record the power to insert on this Assignment any further identification that may be necessary to comply with the rules of the United States Patent and Trademark Office, or rules of other entities, including United States or foreign governments or patent offices, for recordation of this document.

Assignor shall execute or cause to be delivered to Assignee such instruments and other documents, and shall take such other actions, as Assignee may reasonably request after the Assignment Date, that are necessary for the purpose of carrying out or evidencing the assignment of the Patents and Patent Applications pursuant to this Assignment.

IN WITNESS WHEREOF, Assignor has caused its duly authorized officer to execute this Assignment of Patents and Patent Applications as of the Assignment Date.

CYPRESS SEMICONDUCTOR CORPORATION

By: 

Name: Brad Buss

Title: Chief Financial Officer

SCHEDULE A

PATENTS AND PATENT APPLICATIONS

METHOD AND APPARATUS FOR PRECHARGING MATCH OUTPUT IN A CASCADED CONTENT ADDRESSABLE MEMORY SYSTEM	235,663 - USP 5,621,677
FULLY STATIC CAM CELLS WITH LOW WRITE POWER AND METHODS OF MATCHING AND WRITING TO THE SAME	281,436 - USP 5,452,243
INTEGRATED CONTENT ADDRESSABLE MEMORY ARRAY WITH PROCESSING LOGICAL AND A HOST COMPUTER INTERFACE	284,347- USP 5,649,149
INSTRUCTION SET FOR A CONTENT ADDRESSABLE MEMORY ARRAY WITH READ/WRITE CIRCUITS AND AN INTERFACE REGISTER LOGIC BLOCK	284,372 - USP 5,860,085
MEMORY DEVICE AND SENSE AMPLIFIER CIRCUIT WITH FASTER SENSING SPEED AND IMPROVED INSENSITIVIES TO FABRICATION PROCESS VARIATIONS	SERIAL NUMBER NOT RECEIVED
DIFFERENTIAL DYNAMIC CONTENT ADDRESSABLE MEMORY AND HIGH SPEED NETWORK ADDRESS FILTERING	886,909 - USP 5,849,696
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CONTENT ADDRESSABLE MEMORY (CAM) DEVICE HAVING SELECTABLE ACCESS AND METHOD THEREFOR	10/264,667

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A MAGNITUDE COMPARATOR CIRCUIT FOR CONTENT ADDRESSABLE MEMORY WITH PROGRAMMABLE PRIORITY SELECTION	10/266,953
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STAGGERED COMPARE ARCHITECTURE FOR CONTENT ADDRESSABLE MEMORY (CAM)	10/746,899 - USP 6,958,925

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ARCHITECTURE FOR NETWORK SEARCH ENGINES WITH FIXED LATENCY, HIGH CAPACITY, AND HIGH THROUGHPUT	10/841,607
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PSEUDO TERNARY CONTENT ADDRESSABLE MEMORY DEVICE HAVING ROW REDUNDANCY AND METHOD THEREFOR	11/281,227

Note: "USP" = U.S. Patent