

PATENT ASSIGNMENT

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NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
Saxon IP Assets, LLC	10/16/2007
RECEIVING PARTY DATA	
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PROPERTY NUMBERS Total: 1	
Property Type	Number
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Total Attachments: 5
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ASSIGNMENT OF PATENT RIGHTS

For good and valuable consideration, the receipt of which is hereby acknowledged, Saxon IP Assets, LLC, a Delaware limited liability company, having offices at 4509 Freidrich Lane, Bldg 2, Austin, TX 78744, (“*Assignor*”), does hereby sell, assign, transfer, and convey unto Saxon Innovations, LLC, a Texas limited liability company, with an address at 100 East Ferguson, Suite 900, Tyler, Texas 75702 (“*Assignee*”), or its designees, all right, title, and interest that exist today and may exist in the future in and to any and all of the following (collectively, the “*Patent Rights*”):

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- (b) all patents and patent applications (i) to which any of the Patents directly or indirectly claims priority, and (ii) for which any of the Patents directly or indirectly forms a basis for priority;
- (c) all reissues, reexaminations, extensions, continuations, continuations in part, continuing prosecution applications, requests for continuing examinations, divisions, registrations of any item in any of the foregoing categories (a) and (b);
- (d) all foreign counterpart patents and foreign counterpart patent applications, that are related to any item in any of the foregoing categories (a) through (c), including, without limitation, certificates of invention, utility models, industrial design protection, design patent protection, and other governmental grants or issuances;
- (e) all items in any of the foregoing in categories (b) through (d), whether or not expressly listed as Patents below and whether or not claims in any of the foregoing have been rejected, withdrawn, cancelled, or the like;
- (f) all inventions, invention disclosures, and discoveries described in any item in any of the foregoing categories (a) through (e) and all other rights arising out of such inventions, invention disclosures, and discoveries;
- (g) all rights to apply in any or all countries of the world for patents, certificates of invention, utility models, industrial design protections, design patent protections, or other governmental grants or issuances of any type arising from any item in any of the foregoing categories (a) through (f), including, without limitation, under the Paris Convention for the Protection of Industrial Property, the International Patent Cooperation Treaty, or any other convention, treaty, agreement, or understanding;
- (h) to the extent they exist, all causes of action (whether known or unknown or whether currently pending, filed, or otherwise) and other enforcement rights under, or on account of, any of the Patents and/or any item in any of the foregoing categories (b) through (g), including, without limitation, all causes of action and other enforcement rights for

- (i) damages,
- (ii) injunctive relief, and
- (iii) any other remedies of any kind for past, current, and future infringement; and
- (i) all rights to collect royalties and other payments under or on account of any of the Patents and/or any item in any of the foregoing categories (b) through (h).

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
[Patent numbers]	[Country]	[Filing date(s)]	[Title of patent and name of first named inventor]
4,669,179	United States		INTEGRATED CIRCUIT FABRICATION PROCESS FOR FORMING A BIPOLAR TRANSISTOR HAVING EXTRINSIC BASE REGIONS
0221742	European Patent Convention		INTEGRATED CIRCUIT FABRICATION PROCESS FOR FORMING A BIPOLAR TRANSISTOR HAVING EXTRINSIC BASE REGIONS
0221742	Great Britain		INTEGRATED CIRCUIT FABRICATION PROCESS FOR FORMING A BIPOLAR TRANSISTOR HAVING EXTRINSIC BASE REGIONS
1826942	Japan		INTEGRATED CIRCUIT FABRICATION PROCESS FOR FORMING A BIPOLAR TRANSISTOR HAVING EXTRINSIC BASE REGIONS
74228	Austria		INTEGRATED CIRCUIT FABRICATION PROCESS FOR FORMING A BIPOLAR TRANSISTOR HAVING EXTRINSIC BASE REGIONS

Patent or Application No.	Country	Filing Date	<u>Title of Patent and First Named Inventor</u>
0221742	Switzerland		INTEGRATED CIRCUIT FABRICATION PROCESS FOR FORMING A BIPOLAR TRANSISTOR HAVING EXTRINSIC BASE REGIONS
3684555	West Germany		INTEGRATED CIRCUIT FABRICATION PROCESS FOR FORMING A BIPOLAR TRANSISTOR HAVING EXTRINSIC BASE REGIONS
2030389	Spain		INTEGRATED CIRCUIT FABRICATION PROCESS FOR FORMING A BIPOLAR TRANSISTOR HAVING EXTRINSIC BASE REGIONS
0221742	France		INTEGRATED CIRCUIT FABRICATION PROCESS FOR FORMING A BIPOLAR TRANSISTOR HAVING EXTRINSIC BASE REGIONS
0221742	Italy		INTEGRATED CIRCUIT FABRICATION PROCESS FOR FORMING A BIPOLAR TRANSISTOR HAVING EXTRINSIC BASE REGIONS
0221742	Sweden		INTEGRATED CIRCUIT FABRICATION PROCESS FOR FORMING A BIPOLAR

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
			TRANSISTOR HAVING EXTRINSIC BASE REGIONS
0221742	Netherland		INTEGRATED CIRCUIT FABRICATION PROCESS FOR FORMING A BIPOLAR TRANSISTOR HAVING EXTRINSIC BASE REGIONS
0221742	Belgium		INTEGRATED CIRCUIT FABRICATION PROCESS FOR FORMING A BIPOLAR TRANSISTOR HAVING EXTRINSIC BASE REGIONS

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The terms and conditions of this Assignment of Patent Rights will inure to the benefit of Assignee, its successors, assigns, and other legal representatives and will be binding upon Assignor, its successors, assigns, and other legal representatives.

IN WITNESS WHEREOF this Assignment of Patent Rights is executed at Austin
TR on 16 Oct 2007.

