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APPLICATION FOR: SYSTEM FOR SUPPORTING PARTIAL CACHE LINE WRITE OPERATIONS TO A MEMORY MODULE TO REDUCE WRITE DATA TRAFFIC ON A MEMORY CHANNEL INVENTOR(S): KEVIN C. GOWER, WARREN MAULE SERIAL NO. FILING DATE: 08/31/2007 11/848342 CONTRACTOR: CONTRACT NO. HR0011-07-9-0002 INTERNATIONAL BUSINESS MACHINES CORPORATION The Invention identified above is a "Subject Invention" under the Patent Rights Clause included in the above contract with: (DEFENSE ADVANCED RESEARCH PROJECTS AGENCY) DARPA (SPECIFY GOV'T. AGENCY HERE) This document is confirmatory of the paid-up license granted to the Government under the above contract in the Subject Invention, this patent application and any resulting patent(s), and of all other rights acquired by the Government by the referenced clause. The Government is hereby granted an irrevocable power to inspect and make copies of the above-identified patent application. Signed this _____5th ____ day of _____SEPTEMBER_____, 2007 INTERNATIONAL BUSINESS MACHINES CORPORATION CONTRACTOR BY: CONTRACTOR'S OFFICIAL AND TITLE John D Flynn IP Senior Counsel - STG **IBM BUSINESS ADDRESS**

BUSINESS ADDRESS 11501 Burnet Road Austin, TX 78758

IBM Disclosure No.: AUS820071508
IBM Docket No.: AUS920070757US1

PATENT REEL: 020241 FRAME: 0562

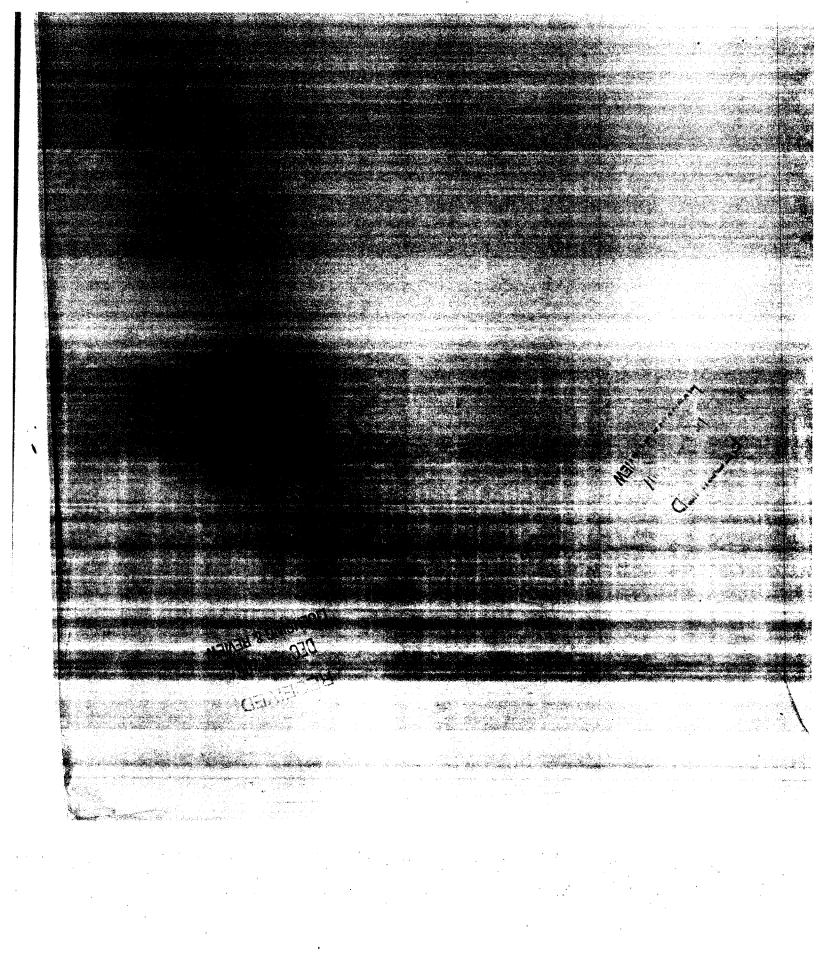
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