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A. Patent Application No.(s)

11/848,342

B. Patent No.(s)

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Name: Kimberly B. Downey

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11-8-07

CONFIRMATORY INSTRUMENT

APPLICATION FOR: SYSTEM FOR SUPPORTING PARTIAL CACHE LINE WRITE
OPERATIONS TO A MEMORY MODULE TO REDUCE WRITE DATA TRAFFIC ON A
MEMORY CHANNEL

INVENTOR(S): KEVIN C. GOWER, WARREN MAULE

SERIAL NO. 11/848342	FILING DATE: 08/31/2007
CONTRACTOR: INTERNATIONAL BUSINESS MACHINES CORPORATION	CONTRACT NO. HR0011-07-9-0002

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INTERNATIONAL BUSINESS
MACHINES CORPORATION
CONTRACTOR

BY: 

CONTRACTOR'S OFFICIAL AND TITLE

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Austin, TX 78758

IBM Disclosure No.: AUS820071508
IBM Docket No.: AUS920070757US1

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