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 4. Application or patent number(s): This A. Patent Application No.(s) 11/768,791 Additional numbers at 	document is being filed together with a new application. B. Patent No.(s) tached? □ Yes X No			
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CONFIRMATORY INSTRUMENT

768,791 |||

APPLICATION FOR (TITLE OF INVENTION) METHOD AND APPARATUS TO DEBUG AN INTEGRATED CIRCUIT CHIP VIA SYNCHRONOUS CLOCK STOP AND SCAN

INVENTOR(S):

Ruud A Haring, Alan Gara, Matthew R Ellavsky, Ralph Bellofatto, Mark E Giampapa, Thomas Gooding, Martin Ohmacht and Lance G. Hehenberger

SERIAL NO. 11/768,791	FILING DATE: 26-Jun-2007
CONTRACTOR:	CONTRACT NO.
INTERNATIONAL BUSINESS	B554331
MACHINES CORPORATION	

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