

PATENT ASSIGNMENT

Electronic Version v1.1

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SUBMISSION TYPE:

NEW ASSIGNMENT

NATURE OF CONVEYANCE:

ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
Technology Properties Limited	04/23/2008

RECEIVING PARTY DATA

Name:	VNS Portfolio LLC
Street Address:	20400 Stevens Creek Blvd.
Internal Address:	Fifth Floor
City:	Cupertino
State/Country:	CALIFORNIA
Postal Code:	95014

PROPERTY NUMBERS Total: 11

Property Type	Number
Application Number:	10801942
Application Number:	11592717
Application Number:	11441818
Application Number:	11731747
Application Number:	11652726
Application Number:	11741649
Application Number:	11741659
Application Number:	11726739
Application Number:	11854215
Application Number:	11854242
Application Number:	61034819

CORRESPONDENCE DATA

Fax Number: (408)446-0905

Correspondence will be sent via US Mail when the fax attempt is unsuccessful.

PATENT

500523301

REEL: 020856 FRAME: 0008

OP \$440.00 10801942

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Email: mark.salvatore@tplgroup.net
Correspondent Name: Mark Salvatore
Address Line 1: 20400 Stevens Creek Blvd.
Address Line 2: Fifth Floor
Address Line 4: Cupertino, CALIFORNIA 95014

ATTORNEY DOCKET NUMBER:

TPL-0002

NAME OF SUBMITTER:

Mark Salvatore

Total Attachments: 20

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ASSIGNMENT

For one dollar and other good and valuable consideration, the receipt of which is hereby acknowledged,

Technology Properties Limited,

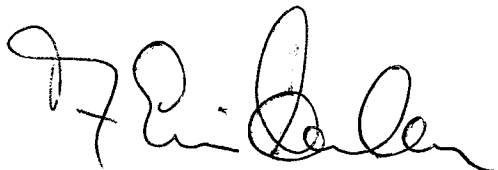
a California corporation (hereinafter "Assignor"), having its principal place of business at 20400 Stevens Creek Boulevard, Suite 500, Cupertino, California 95014, hereby assigns to

VNS Portfolio LLC

a limited liability company organized under the laws of the state of Delaware, having its principal place of business at 20400 Stevens Creek Boulevard, Suite 500, Cupertino, California 95014, and its nominees, successors or assigns (hereinafter collectively "Assignee"), the entire right, title and interest in and to the patents and patent applications attached hereto as Exhibits I and II (Exhibit I listing the properties by country; Exhibit II listing them by family), the invention or inventions described therein; together with the worldwide right to claim priority to and the benefit of such patent and patent applications and the right to collect damages for infringements thereof.

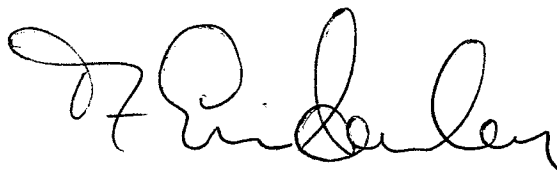
IN WITNESS WHEREOF, the parties hereby execute this assignment on April 23, 2008.

Technology Properties Limited



F. Eric Saunders, VP Intellectual Property

VNS Portfolio LLC



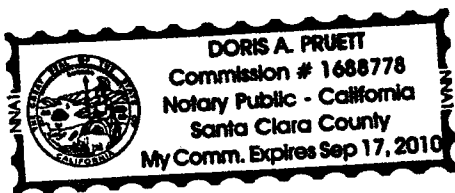
F. Eric Saunders, VP Intellectual Property

State of California

County of Santa Clara

On April 24, 2008 before me, Doris A. Pruett,
Date Here Insert Name and Title of the Officer

personally appeared F. Eric Saunders,
Name(s) of Signer(s)



Place Notary Seal Above

who proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is/are subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.

I certify under PENALTY OF PERJURY under the laws of the State of California that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.

Signature

Doris A. Pruett

Signature of Notary Public

Exhibit I

United States Cases

Case No.	Title	Inventors	Application Number	Filing Date
0057-011	Computer Processor Array with Dedicated Interconnections	Moore, Charles H.	10/801,942	03/16/2004
0057-012P1	Method For Determining Information About The Internal Workings Of A Chip Based On Electro-Magnetic Emissions Therefrom	Huie, John	11/592,717	11/03/2006
0057-014P3	Method and Apparatus for Monitoring Inputs to a Computer Array	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	11/441,818	05/26/2006
0057-015P1	Method and Apparatus for Operating A Computer Processor Array	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	11/731,747	03/30/2007
0057-028	CMOS SRAM/ROM Unified Bit Cell	Miller, Dennis Ray Rahman, Md Hafizur Kabir, Mohammad Ehsanul	11/652,726	01/12/2007
0057-029	System and Method for Multi-Port Read and Write Operations	Rible, John W.	11/741,649	04/27/2007
0057-029P1	System And Method For Processing Data In A Pipeline Of Computers	Montvelishsky, Michael B. Rible, John W.	11/741,659	04/27/2007
0057-033	Analog-To-Digital Converter System With Increased Sampling Frequency	Moore, Charles H. Huie, John Snively, Les	11/726,739	03/22/2007
0057-034	System for Convolution Calculation with Multiple Computer Processors	Montvelishsky, Michael B.	11/854,215	09/12/2007
0057-042	Carry-Select Adder	Leeland, Steven	11/854,242	09/12/2007
0057-049V	System and Method for High-Bandwidth Data Transfer with Low Operating Power	Tian, Ying	61/034,819	03/07/2008

Exhibit I CHINA

Date: April 24, 2008

Client	Case No. Client Case #	Title	Country	Inventor Names	Application Number	Filing Date	Patent Number	Date Issued	Expiration Date	Status
IntellaSys/SEAforth (INT)	0057-011-CN 5585052	Computer Processor Array	China	Moore, Charles H.	80008575.7	09/18/2006				Pending
IntellaSys/SEAforth (INT)	0057-012V-CN 5596950	Method For Determining Information About The Internal Workings Of A Chip Based On Electro- Magnetic Emissions Therefrom	China	Huie, John	80009135.8	09/21/2007				Pending
IntellaSys/SEAforth (INT)	0057-013-CN 5592850	Asynchronous Computer Communication	China	Moore, Charles H.	200780000017.5	04/30/2007				Pending
IntellaSys/SEAforth (INT)	0057-014-CN 5592950	Asynchronous Power Saving Computer	China	Moore, Charles H.	80000014.1	04/30/2007				Pending
IntellaSys/SEAforth (INT)	0057-014P1-CN 5597050	Execution Of Instructions Directly From Input Source	China	Moore, Charles H.	80000013.7	04/30/2007				Pending
IntellaSys/SEAforth (INT)	0057-014P2-CN 5597150	Microloop Computer Instructions	China	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	200780000015.6	04/30/2007				Pending
IntellaSys/SEAforth (INT)	0057-014P3-CN 5597250	Method And Apparatus For Monitoring Inputs To A Computer	China	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	80000016.0	04/30/2007				Pending
IntellaSys/SEAforth (INT)	0057-015P1-CN 5597350	Method and Apparatus for Operating a Computer Processor Array	China	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	2007-10090753.6	04/02/2007				Pending
IntellaSys/SEAforth (INT)	0057-026P1-CN 5600450	Computer System With Increased Operating Efficiency	China	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	200780000018.X	04/30/2007				Pending
IntellaSys/SEAforth (INT)	0057-033-CN 5617650	Analog-To-Digital Converter System With Increased Sampling Frequency	China	Moore, Charles H. Huie, John Snively, Les	2008-10087166.6	03/21/2008				Pending

Exhibit I

EPO

Date: April 24, 2008

Client	Case No. Client Case #	Title	Country	Inventor Names	Application Number	Filing Date	Patent Number	Date Issued	Expiration Date	Status
Inteliasys/SEAForth (INT)	0057-011-EP 5585030	Computer Processor Array with Dedicated Interconnections	EPO	Moore, Charles H.	05723250.6	10/12/2006				Pending
Inteliasys/SEAForth (INT)	0057-012V- EP 5596930	Method For Determining Information About The Internal Workings Of A Chip Based On Electro- Magnetic Emissions Therefrom	EPO	Huie, John	06827511.4	06/04/2007				Pending
Inteliasys/SEAForth (INT)	0057-013-EP 5592830	Asynchronous Computer Communication	EPO	Moore, Charles H.	07250648.8	02/15/2007				Pending
Inteliasys/SEAForth (INT)	0057-014-EP 5592930	Asynchronous Power Saving Computer	EPO	Moore, Charles H.	07250645.4	02/15/2007				Pending
Inteliasys/SEAForth (INT)	0057-014P1- EP 5597030	Execution Of Instructions Directly From Input Source	EPO	Moore, Charles H.	07250649.6	02/15/2007				Pending
Inteliasys/SEAForth (INT)	0057-014P2- EP 5597130	Microloop Computer Instructions	EPO	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	07250614.0	02/15/2007				Pending
Inteliasys/SEAForth (INT)	0057-014P3- EP 5597230	Method And Apparatus For Monitoring Inputs To A Computer	EPO	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	07250646.2	02/15/2007				Pending
Inteliasys/SEAForth (INT)	0057-015P1- EP 5597330	Method and Apparatus for Operating a Computer Processor Array	EPO	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	07251416.9	03/30/2007				Pending
Inteliasys/SEAForth (INT)	0057-017P1- EP 5597630	Circular Register Arrays Of A Computer	EPO	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	07250644.7	02/15/2007				Pending
Inteliasys/SEAForth (INT)	0057-026P1- EP 5600430	Computer System With Increased Operating Efficiency	EPO	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	07250647.0	02/15/2007				Pending
Inteliasys/SEAForth (INT)	0057-032-EP 5617530	Multi-Speed Ring Oscillator	EPO	Goff, Lonnie		04/09/2008				Pending
Inteliasys/SEAForth (INT)	0057-033-EP 5617630	Analog-To-Digital Converter System With Increased Sampling Frequency	EPO	Moore, Charles H. Huie, John Snively, Les		03/14/2008				Pending
Inteliasys/SEAForth (INT)	0057-033P1- EP 5617730	Variable Sized Aperture Window Of An Analog-To-Digital Converter	EPO	Moore, Charles H. Huie, John Snively, Les		03/18/2008				Pending
Inteliasys/SEAForth (INT)	0057-034-EP 5617830	System for Convolution Calculation with Multiple Computer Processors	EPO	Montvelitsky, Michael B.		03/28/2008				Pending

Exhibit I INDIA

Date: April 24, 2008

Client	Case No. Client Case #	Title	Country	Inventor Names	Application Number	Filing Date	Patent Number	Date Issued	Expiration Date	Status
IntellaSys/SEAforth (INT)	0057-012V- IN 5596951	Method For Determining Information About The Internal Workings Of A Chip Based On Electro- Magnetic Emissions Therefrom	India	Huie, John	7188/DELNP/2007	09/18/2007				Pending
IntellaSys/SEAforth (INT)	0057-013-IN 5592851	Asynchronous Computer Communication	India	Moore, Charles H.	331/DEL/2007	02/19/2007				Pending
IntellaSys/SEAforth (INT)	0057-014-IN 5592951	Asynchronous Power Saving Computer	India	Moore, Charles H.	332/DEL/2007	02/19/2007				Pending
IntellaSys/SEAforth (INT)	0057-014P1- IN 5597051	Execution Of Instructions Directly From Input Source	India	Moore, Charles H.	326/DEL/2007	02/19/2007				Pending
IntellaSys/SEAforth (INT)	0057-014P2- IN 5597151	Microloop Computer Instructions	India	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	327/DEL/2007	02/19/2007				Pending
IntellaSys/SEAforth (INT)	0057-014P3- IN 5597251	Method And Apparatus For Monitoring Inputs To A Computer	India	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	328/DEL/2007	02/19/2007				Pending
IntellaSys/SEAforth (INT)	0057-015P1- IN 5597351	Method and Apparatus for Operating a Computer Processor Array	India	Moore, Charles H.	717/DEL/2007	03/30/2007				Pending
IntellaSys/SEAforth (INT)	0057-017P1- IN 5597651	Circular Register Arrays Of A Computer	India	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	330/DEL/2007	02/19/2007				Pending
IntellaSys/SEAforth (INT)	0057-026P1- IN 5600451	Computer System With Increased Operating Efficiency	India	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	329/DEL/2007	02/19/2007				Pending
IntellaSys/SEAforth (INT)	0057-033-IN 5617651	Analog-To-Digital Converter System With Increased Sampling Frequency	India	Moore, Charles H. Huie, John Snively, Les	624/DEL/2008	03/12/2008				Pending

Exhibit I JAPAN

Date: April 24, 2008

Client	Case No. Client Case #	Title	Country	Inventor Names	Application Number	Filing Date	Patent Number	Date Issued	Expiration Date	Status
Intelliasys/SEAForth (INT)	0057-011-JP 5585051	Computer Processor Array	Japan	Moore, Charles H.	2007-503910	09/14/2006				Pending
Intelliasys/SEAForth (INT)	0057-012V-JP 5596952	Method For Determining Information About The Internal Workings Of A Chip Based On Electro- Magnetic Emissions Therefrom	Japan	Huie, John	Not Yet Assigned	09/21/2007				Pending
Intelliasys/SEAForth (INT)	0057-013-JP 5592852	Asynchronous Computer Communication	Japan	Moore, Charles H.	Not Yet Assigned	04/26/2007				Pending
Intelliasys/SEAForth (INT)	0057-014-JP 5592952	Asynchronous Power Saving Computer	Japan	Moore, Charles H.	Not Yet Assigned	04/26/2007				Pending
Intelliasys/SEAForth (INT)	0057-014P1-JP 5597052	Execution Of Instructions Directly From Input Source	Japan	Moore, Charles H.	Not Yet Assigned	04/27/2007				Pending
Intelliasys/SEAForth (INT)	0057-014P2-JP 5597152	Microloop Computer Instructions	Japan	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	Not Yet Assigned	04/27/2007				Pending
Intelliasys/SEAForth (INT)	0057-014P3-JP 5597252	Method And Apparatus For Monitoring Inputs To A Computer	Japan	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	Not Yet Assigned	04/27/2007				Pending
Intelliasys/SEAForth (INT)	0057-015P1-JP 5597352	Method and Apparatus for Operating a Computer Processor Array	Japan	Moore, Charles H.	2007-086857	03/29/2007				Pending
Intelliasys/SEAForth (INT)	0057-026P1-JP 5600452	Computer System With Increased Operating Efficiency	Japan	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	Not Yet Assigned	04/27/2007				Pending
Intelliasys/SEAForth (INT)	0057-033-JP 5617652	Analog-To-Digital Converter System With Increased Sampling Frequency	Japan	Moore, Charles H. Huie, John Snively, Les	JP-2008-076423	03/24/2008				Pending
Intelliasys/SEAForth (INT)	0057-033P1-JP 5617752	Variable Sized Aperture Window Of An Analog-To-Digital Converter	Japan	Moore, Charles H. Huie, John Snively, Les	JP-2008-076424	03/24/2008				Pending
Intelliasys/SEAForth (INT)	0057-034-JP 5617852	System for Convolution Calculation with Multiple Computer Processors	Japan	Montvelitsky, Michael B.	JP-2008-097204	04/03/2008				Pending

Date: April 24, 2008

Exhibit I
KOREA

Client	Case No. Client Case #	Title	Country	Inventor Names	Application Number	Filing Date	Patent Number	Date Issued	Expiration Date	Status
IntelSys/SEAforth (INT)	0057-011-KR 5585050	Computer Processor Array	Korea	Moore, Charles H.	7021454/2006	10/16/2006				Pending
IntelSys/SEAforth (INT)	0057-012V-KR 5596953	Method For Determining Information About The Internal Workings Of A Chip Based On Electro- Magnetic Emissions Therefrom	Korea	Huie, John	7021645/07	09/20/2007				Pending
IntelSys/SEAforth (INT)	0057-013-KR 5592853	Asynchronous Computer Communication	Korea	Moore, Charles H.	7009920/07	04/30/2007				Pending
IntelSys/SEAforth (INT)	0057-014-KR 5592953	Asynchronous Power Saving Computer	Korea	Moore, Charles H.	7009921/07	04/30/2007				Pending
IntelSys/SEAforth (INT)	0057-014P1-KR 5597053	Execution Of Instructions Directly From Input Source	Korea	Moore, Charles H.	7009924/07	04/30/2007				Pending
IntelSys/SEAforth (INT)	0057-014P2-KR 5597153	Microloop Computer Instructions	Korea	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	7009923/07	04/30/2007				Pending
IntelSys/SEAforth (INT)	0057-014P3-KR 5597253	Method And Apparatus For Monitoring Inputs To A Computer	Korea	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	7009925/07	04/30/2007				Pending
IntelSys/SEAforth (INT)	0057-015P1-KR 5597353	Method and Apparatus for Operating a Computer Processor Array	Korea	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	2007-0032588	04/02/2007				Pending
IntelSys/SEAforth (INT)	0057-026P1-KR 5600453	Computer System With Increased Operating Efficiency	Korea	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	7009922/07	04/30/2007				Pending
IntelSys/SEAforth (INT)	0057-033-KR 5617653	Analog-To-Digital Converter System With Increased Sampling Frequency	Korea	Moore, Charles H. Huie, John Snively, Les	KR-27111/2008	03/24/2008				Pending
IntelSys/SEAforth (INT)	0057-033P1-KR 5617753	Variable Sized Aperture Window Of An Analog-To-Digital Converter	Korea	Moore, Charles H. Huie, John Snively, Les	KR-27112/2008	03/24/2008				Pending

Exhibit I PCT

Date: April 24, 2008

Client	Case No. Client Case #	Title	Country	Inventor Names	Application Number	Filing Date	Patent Number	Date Issued	Expiration Date	Status
IntelAsys/SEAforth (INT)	0057-011- PCT 0057-011- PCT	Computer Processor Array	PCT	Moore, Charles H.	PCT/US2005/005140	02/18/2005				Done
IntelAsys/SEAforth (INT)	0057-012V- PCT 5596999	Method For Determining Information About The Internal Workings Of A Chip Based On Electro- Magnetic Emissions Therefrom	PCT	Huie, John	PCT/US2006/043101	11/03/2006				Pending
IntelAsys/SEAforth (INT)	0057-013- PCT 0057-013PCT	Asynchronous Computer Communication	PCT	Moore, Charles H.	PCT/US2007/004031	02/16/2007				Pending
IntelAsys/SEAforth (INT)	0057-014P1- PCT 0057-014P1- PCT	Execution Of Instructions Directly From Input Source	PCT	Moore, Charles H.	PCT/US2007/004030	02/16/2007				Pending
IntelAsys/SEAforth (INT)	0057-014P2- PCT 0057-014P2- PCT	Microloop Computer Instructions	PCT	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	PCT/US2007/004029	02/16/2007				Pending
IntelAsys/SEAforth (INT)	0057-014P3- PCT	Method And Apparatus For Monitoring Inputs To A Computer	PCT	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	PCT/US2007/004083	02/16/2007				Pending
IntelAsys/SEAforth (INT)	0057-014- PCT 0057-014- PCT	Asynchronous Power Saving Computer	PCT	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	PCT/US2007/004080	02/16/2007				Pending
IntelAsys/SEAforth (INT)	0057-015-P1- PCT 0057-015P1- PCT	Method And Apparatus For Operating A Computer Processor Array	PCT	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	PCT/US2007/008235	03/30/2007				Pending
IntelAsys/SEAforth (INT)	0057-015V- PCT	Allocation Of Resources Among An Array Of Computers	PCT	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	PCT/US2007/004081	02/16/2007				Pending
IntelAsys/SEAforth (INT)	0057-017P1- PCT 0057-017P1- PCT	Circular Register Arrays Of A Computer	PCT	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	PCT/US2007/012539	05/25/2007				Pending
IntelAsys/SEAforth (INT)	0057- 026P1PCT 0057-026P1- PCT	Computer System With Increased Operating Efficiency	PCT	Moore, Charles H.	PCT/US2007/004082	02/16/2007				Pending

Exhibit I PCT

Date: April 24, 2008

Client	Case No. Client Case #	Title	Country	Inventor Names	Application Number	Filing Date	Patent Number	Date Issued	Expiration Date	Status
IntellaSys/SEAforth (INT)	0057-027V- PCT	Inversion Of Alternate Instruction And/Or Data Bits In A Computer	PCT	Moore , Charles H.	PCT/US2007/026172	12/21/2007				Pending
IntellaSys/SEAforth (INT)	0057-028- PCT	CMOS SRAM/ROM Unified Bit Cell	PCT	Miller , Dennis Ray Rahman, Md Hafizur Kabir, Mohammad Ehsanul	PCT/US2008/000260	01/08/2008				Pending
IntellaSys/SEAforth (INT)	0057-032- PCT	Multi-Speed Ring Oscillator	PCT	Goff , Lonnie						New
IntellaSys/SEAforth (INT)	0057-033P1- 5617599	Variable Sized Aperture Window Of An Analog-To-Digital Converter	PCT	Moore , Charles H. Huie, John Snively, Les	NOT YET ASSIGNED	03/20/2008				Pending
IntellaSys/SEAforth (INT)	0057-033- PCT	Analog-To-Digital Converter System With Increased Sampling Frequency	PCT	Moore , Charles H. Huie, John Snively, Les	NOT YET ASSIGNED	03/20/2008				Pending
IntellaSys/SEAforth (INT)	0057-034- PCT	System for Convolution Calculation with Multiple Computer Processors	PCT	Montvel'shsky , Michael B.		04/06/2008				Pending

Exhibit I TAIWAN

Date: April 24, 2008

Client	Case No. Client Case #	Title	Country	Inventor Names	Application Number	Filing Date	Patent Number	Date Issued	Expiration Date	Status
IntelSys/SEAforth (INT)	0057-012P1- TW 5596954	Method For Determining Information About The Internal Workings Of A Chip Based On Electro- Magnetic Emissions Therefrom	Taiwan	Huie, John	96112860	04/12/2007				Pending
IntelSys/SEAforth (INT)	0057-013-TW 5592854	Asynchronous Computer Communication	Taiwan	Moore, Charles H.	96105684	02/15/2007				Pending
IntelSys/SEAforth (INT)	0057-014P1- TW 5597054	Execution Of Instructions Directly From Input Source	Taiwan	Moore, Charles H.	96106394	02/16/2007				Pending
IntelSys/SEAforth (INT)	0057-014P2- TW 5597154	Microloop Computer Instructions	Taiwan	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	96106397	02/16/2007				Pending
IntelSys/SEAforth (INT)	0057-014P3- TW 5597254	Method And Apparatus For Monitoring Inputs To A Computer	Taiwan	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	96106396	02/16/2007				Pending
IntelSys/SEAforth (INT)	0057-014-TW 5592954	Asynchronous Power Saving Computer	Taiwan	Moore, Charles H.	96105687	02/15/2007				Pending
IntelSys/SEAforth (INT)	0057-015P1- TW 5597354	Method and Apparatus for Operating a Computer Processor Array	Taiwan	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	96110969	03/29/2007				Pending
IntelSys/SEAforth (INT)	0057-017P1- TW 5597654	Circular Register Arrays Of A Computer	Taiwan	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	96105677	02/15/2007				Pending
IntelSys/SEAforth (INT)	0057-026P1- TW 5600454	Computer System With Increased Operating Efficiency	Taiwan	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	96105683	02/15/2007				Pending
IntelSys/SEAforth (INT)	0057-028-TW 5602154	CMOS SRAM/ROM Unified Bit Cell	Taiwan	Miller, Dennis Ray Rahman, Md Hafjur Kabir, Mohammad Ehsanul	96150267	12/26/2007				Pending
IntelSys/SEAforth (INT)	0057-033-TW 5617654	Analog-To-Digital Converter System With Increased Sampling Frequency	Taiwan	Moore, Charles H. Huie, John Snively, Les	TW-97/108474	03/11/2008				Pending

Exhibit II

Client Case Summary

Date: April 24, 2008

Client	Case No. Client Case #	Title	Country	Inventor Names	Application Number	Filing Date	Patent Number	Date Issued	Expiration Date	Status
Intelliasys/SEAforth (INT)	0057-011	Computer Processor Array with Dedicated Interconnections	US	Moore, Charles H.	10/801,942	03/16/2004				Pending
Intelliasys/SEAforth (INT)	0057-011-CN 5585052	Computer Processor Array	China	Moore, Charles H.	80008575.7	09/18/2006				Pending
Intelliasys/SEAforth (INT)	0057-011-EP 5585030	Computer Processor Array with Dedicated Interconnections	EPO	Moore, Charles H.	05723250.6	10/12/2006				Pending
Intelliasys/SEAforth (INT)	0057-011-JP 5585051	Computer Processor Array	Japan	Moore, Charles H.	2007-503910	09/14/2006				Pending
Intelliasys/SEAforth (INT)	0057-011-KR 5585050	Computer Processor Array	KOREA	Moore, Charles H.	7021454/2006	10/16/2006				Pending
Intelliasys/SEAforth (INT)	0057-011- PCT 0057-011- PCT	Computer Processor Array	PCT	Moore, Charles H.	PCT/US2005/0 05140	02/18/2005				Done
Intelliasys/SEAforth (INT)	0057-012P1	Method For Determining Information About The Internal Workings Of A Chip Based On Electro- Magnetic Emissions Therefrom	US	Huie, John	11/592,717	11/03/2006				Pending
Intelliasys/SEAforth (INT)	0057-012P1- TW 5596954	Method For Determining Information About The Internal Workings Of A Chip Based On Electro- Magnetic Emissions Therefrom	Taiwan	Huie, John	96112860	04/12/2007				Pending
Intelliasys/SEAforth (INT)	0057-012V	Method For Determining Information About The Internal Workings Of A Chip Based On Electro- Magnetic Emissions Therefrom	US	Huie, John	60/733,397	11/04/2005				Expired
Intelliasys/SEAforth (INT)	0057-012V- CN 5596950	Method For Determining Information About The Internal Workings Of A Chip Based On Electro- Magnetic Emissions Therefrom	China	Huie, John	80009135.8	09/21/2007				Pending
Intelliasys/SEAforth (INT)	0057-012V- EP 5596930	Method For Determining Information About The Internal Workings Of A Chip Based On Electro- Magnetic Emissions Therefrom	EPO	Huie, John	06827511.4	06/04/2007				Pending

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Client	Case No. Client Case #	Title	Country	Inventor Names	Application Number	Filing Date	Patent Number	Date Issued	Expiration Date	Status
Inteliasys/SEAForth (INT)	0057-012V-IN 5596951	Method For Determining Information About The Internal Workings Of A Chip Based On Electro-Magnetic Emissions Therefrom	India	Huie, John	7188/DELNP/2 007	09/18/2007				Pending
Inteliasys/SEAForth (INT)	0057-012V-JP 5596952	Method For Determining Information About The Internal Workings Of A Chip Based On Electro-Magnetic Emissions Therefrom	Japan	Huie, John	Not Yet Assigned	09/21/2007				Pending
Inteliasys/SEAForth (INT)	0057-012V-KR 5596953	Method For Determining Information About The Internal Workings Of A Chip Based On Electro-Magnetic Emissions Therefrom	Korea	Huie, John	7021645/07	09/20/2007				Pending
Inteliasys/SEAForth (INT)	0057-012V-PCT 5596999	Method For Determining Information About The Internal Workings Of A Chip Based On Electro-Magnetic Emissions Therefrom	PCT	Huie, John	PCT/US2006/0 43101	11/03/2006				Pending
Inteliasys/SEAForth (INT)	0057-013-CN 5592850	Asynchronous Computer Communication	China	Moore, Charles H.	200780000017. 5	04/30/2007				Pending
Inteliasys/SEAForth (INT)	0057-013-EP 5592830	Asynchronous Computer Communication	EPO	Moore, Charles H.	07250648.8	02/15/2007				Pending
Inteliasys/SEAForth (INT)	0057-013-IN 5592851	Asynchronous Computer Communication	India	Moore, Charles H.	331/DEL2007	02/19/2007				Pending
Inteliasys/SEAForth (INT)	0057-013-JP 5592852	Asynchronous Computer Communication	Japan	Moore, Charles H.	Not Yet Assigned	04/26/2007				Pending
Inteliasys/SEAForth (INT)	0057-013-KR 5592853	Asynchronous Computer Communication	Korea	Moore, Charles H.	7009920/07	04/30/2007				Pending
Inteliasys/SEAForth (INT)	0057-013-PCT 0057-013PCT	Asynchronous Computer Communication	PCT	Moore, Charles H.	PCT/US2007/0 04031	02/16/2007				Pending
Inteliasys/SEAForth (INT)	0057-013-TW 5592854	Asynchronous Computer Communication	Taiwan	Moore, Charles H.	96105684	02/15/2007				Pending
Inteliasys/SEAForth (INT)	0057-014 5592950	Asynchronous Power Saving Computer	US	Moore, Charles H.	11/355,513	02/16/2006				Pending
Inteliasys/SEAForth (INT)	0057-014-CN 5592950	Asynchronous Power Saving Computer	China	Moore, Charles H.	80000014.1	04/30/2007				Pending

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Client	Case No. Client Case #	Title	Country	Inventor Names	Application Number	Filing Date	Patent Number	Date Issued	Expiration Date	Status
IntelAsys/SEAForth (INT)	0057-014-EP 5592930	Asynchronous Power Saving Computer	EPO	Moore, Charles H.	07250645.4	02/15/2007				Pending
IntelAsys/SEAForth (INT)	0057-014-IN 5592951	Asynchronous Power Saving Computer	India	Moore, Charles H.	332/DEL2007	02/19/2007				Pending
IntelAsys/SEAForth (INT)	0057-014-JP 5592952	Asynchronous Power Saving Computer	Japan	Moore, Charles H.	Not Yet Assigned	04/26/2007				Pending
IntelAsys/SEAForth (INT)	0057-014-KR 5592953	Asynchronous Power Saving Computer	Korea	Moore, Charles H.	7009921/07	04/30/2007				Pending
IntelAsys/SEAForth (INT)	0057-014P1 Source	Execution Of Instructions Directly From Input	US	Moore, Charles H.	11/441.78-4	05/26/2006				Pending
IntelAsys/SEAForth (INT)	0057-014P1- CN 5597050	Execution Of Instructions Directly From Input	China	Moore, Charles H.	80000013.7	04/30/2007				Pending
IntelAsys/SEAForth (INT)	0057-014P1- EP 5597030	Execution Of Instructions Directly From Input	EPO	Moore, Charles H.	07250649.6	02/15/2007				Pending
IntelAsys/SEAForth (INT)	0057-014P1- IN 5597051	Execution Of Instructions Directly From Input	India	Moore, Charles H.	326/DEL2007	02/19/2007				Pending
IntelAsys/SEAForth (INT)	0057-014P1- JP 5597052	Execution Of Instructions Directly From Input	Japan	Moore, Charles H.	Not Yet Assigned	04/27/2007				Pending
IntelAsys/SEAForth (INT)	0057-014P1- KR 5597053	Execution Of Instructions Directly From Input	Korea	Moore, Charles H.	7009924/07	04/30/2007				Pending
IntelAsys/SEAForth (INT)	0057-014P1- PCT 0057-014P1- Source	Execution Of Instructions Directly From Input	PCT	Moore, Charles H.	PCT/US2007/0 04030	02/16/2007				Pending
IntelAsys/SEAForth (INT)	0057-014P1- TW 5597054	Execution Of Instructions Directly From Input	Taiwan	Moore, Charles H.	96106394	02/16/2007				Pending
IntelAsys/SEAForth (INT)	0057-014P2 Source	Processor and Method for Executing a Program Loop Within an Instruction Word	US	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	11/441.812	05/26/2006				Pending
IntelAsys/SEAForth (INT)	0057-014P2- CN 5597150	Microloop Computer Instructions	China	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	200780000015. 6	04/30/2007				Pending
IntelAsys/SEAForth (INT)	0057-014P2- EP 5597130	Microloop Computer Instructions	EPO	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	07250614.0	02/15/2007				Pending
IntelAsys/SEAForth (INT)	0057-014P2- IN 5597151	Microloop Computer Instructions	India	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	327/DEL2007	02/19/2007				Pending

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Client	Case No. Client Case #	Title	Country	Inventor Names	Application Number	Filing Date	Patent Number	Date Issued	Expiration Date	Status
IntellaSys/SEAforth (INT)	0057-014P2- JP 5597152	Microloop Computer Instructions	Japan	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	Not Yet Assigned	04/27/2007				Pending
IntellaSys/SEAforth (INT)	0057-014P2- KR 5597153	Microloop Computer Instructions	Korea	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	7009923/07	04/30/2007				Pending
IntellaSys/SEAforth (INT)	0057-014P2- PCT 0057-014P2- PCT	Microloop Computer Instructions	PCT	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	PCT/US2007/0 04029	02/16/2007				Pending
IntellaSys/SEAforth (INT)	0057-014P2- TW 5597154	Microloop Computer Instructions	Taiwan	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	96106397	02/16/2007				Pending
IntellaSys/SEAforth (INT)	0057-014P3	Method and Apparatus for Monitoring Inputs to a Computer Array	US	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	11/441,818	05/26/2006				Pending
IntellaSys/SEAforth (INT)	0057-014P3- CN 5597250	Method And Apparatus For Monitoring Inputs To A Computer	China	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	80000016.0	04/30/2007				Pending
IntellaSys/SEAforth (INT)	0057-014P3- EP 5597230	Method And Apparatus For Monitoring Inputs To A Computer	EPO	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	07250646.2	02/15/2007				Pending
IntellaSys/SEAforth (INT)	0057-014P3- IN 5597251	Method And Apparatus For Monitoring Inputs To A Computer	India	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	328/DEL2007	02/19/2007				Pending
IntellaSys/SEAforth (INT)	0057-014P3- JP 5597252	Method And Apparatus For Monitoring Inputs To A Computer	Japan	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	Not Yet Assigned	04/27/2007				Pending
IntellaSys/SEAforth (INT)	0057-014P3- KR 5597253	Method And Apparatus For Monitoring Inputs To A Computer	Korea	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	7009925/07	04/30/2007				Pending
IntellaSys/SEAforth (INT)	0057-014P3- PCT	Method And Apparatus For Monitoring Inputs To A Computer	PCT	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	PCT/US2007/0 04083	02/16/2007				Pending
IntellaSys/SEAforth (INT)	0057-014P3- TW 5597254	Method And Apparatus For Monitoring Inputs To A Computer	Taiwan	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	96106396	02/16/2007				Pending
IntellaSys/SEAforth (INT)	0057-014- PCT 0057-014- PCT	Asynchronous Power Saving Computer	PCT	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	PCT/US2007/0 04080	02/16/2007				Pending
IntellaSys/SEAforth (INT)	0057-014-TW 5592954	Asynchronous Power Saving Computer	Taiwan	Moore, Charles H.	96105687	02/15/2007				Pending

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Client	Case No. Client Case #	Title	Country	Inventor Names	Application Number	Filing Date	Patent Number	Date Issued	Expiration Date	Status
Inteliasys/SEAForth (INT)	0057-015P1	Method and Apparatus for Operating A Computer Processor	US	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	11/731,747	03/30/2007				Pending
Inteliasys/SEAForth (INT)	0057-015P1- CN 5597350	Method and Apparatus for Operating a Computer Processor Array	China	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	2007- 10090753.6	04/02/2007				Pending
Inteliasys/SEAForth (INT)	0057-015P1- EP 5597330	Method and Apparatus for Operating a Computer Processor Array	EPO	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	07251416.9	03/30/2007				Pending
Inteliasys/SEAForth (INT)	0057-015P1- IN 5597351	Method and Apparatus for Operating a Computer Processor Array	India	Moore, Charles H.	717/DEL/2007	03/30/2007				Pending
Inteliasys/SEAForth (INT)	0057-015P1- JP 5597352	Method and Apparatus for Operating a Computer Processor Array	Japan	Moore, Charles H.	2007-086857	03/29/2007				Pending
Inteliasys/SEAForth (INT)	0057-015P1- KR 5597353	Method and Apparatus for Operating a Computer Processor Array	Korea	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	2007-0032588	04/02/2007				Pending
Inteliasys/SEAForth (INT)	0057-015P1- PCT 0057-015P1- PCT	Method And Apparatus For Operating A Computer Processor Array	PCT	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	PCT/US2007/0 08235	03/30/2007				Pending
Inteliasys/SEAForth (INT)	0057-015P1- TW 5597354	Method and Apparatus for Operating a Computer Processor Array	Taiwan	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	96110969	03/29/2007				Pending
Inteliasys/SEAForth (INT)	0057-015V	Allocation Of Resources Among An Array Of Computers	US	Moore, Charles H.	60/788,265	03/31/2006				Expired
Inteliasys/SEAForth (INT)	0057-015V- PCT	Allocation Of Resources Among An Array Of Computers	PCT	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	PCT/US2007/0 04081	02/16/2007				Pending
Inteliasys/SEAForth (INT)	0057-016V	Scalable Embedded Array Platform	US	Moore, Charles H. Fox, Jeffrey Arthur	60/797,345	05/03/2006				Expired
Inteliasys/SEAForth (INT)	0057-017P1	Circular Register Arrays Of A Computer	US	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	11/503,372	08/11/2006				Pending
Inteliasys/SEAForth (INT)	0057-017P1- EP 5597630	Circular Register Arrays Of A Computer	EPO	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	07250644.7	02/15/2007				Pending

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Client	Case No. Client Case #	Title	Country	Inventor Names	Application Number	Filing Date	Patent Number	Date Issued	Expiration Date	Status
IntelAsys/SEAforth (INT)	0057-017P1- IN 5597651	Circular Register Arrays Of A Computer	India	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	330/DEL2007	02/19/2007				Pending
IntelAsys/SEAforth (INT)	0057-017P1- PCT 0057-017P1- PCT	Circular Register Arrays Of A Computer	PCT	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	PCT/US2007/0 12539	05/25/2007				Pending
IntelAsys/SEAforth (INT)	0057-017P1- TW 5597654	Circular Register Arrays Of A Computer	Taiwan	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	96105677	02/15/2007				Pending
IntelAsys/SEAforth (INT)	0057-017V	Allocation Of Resources Among An Array Of Computers	US	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	60/818,084	06/30/2006				Expired
IntelAsys/SEAforth (INT)	0057-026P1	Computer System With Increased Operating Efficiency	US	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	11/653,187	01/12/2007				Pending
IntelAsys/SEAforth (INT)	0057-026P1- CN 5600450	Computer System With Increased Operating Efficiency	China	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	200780000018. X	04/30/2007				Pending
IntelAsys/SEAforth (INT)	0057-026P1- EP 5600430	Computer System With Increased Operating Efficiency	EPO	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	07250647.0	02/15/2007				Pending
IntelAsys/SEAforth (INT)	0057-026P1- IN 5600451	Computer System With Increased Operating Efficiency	India	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	329/DEL2007	02/19/2007				Pending
IntelAsys/SEAforth (INT)	0057-026P1- JP 5600452	Computer System With Increased Operating Efficiency	Japan	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	Not Yet Assigned	04/27/2007				Pending
IntelAsys/SEAforth (INT)	0057-026P1- KR 5600453	Computer System With Increased Operating Efficiency	Korea	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	7009922/07	04/30/2007				Pending
IntelAsys/SEAforth (INT)	0057- 026P1PCT 0057-026P1- PCT	Computer System With Increased Operating Efficiency	PCT	Moore, Charles H.	PCT/US2007/0 04082	02/16/2007				Pending
IntelAsys/SEAforth (INT)	0057-026P1- TW 5600454	Computer System With Increased Operating Efficiency	Taiwan	Moore, Charles H. Fox, Jeffrey Arthur Rible, John W.	96105683	02/15/2007				Pending
IntelAsys/SEAforth (INT)	0057-026V	SEAforth Applications Guide	US	Rible, John W. Fox, Jeffrey Arthur Montvelitsky, Michael B. Elliot, Gibson D.	60/849,498	09/29/2006				Expired
IntelAsys/SEAforth (INT)	0057-027P1	Inversion of Alternate Instructions And/Or Data Bits In A Computer	US	Moore, Charles H.	12/005,156	12/21/2007				Pending

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Client	Case No. Client Case #	Title	Country	Inventor Names	Application Number	Filing Date	Patent Number	Date Issued	Expiration Date	Status
IntellaSys/SEAForth (INT)	0057-027V	Embedded Array Processor And Applications	US	Moore, Charles H.	60/876,379	12/21/2006				Expired
IntellaSys/SEAForth (INT)	0057-027V- PCT	Inversion Of Alternate Instruction And/Or Data Bits In A Computer	PCT	Moore, Charles H.	PCT/US2007/0 26172	12/21/2007				Pending
IntellaSys/SEAForth (INT)	0057-028	CMOS SRAM/ROM Unified Bit Cell	US	Miller, Dennis Ray Rahman, Md Hafijur Kabir, Mohammad Ehsanul	11/652,726	01/12/2007				Pending
IntellaSys/SEAForth (INT)	0057-028- PCT	CMOS SRAM/ROM Unified Bit Cell	PCT	Miller, Dennis Ray Rahman, Md Hafijur Kabir, Mohammad Ehsanul	PCT/US2008/0 00260	01/08/2008				Pending
IntellaSys/SEAForth (INT)	0057-028- PCT	CMOS SRAM/ROM Unified Bit Cell	Taiwan	Miller, Dennis Ray Rahman, Md Hafijur Kabir, Mohammad Ehsanul	961,50267	12/26/2007				Pending
IntellaSys/SEAForth (INT)	0057-029	System and Method for Multi-Port Read and Write Operations	US	Rible, John W.	11/741,649	04/27/2007				Pending
IntellaSys/SEAForth (INT)	0057-029P1	System And Method For Processing Data In A Pipeline Of Computers	US	Montvelitsky, Michael B. Rible, John W.	11/741,659	04/27/2007				Pending
IntellaSys/SEAForth (INT)	0057-030V	Triple-Play Chip Solutions For Next- Generation Consumer Electronics	US	Moore, Charles H.	60/878,296	01/03/2007				Expired
IntellaSys/SEAForth (INT)	0057-031 07-0012	System and Method for Converting Analog Values Into Digital Form	US	Swain, Al L.	11/890,539	08/06/2007				Pending
IntellaSys/SEAForth (INT)	0057-032	Multi-Speed Ring Oscillator	US	Goff, Lonnie	11/788,035	04/18/2007				Pending
IntellaSys/SEAForth (INT)	0057-032-EP 5617530	Multi-Speed Ring Oscillator	EPO	Goff, Lonnie		04/09/2008				Pending
IntellaSys/SEAForth (INT)	0057-033	Analog-To-Digital Converter System With Increased Sampling Frequency	US	Moore, Charles H. Huie, John Snively, Les	11/726,739	03/22/2007				Pending
IntellaSys/SEAForth (INT)	0057-033-CN 5617650	Analog-To-Digital Converter System With Increased Sampling Frequency	China	Moore, Charles H. Huie, John Snively, Les	2008- 10087166.6	03/21/2008				Pending

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Client	Case No. Client Case #	Title	Country	Inventor Names	Application Number	Filing Date	Patent Number	Date Issued	Expiration Date	Status
IntelliaSys/SEAforth (INT)	0057-033-EP 5617630	Analog-To-Digital Converter System With Increased Sampling Frequency	EPO	Moore, Charles H. Huie, John Snively, Les		03/14/2008				Pending
IntelliaSys/SEAforth (INT)	0057-033-IN 5617651	Analog-To-Digital Converter System With Increased Sampling Frequency	India	Moore, Charles H. Huie, John Snively, Les	624/DEL2008	03/12/2008				Pending
IntelliaSys/SEAforth (INT)	0057-033-JP 5617652	Analog-To-Digital Converter System With Increased Sampling Frequency	Japan	Moore, Charles H. Huie, John Snively, Les	JP-2008- 076423	03/24/2008				Pending
IntelliaSys/SEAforth (INT)	0057-033-KR 5617653	Analog-To-Digital Converter System With Increased Sampling Frequency	South Korea	Moore, Charles H. Huie, John Snively, Les	KR- 27111/2008	03/24/2008				Pending
IntelliaSys/SEAforth (INT)	0057-033PI 5617730	Variable Sized Aperture Window Of An Analog-To-Digital Converter	US	Moore, Charles H. Huie, John Snively, Les	11/800,708	05/07/2007				Pending
IntelliaSys/SEAforth (INT)	0057-033PI- EP 5617730	Variable Sized Aperture Window Of An Analog-To-Digital Converter	EPO	Moore, Charles H. Huie, John Snively, Les		03/18/2008				Pending
IntelliaSys/SEAforth (INT)	0057-033PI- JP 5617752	Variable Sized Aperture Window Of An Analog-To-Digital Converter	Japan	Moore, Charles H. Huie, John Snively, Les	JP-2008- 076424	03/24/2008				Pending
IntelliaSys/SEAforth (INT)	0057-033PI- KR 5617753	Variable Sized Aperture Window Of An Analog-To-Digital Converter	South Korea	Moore, Charles H. Huie, John Snively, Les	KR- 27112/2008	03/24/2008				Pending
IntelliaSys/SEAforth (INT)	0057-033PI- PCT 5617799	Variable Sized Aperture Window Of An Analog-To-Digital Converter	PCT	Moore, Charles H. Huie, John Snively, Les	NOT YET ASSIGNED	03/20/2008				Pending
IntelliaSys/SEAforth (INT)	0057-033- PCT 5617699	Analog-To-Digital Converter System With Increased Sampling Frequency	PCT	Moore, Charles H. Huie, John Snively, Les	NOT YET ASSIGNED	03/20/2008				Pending
IntelliaSys/SEAforth (INT)	0057-033-TW 5617654	Analog-To-Digital Converter System With Increased Sampling Frequency	Taiwan	Moore, Charles H. Huie, John Snively, Les	TW-97108474	03/11/2008				Pending

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Client	Case No. Client Case #	Title	Country	Inventor Names	Application Number	Filing Date	Patent Number	Date Issued	Expiration Date	Status
IntelSys/SEAForth (INT)	0057-034	System for Convolution Calculation with Multiple Computer Processors	US	Montvelishsky, Michael B.	11/854,215	09/12/2007				Pending
IntelSys/SEAForth (INT)	0057-034-EP 5617830	System for Convolution Calculation with Multiple Computer Processors	EPO	Montvelishsky, Michael B.		03/28/2008				Pending
IntelSys/SEAForth (INT)	0057-034-JP 5617852	System for Convolution Calculation with Multiple Computer Processors	Japan	Montvelishsky, Michael B.	JP-2008- 097204	04/03/2008				Pending
IntelSys/SEAForth (INT)	0057-034- PCT 0057-034- PCT	System for Convolution Calculation with Multiple Computer Processors	PCT	Montvelishsky, Michael B.		04/06/2008				Pending
IntelSys/SEAForth (INT)	0057-034V	System for Convolution Calculation with Multiple Computer Processors	US	Montvelishsky, Michael B.	60/910,626	04/06/2007				Converted
IntelSys/SEAForth (INT)	0057-035V	System for Convolution Calculation with Multiple Computer Processors	US	Montvelishsky, Michael B.	60/910,629	04/06/2007				Converted
IntelSys/SEAForth (INT)	0057-038	Method and Apparatus for Loading Data and Instructions Into a Computer	US	Montvelishsky, Michael B. Moore, Charles H. Fox, Jeffrey Arthur	11/800,714	05/07/2007				Pending
IntelSys/SEAForth (INT)	0057-039	Method and Apparatus for Connecting Multiple Multimode Processors	US	Goff, Lonnie	11/809,197	05/31/2007				Pending
IntelSys/SEAForth (INT)	0057-040V	Digital to Analog Converter	US	Montvelishsky, Michael B.	60/962,559	07/30/2007			07/30/2008	Pending
IntelSys/SEAForth (INT)	0057-042	Carry-Select Adder	US	Leeland, Steven	11/854,242	09/12/2007				Pending