

PATENT ASSIGNMENT

Electronic Version v1.1
 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
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NATURE OF CONVEYANCE:	ASSIGNMENT
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CONVEYING PARTY DATA

Name	Execution Date
SonicBlue Incorporated	11/14/2006

RECEIVING PARTY DATA

Name:	S3 Graphics Co., Ltd.
Street Address:	Charles Adams, Ritchie & Duckworth
Internal Address:	Zephyr House, Mary Street, P.O. Box 709
City:	Grand Cayman
State/Country:	BRITISH VIRGIN ISLANDS

PROPERTY NUMBERS Total: 8

Property Type	Number
Patent Number:	6760025
Patent Number:	6297847
Patent Number:	6476808
Patent Number:	6417862
Patent Number:	6251695
Patent Number:	6614442
Patent Number:	6172718
Patent Number:	6005412

CORRESPONDENCE DATA

Fax Number: (650)812-3444
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ATTORNEY DOCKET NUMBER:

VIA : S3 GRAPHICS

NAME OF SUBMITTER:

Colby B. Springer

Total Attachments: 10

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**ASSIGNMENT OF PATENTS AND PATENT APPLICATIONS AND
DISCLOSURES**

WHEREAS, SONICblue Incorporated (SONICblue Inc.), a corporation organized and existing under the laws of the State of Delaware, having changed its name from S3 Incorporated (S3 Inc.) to SONICblue Incorporated (“Assignor”), and having an office and place of business at 2841 Mission College Boulevard, Santa Clara, CA 95054, is the owner of the inventions, patents, patent applications and disclosures listed in Schedule 1 annexed hereto and made a part hereof; and

WHEREAS, S3 Graphics Co., Ltd., a corporation organized and existing under the laws of the Cayman Islands, having a registered office at Charles Adams, Ritchie & Duckworth, Zephyr House, Mary Street, P.O. Box 709, Grand Cayman, British West Indies (“Assignee”), is desirous of acquiring the entire right, title, and interest in and to the inventions, patents, patent applications and disclosures listed in Schedule 1 annexed hereto, in the United States of America, and in its colonies, territories, and dependencies, and also in all countries foreign to the United States of America.

NOW, THEREFORE, TO ALL WHOM IT MAY CONCERN:

Be it known that for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, the aforesaid Assignor, has sold, assigned, transferred and set over, and by these presents does hereby sell, assign, transfer and set over to said Assignee, the entire right, title and interest in and to said inventions, patents, patent applications and disclosures listed in Schedule 1, and any and all continuations, divisions and renewals of and substitutes for said applications, and in, to and under any and all Letters Patent which may be granted on or as a result thereof in the United States and any and all other countries, and any reissue or reissues or extension or extensions of said Letters Patent; and does hereby assign to and authorize said Assignee, to file applications for Letters Patent in all countries, the same to be held and enjoyed by said Assignee, its successors, assigns, nominees or legal representatives, to the full end of the term or terms for which said Letters Patent respectively may be granted, reissued or extended, as fully

and entirely as the same would have been held and enjoyed by Assignor had this assignment, sale and transfer not been made.

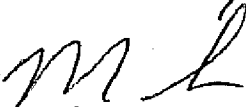
AND Assignor covenants to execute all additional instruments and to do all things necessary for carrying out the purpose of this instrument, at the expense of said Assignee and its successors.

AND Assignor hereby authorizes and requests the Commissioner of Patents and Trademarks of the United States and any official of any country or countries foreign to the United States whose duty it is to issue patents on applications as aforesaid, to issue to said Assignee, as assignee of the entire right, title and interest, any and all Letters Patent for said inventions, patents, patent applications, and disclosures listed in Schedule 1, including any and all Letters Patent of the United States which may be issued and granted on or as a result of the applications aforesaid, in accordance with the terms of this assignment.

This assignment is effective November 14, 2006.

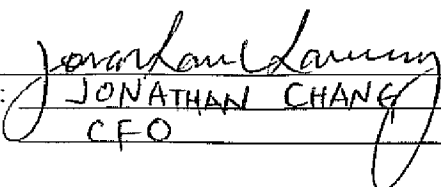
SONICblue Incorporated

Signed at San Jose, CA
This 15th day of January, 2007

By: 
Name: Marcus Smith
Title: CFO

S3 Graphics Co., Ltd.

Signed at Fremont, CA
This 2nd day of March, 2006 jc

By: 
Name: JONATHAN CHANG
Title: CFO

Schedule 1

Patent No.	IPC Class.	Pub. No.	Pub. Date	App. No.	App. Date	Inventor	Assignor	Attorney
CRO001	PA181US	1845	1922/49	07045266	1922/49	U.S. Patent No. 5,203,159		
CRO002	PA181US	1847	1985/071	07181139	1985/071	U.S. Patent No. 5,241,642		
CRO003	PA181US	1848	1985/071	07181139	1985/071	U.S. Patent No. 5,241,642		
CRO004	PA181US	1849	1985/071	07181139	1985/071	U.S. Patent No. 5,241,642		
CRO005	PA181US	1850	1985/071	07181139	1985/071	U.S. Patent No. 5,241,642		
CRO006	PA181US	1851	1985/071	07181139	1985/071	U.S. Patent No. 5,241,642		
CRO007	PA181US	1852	1985/071	07181139	1985/071	U.S. Patent No. 5,241,642		
CRO008	PA181US	1853	1985/071	07181139	1985/071	U.S. Patent No. 5,241,642		
CRO009	PA181US	1854	1985/071	07181139	1985/071	U.S. Patent No. 5,241,642		
CRO010	PA181US	1855	1985/071	07181139	1985/071	U.S. Patent No. 5,241,642		
CRO011	PA181US	1856	1985/071	07181139	1985/071	U.S. Patent No. 5,241,642		
CRO012	PA181US	1857	1985/071	07181139	1985/071	U.S. Patent No. 5,241,642		
CRO013	PA181US	1858	1985/071	07181139	1985/071	U.S. Patent No. 5,241,642		
CRO014	PA181US	1859	1985/071	07181139	1985/071	U.S. Patent No. 5,241,642		
CRO015	PA181US	1860	1985/071	07181139	1985/071	U.S. Patent No. 5,241,642		
CRO016	PA181US	1861	1985/071	07181139	1985/071	U.S. Patent No. 5,241,642		
CRO017	PA181US	1862	1985/071	07181139	1985/071	U.S. Patent No. 5,241,642		
CRO018	PA181US	1863	1985/071	07181139	1985/071	U.S. Patent No. 5,241,642		
CRO019	PA181US	1864	1985/071	07181139	1985/071	U.S. Patent No. 5,241,642		
CRO020	PA181US	1865	1985/071	07181139	1985/071	U.S. Patent No. 5,241,642		

Initial *By*

Schedule 1

530051 PCT	PA177EP	1877	EP	Double Buffered Graphics and Video Accelerator Having a Write Backing Memory	1990050	997277A.1		
530052	1770US	1770	US	System and Method of Doing the Same				
530053	1770PCT	1770	PCT	Communicating the Status of a Peripheral Device Controller to a Host Processor				
530054	7	7	US	Communicating the Status of a Peripheral Device Controller to a Host Processor	20010710	997208A.3		
530055 PCT	PA1720EP	1720	EP	Communicating the Status of a Peripheral Device Controller to a Host Processor				
530056	PA177AUS	177A	US	Device and Method for Blocking True Colors and Fog Colors to Generate Display Colors	19990603	00260107	U.S. Patent No. 6,417,802	U.S. Patent No. 6,417,802
530057	1770US	1770	US	Device and Method for Blocking True Colors and Fog Colors to Generate Display Colors				
530058	PA1740US	1740	US	System and Method for Mapping True Colors to a Subset of Computer-Generated Objects	20001117	09708737		
530059	PA1747US	1747	US	System and Method for Mapping True Colors to a Subset of Computer-Generated Objects	20001181	09708775	U.S. Patent No. 6,337,250	U.S. Patent No. 6,337,250
530060	PA1785US	1785	US	System and Method for Mapping True Colors to a Subset of Computer-Generated Objects	19970627	04184044	U.S. Patent No. 6,235,405	U.S. Patent No. 6,235,405
530061	7	7	US	Method of Determining LUT for Graphics Primitives				
530062	PA1820US	1820	US	Real Time DRAM Eliminating a Performance Penalty for Creating a Page Boundary	19980205	09010345	U.S. Patent No. 6,000,610	U.S. Patent No. 6,000,610
530063	1820PCT	1820	PCT	Real Time DRAM Eliminating a Performance Penalty for Creating a Page Boundary				
530064	PA1718US	1718	US	System and Method for Copy Protecting Computer Graphics	19980522	09060323	U.S. Patent No. 6,345,009	U.S. Patent No. 6,345,009
530065	PA1831US	1831	US	System and Method for Copy Protecting Computer Graphics	19980417	09061725	U.S. Patent No. 6,172,738	U.S. Patent No. 6,172,738
530066	PA1830US	1830	US	Adaptive Dynamic Aperture Correction	19980210	09021718	U.S. Patent No. 6,000,794	U.S. Patent No. 6,000,794
530067	PA1830US	1830	US	Full-Panel Display Controller with Improved Channeling and Frame Rate Control	19980527	09060504	U.S. Patent No. 6,041,419	U.S. Patent No. 6,041,419
530068	PA1830US	1830	US	Programmable Delay Timing Controller for High-Speed Data Interfaces	19980524	09061045		
530069	PA1830CA	1830	CA	Programmable Delay Timing Controller for High-Speed Data Interfaces				
530070	PA1800US	1800	US	AGP-DDR Interface for Full Swing and Reduced Swing (BS7L) Signals on an Integrated Circuit Chip	19980408	09057047	U.S. Patent No. 6,005,412	U.S. Patent No. 6,005,412
530071	PA1800R	1800	RR	Reduced Swing (BS7L) Signals on an Integrated Circuit Chip	19980210	09021019		
530072	PA1800P	1800	P	Reduced Swing (BS7L) Signals on an Integrated Circuit Chip				
530073	1800PCT	1800	PCT	Reduced Swing (BS7L) Signals on an Integrated Circuit Chip				
530074	PA1840US	1840	US	Reduced Swing (BS7L) Signals on an Integrated Circuit Chip	19980210	09021019		
530075	PA1840US	1840	US	Reduced Swing (BS7L) Signals on an Integrated Circuit Chip				
530076	PA1840P	1840	P	Reduced Swing (BS7L) Signals on an Integrated Circuit Chip				
530077	PA1840EP	1840	EP	Reduced Swing (BS7L) Signals on an Integrated Circuit Chip				
530078	PA1840CA	1840	CA	Reduced Swing (BS7L) Signals on an Integrated Circuit Chip				
530079 EP	PA1840UK	1840	UK	Non-Buffered Branching Texture Cache System and Method	19980400	09057028	U.S. Patent No. 6,011,505	U.S. Patent No. 6,011,505
530080 EP	PA1840DE	1840	DE	Non-Buffered Branching Texture Cache System and Method	19980524	09061045		
530081	PA1840CA	1840	CA	Non-Buffered Branching Texture Cache System and Method	19980524	09061045		
530082 EP	PA1840UK	1840	UK	Non-Buffered Branching Texture Cache System and Method	19980524	09061045		
530083 EP	PA1840DE	1840	DE	Non-Buffered Branching Texture Cache System and Method	19980704	09101111.5	UK Patent No. 0993707	UK Patent No. 0993707
530084	PA1840US	1840	US	Non-Buffered Branching Texture Cache System and Method	19980704	09101111.5	DE Patent No. 09918022	DE Patent No. 09918022
530085	PA1840US	1840	US	Non-Buffered Branching Texture Cache System and Method	19980514	09070073	U.S. Patent No. 6,254,195	U.S. Patent No. 6,254,195
530086	PA1840US	1840	US	System and Method for Performing Branching with a Graphics Unit Having an Oversampling Buffer	19980514	09070073		
530087	PA1840US	1840	US	System and Method for Performing Branching with an Oversampling Buffer	19980515	09060323	U.S. Patent No. 6,144,265	U.S. Patent No. 6,144,265

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