

PATENT ASSIGNMENT

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SUBMISSION TYPE:	NEW ASSIGNMENT								
NATURE OF CONVEYANCE:	ASSIGNMENT								
CONVEYING PARTY DATA									
<table border="1" style="width:100%; border-collapse: collapse;"> <thead> <tr> <th style="width:70%;">Name</th> <th>Execution Date</th> </tr> </thead> <tbody> <tr> <td>Hiroshi MAKI</td> <td>04/28/2008</td> </tr> <tr> <td>Tsuyoshi YOKOMORI</td> <td>04/28/2008</td> </tr> <tr> <td>Tatsuyuki OKUBO</td> <td>04/28/2008</td> </tr> </tbody> </table>		Name	Execution Date	Hiroshi MAKI	04/28/2008	Tsuyoshi YOKOMORI	04/28/2008	Tatsuyuki OKUBO	04/28/2008
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Hiroshi MAKI	04/28/2008								
Tsuyoshi YOKOMORI	04/28/2008								
Tatsuyuki OKUBO	04/28/2008								
RECEIVING PARTY DATA									
Name:	Renesas Technology Corp.								
Street Address:	6-2, Otemachi 2-chome, Chiyoda-ku								
City:	Tokyo								
State/Country:	JAPAN								
PROPERTY NUMBERS Total: 1									
<table border="1" style="width:100%; border-collapse: collapse;"> <thead> <tr> <th style="width:30%;">Property Type</th> <th>Number</th> </tr> </thead> <tbody> <tr> <td>Application Number:</td> <td>12137522</td> </tr> </tbody> </table>		Property Type	Number	Application Number:	12137522				
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Application Number:	12137522								
CORRESPONDENCE DATA									
Fax Number:	(703)610-8686								
<i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i>									
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NAME OF SUBMITTER:	Mitchell W. Shapiro								
Total Attachments: 1 source=assignment#page1.tif									

CH \$40.00 12137522

ASSIGNMENT

(譲渡証)

As a below named inventor, I hereby declare that:

IN CONSIDERATION of the sum of One Dollar (\$1.00) or the equivalent thereof, and other good and valuable consideration paid to me citizen of Japan by RENESAS TECHNOLOGY CORP., a corporation organized under the laws of Japan, located at 6-2, Otemachi 2-chome, Chiyoda-ku, Tokyo, Japan, receipt of which is hereby acknowledged I do hereby sell and assign to said RENESAS TECHNOLOGY CORP., its successors and assigns, all my right, title and interest, in and for the United States of America, in and to

MANUFACTURING METHOD FOR SEMICONDUCTOR INTEGRATED DEVICE

invented by me (if only one is named below) or us (if plural inventors are named below) and described in the application for United States Letters Patent therefor, executed on even date herewith, and all United States Letters Patent which may be granted therefor, and all divisions, continuations and extensions thereof, the said interest being the entire ownership of the said Letters Patent when granted, to be held and enjoyed by said RENESAS TECHNOLOGY CORP., its successors, assigns or other legal representatives, to the full end of term for which said Letters Patent may be granted, as fully and entirely as the same would have been held and enjoyed by me or us if this assignment and sale had not been made;

And I hereby agree to sign and execute any further documents or instruments which may be necessary, lawful, and proper in the prosecution of the above-named application or in the preparation and prosecution of any continuing, continuation-in-part, substitute, divisional, renewal, reviewed or reissue applications or in any amendment, extension, or interference proceedings, or otherwise to secure the title thereto in said assignee;

And I do hereby authorize and request the Commissioner of Patents to issue said Letters Patent to said RENESAS TECHNOLOGY CORP.,

Signed on the date(s) indicated aside signatures:

	INVENTOR(S) (発明者フルネームサイン)	Date Signed (署名日)
1)	<u>Hiroshi Maki</u> (Hiroshi MAKI)	<u>28 / April / 2008</u>
2)	<u>Tsuyoshi Yokomori</u> (Tsuyoshi YOKOMORI)	<u>28 / April / 2008</u>
3)	<u>Tatsuyuki Okubo</u> (Tatsuyuki OKUBO)	<u>28 / April / 2008</u>
4)	_____	_____
5)	_____	_____
6)	_____	_____
7)	_____	_____